I. Slew Rate

Slew Rate (SR) = \frac{dV_o}{dt}\bigg|_{\text{max}}

What is physical origin of slew rate and how is this different than bandwidth?

To answer this we need knowledge of internal transistor function and so we cannot directly address this question right now. Instead consider a simple RC circuit.

\[ V_o = V_{\text{in}} \frac{1}{j\omega C + R} \]

\[ \frac{V_o}{V_{\text{in}}} = \frac{1}{1 + j\omega CR} = \frac{1}{1 + \frac{j\omega}{RC}} \]

\[ 20 \log \left( \frac{V_o}{V_{\text{in}}} \right) \]

Bandwidth: \( w = (RC)^{-1} \)
In Time-domain, what is voltage across the capacitor \( V_o \)?

\[
i = C \frac{dV_o}{dt}
\]

\[
\frac{dV_o}{dt} = \frac{i}{C}
\]

1. **Slow Rate**

In a real circuit, the voltage source \( V_s \) will be limited in how much current it can supply, and this will limit the slow rate.

\[
SR = \frac{dV_o}{dt} \left|_{max} \right. = \frac{i_{max}}{C}
\]

For \( V_s = V_o \cos(\omega t) \)

\[
\frac{dV_s}{dt} = V_o \omega \cos(\omega t) \quad \text{max slope} = V_o \omega
\]

if

\[
\begin{aligned}
V_o \omega &> \frac{i_{max}}{C} \quad (SR \ \text{limited}) \\
V_o \omega &< \frac{i_{max}}{C} \quad (BW \ \text{limited})
\end{aligned}
\]

**Scenario #1**

\[
\begin{aligned}
W &< (RC)^{-1} \\
V_o \omega &< \frac{i_{max}}{C}
\end{aligned}
\]

**Scenario #2**

\[
\begin{aligned}
W &> (RC)^{-1} \\
V_o \omega &> \frac{i_{max}}{C}
\end{aligned}
\]

**Scenario #3**

\[
\begin{aligned}
W &< (RC)^{-1} \\
V_o \omega &< \frac{i_{max}}{C}
\end{aligned}
\]

**Scenario #3**

\[
\begin{aligned}
W &< (RC)^{-1} \\
V_o \omega &> \frac{i_{max}}{C}
\end{aligned}
\]

(Vo versus \( t \))

(Figures showing phase shift and oscillations)
II. Slew Rate example #1

**Question**

\[ SR = 40 \text{ v/\mu s} \]

What is highest frequency in which a 20 Volt peak-to-peak sinusoid can be produced at output?

**Answer**

\[ V_o = 10 \sin (2\pi f t) \]

\[ \left. \frac{dV_o}{dt} \right|_{\text{max}} = 10 (2\pi f) \]

\[ SR = 40 \text{ v/\mu s} = 10 (2\pi f_{\text{max}}) / 1 \times 10^6 \]

\[ f_{\text{max}} = \frac{40}{2 \pi} / 1 \times 10^6 \]

\[ f_{\text{max}} = 637 \text{ kHz} \]
The frequency at which an output sinusoid with amplitude equal to the rated output voltage $V_{o,\text{max}}$ begins to show distortion due to slow-rate limiting.

\[ V_{in} = V_{o,max} \sin \omega t \]

\[ SR = \frac{dV_{in}}{dt}_{\text{max}} = V_{o,max} \omega \cos \omega t \]

Max slope = $V_{o,max} \omega m$

Then,

\[ SR = V_{o,max} \omega m \]

\[ SR = V_{o,max} \omega \]

\[ f_m = \frac{SR}{2\pi V_{o,max}} \]

We need to buffer a 500kHz sinusoid with 10 $V_{p-p}$ amplitude which op-amp will work without distorting output signal.

<table>
<thead>
<tr>
<th>Op-Amp #1</th>
<th>Op-Amp #2</th>
<th>Op-Amp #3</th>
<th>Op-Amp #4</th>
</tr>
</thead>
<tbody>
<tr>
<td>$f_{id} = 250kHz$</td>
<td>$f_{id} = 600kHz$</td>
<td>$f_{id} = 600kHz$</td>
<td>$f_{id} = 10MHz$</td>
</tr>
<tr>
<td>$SR = 1000, \text{V/µs}$</td>
<td>$SR = 1, \text{V/µs}$</td>
<td>$SR = 1, \text{V/µs}$</td>
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</tr>
<tr>
<td>$V_{o,max} = 20, \text{V}$</td>
<td>$V_{o,max} = 10, \text{V}$</td>
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</tr>
</tbody>
</table>

No, $f_{id}$ too low!

\[ f_m = \frac{1 \times 10^6}{2\pi(10)} = 16\, \text{kHz} \]

No!

\[ 400kHz = \frac{SR}{2\pi(20)} \]

\[ 50\, \text{V/µs} \]

$W_{p-p} = 31\, \text{V/µs}$

Yes!

But probably expensive.

Op-Amp #3 a better choice.