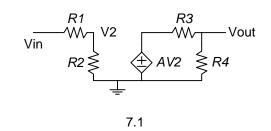
Voltage

EE42 - Problem Set #7 Solution

7.1.a Voltage divider formed by R1 and R2: $V2 = Vin \cdot \frac{R2}{R1 + R2}$

gain of VCVS:
$$A \cdot V2 = Vin \cdot \frac{A \cdot R2}{R1 + R2}$$



So,
$$\operatorname{Vout} = \operatorname{Vin} \cdot \frac{A \cdot R2}{R1 + R2} \cdot \frac{R4}{R3 + R4}$$
(1)

 $Vout = A \cdot V2 \cdot \frac{R4}{R3 + R4}$

7.1.b Let Vout = Vout_DC + Δ Vout, Vin = VIn_DC + Δ Vin in Eq. (1),

$$Vout_DC + \Delta Vout = (Vin_DC + \Delta Vin) \cdot \frac{A \cdot R2}{R1 + R2} \cdot \frac{R4}{R3 + R4}$$

Sort terms of DC and Δ , we get,

Voltage divider by R3 and R4:

$$Vout_DC = Vin_DC \cdot \frac{A \cdot R^2}{R^2} \cdot \frac{R^4}{R^3 + R^4}$$
$$\Delta Vout = \Delta Vin \cdot \frac{A \cdot R^2}{R^2 + R^2} \cdot \frac{R^4}{R^3 + R^4}$$
(2)

Derive the small-signal gain from (2):

Aac =
$$\frac{\Delta \text{Vout}}{\Delta \text{Vin}}$$
 = A $\cdot \frac{\text{R2}}{\text{R1} + \text{R2}} \cdot \frac{\text{R4}}{\text{R3} + \text{R4}}$

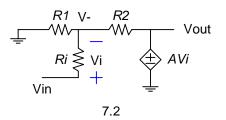
7.2.a KCL at node V-:
$$\frac{V_- - V_{out}}{R^2} + \frac{V_-}{R^1} = 0$$
 (3)

We also know: V_{out}

$$t = A \cdot V_i = A \cdot (V_{in} - V_{in})$$



Given $\frac{V_{-} - V_{out}}{R2} + \frac{V_{-}}{R1} = 0$ $V_{out} = A \cdot (V_{in} - V_{-})$ Find $(V_{out}, V_{-}) \rightarrow \begin{bmatrix} A \cdot V_{in} \cdot \frac{(R1 + R2)}{(R1 + R1 \cdot A + R2)} \\ R1 \cdot A \cdot \frac{V_{in}}{(R1 + R1 \cdot A + R2)} \end{bmatrix}$



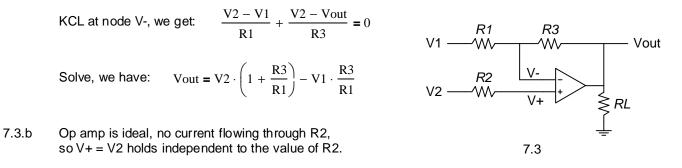
So,
$$V_{out} = V_{in} \cdot \frac{R1 + R2}{R1 + \frac{R1 + R2}{A}} = 10.999395$$
 (5)

7.2.b A is large, so the term contains A in the denominator is small compared to R1. Apply Taylor expansion to (5), we have:

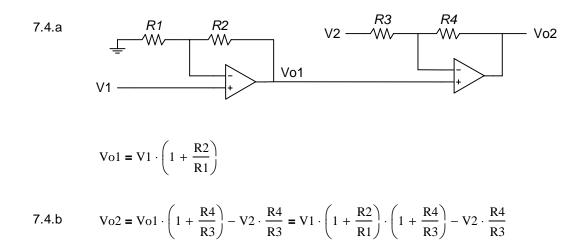
$$V_{out} = V_{in} \cdot \frac{R1 + R2}{R1 + \frac{R1 + R2}{A}} = V_{in} \cdot \left(\frac{R1 + R2}{R1}\right) \cdot \left(1 - \frac{R1 + R2}{A \cdot R1}\right)$$

The fraction of deviation from ideal gain is: $\frac{\Delta V_{out}}{V_{out}(ideal)} = -\frac{R1 + R2}{A \cdot R1} = \frac{-1}{A \cdot \frac{R1}{R1 + R2}} = -5.5 \cdot 10^{-5}$ (6)

- Note: The term in the denominator of (6) is called "loop gain", i.e., the gain derived when going around the feedback loop of the op amp, including the resistive network forming a divider.
- 7.3.a Assuming ideal op amp, 1) V+ = V-; 2) no current into + and terminals, so V- = V+ = V2 holds.



7.3.c VCVS is a voltage source, so RL at output in parallel to the VCVS will not affect the its voltage.



Note: 1) In (5), term contains A in the denominator is the error term due to finite gain of VCVS. 2) If $A = \infty$, then Vout reduces to the form assuming op amp is ideal.

EE42 - PS#7

7.5.a KCL at node V2:
$$\frac{Vin - V2}{R1} + I_B - \frac{V2}{R2} = 0$$

KCL at node Vout: $\frac{Vout}{R4} + \frac{Vout - Vcc}{R3} + gm \cdot V2 = 0$

Given

$$\frac{\text{Vin} - \text{V2}}{\text{R1}} + \text{I}_{\text{B}} - \frac{\text{V2}}{\text{R2}} = 0$$
$$\frac{\text{Vout}}{\text{Vout}} + \frac{\text{Vout} - \text{Vcc}}{\text{Vcc}} + \text{gm} \cdot \text{V2} = 0$$

$$\frac{1}{R4} + \frac{1}{R3} + \frac{1}{R3} + gm \cdot V2$$

$$\operatorname{Find}(\operatorname{Vout}, \operatorname{V2}) \rightarrow \begin{bmatrix} -\operatorname{R4} \cdot \frac{\left(-\operatorname{Vcc} \cdot \operatorname{R1} - \operatorname{Vcc} \cdot \operatorname{R2} + \operatorname{gm} \cdot \operatorname{R2} \cdot \operatorname{R3} \cdot \operatorname{Vin} + \operatorname{gm} \cdot \operatorname{R2} \cdot \operatorname{R3} \cdot \operatorname{R1} \cdot \operatorname{I_B}\right) \\ (\operatorname{R3} \cdot \operatorname{R1} + \operatorname{R3} \cdot \operatorname{R2} + \operatorname{R4} \cdot \operatorname{R1} + \operatorname{R4} \cdot \operatorname{R2}) \\ \operatorname{R2} \cdot \frac{\left(\operatorname{Vin} + \operatorname{R1} \cdot \operatorname{I_B}\right)}{(\operatorname{R1} + \operatorname{R2})} \end{bmatrix}$$

So,
$$\operatorname{Vout} = \frac{-\operatorname{gm} \cdot \operatorname{R2} \cdot \operatorname{R3} \cdot \operatorname{R4} \cdot \left(\operatorname{Vin} + \operatorname{I}_{\operatorname{B}} \cdot \operatorname{R1}\right)}{(\operatorname{R1} + \operatorname{R2}) \cdot (\operatorname{R3} + \operatorname{R4})} + \frac{\operatorname{Vcc} \cdot \operatorname{R4}}{\operatorname{R3} + \operatorname{R4}}$$
(7)

Note: If superposition is applied to the analysis, it is very easy to eyeball the solution in (7) consisting of 3 terms:

Vin term, due to the cascade of divider R1 & R2, VCCS gain gm, output resistance R3 || R4;
IB term, silimar to Vin, but need to go through a Thevenin equivalent transform at Vin;

3) Vcc term, simply a divider formed by R3 and R4.

KCL at node V2:
$$\frac{\text{Vin} - \text{V2}}{\text{R1}} - \frac{\text{V2}}{\text{R2}} = 0$$

KCL at node Vout:
$$\frac{Vout}{R4} + \frac{Vout}{R3} + gm \cdot V2 = 0$$

Solve, we have
$$V_{out} = \frac{-gm \cdot R2 \cdot R3 \cdot R4 \cdot Vin}{(R1 + R2) \cdot (R3 + R4)}$$
 (8)

7.5.c If we set IB = Vcc = 0 in (7), we see we get exactly (8). Yun Chiu