

EE 40 Class Schedule Spring 2008

	Monday	Wednesday	Friday	HW posted Fri	HW due Fri	HW Back on Thurs.	Lab	Subjects
Week 1	1/21	1/23	1/25	HW1			No Lab	Definition of current, voltage, KCL and KVL, Ohm's law; power and energy; resistors in parallel, series; dependent sources
	Holiday	Ch 1	Ch 1					
Week 2	1/28 Ch 1	1/30 Ch 2	2/1 Ch 2	HW2	HW1		1. Introduction	Node analysis, mesh analysis,
	2/4 Ch 2	2/6 Ch 2	2/8 Ch 3, 4					
Week 3	2/11 Ch 4	2/13 Ch 4	2/15 Ch 4	HW3	HW2	HW1	No Lab	superposition, equivalent circuits; capacitor and inductor; intro first order circuits;
	2/18 Holiday	2/20 Ch 4	2/22 Test 1					
Week 4	2/25 Ch 5	2/27 Ch 5	2/29 Ch 5	HW4	HW3	HW2	2. Equivalent Circuits	transient response first order ckts; forced response; natural response
	3/3 Ch 5	3/5 Ch 6+SR-1	3/7 Ch 6+SR-1					
Week 5	3/10 Ch 6+SR-1	3/12 Ch 6+SR-1	3/14 Ch 6+SR-1	HW5	HW4		3. RC Circuits	circuit analysis with complex impedance;
	3/17 Ch 14	3/19 Test 2	3/21 Ch 14					
Week 6	3/31 Ch 14	4/2 Ch 14	4/4 Ch 10+ SR-2	HW6	HW5	HW4	4. RLC Transients	dB, logarithmic algebra; Bode Plots; 1st order filters;
	4/7 Ch 10+SR-2	4/9 Ch 10+SR-2	4/11 Ch 10+SR-2					
Week 7	4/14 Ch 10+SR-2	4/16 SR-3	4/18 SR-3	HW7	HW6	HW5	5. RLC Filters	2nd order filter
	4/21 SR-3	4/23 Ch 12+SR-4&5	4/25 Ch 12+SR-4&5					
Week 8	4/28 Ch 12+SR-4&5	4/30 Ch 12+SR-4&5	5/2 Ch 7	HW8	HW7	HW6	No Lab	Op Amps (amp, inverting amp, summing, differential, integrating, A/D);
	5/5 Ch 12+SR-4&5	5/7 Test 3	5/9 Ch 7+Ch 12 +SR-4 &5					
Week 9	5/12 Class review			HW9	HW8	HW7	6. Op-Amps	Op Amp; Diode circuits (rectifier, clipper, clamping, peak detector, level shift)
Week 10				HW10	HW9	HW8	7. Diodes	Diode circuits (rectifier, clipper, clamping, peak detector, level shift)
Week 11				HW10	HW10	HW9	8. Project 1	Diode Ckts; Semiconductor Physics (bandgap, doping, Gauss' law and Poisson eq)
Week 12				HW10	HW10	HW9	9. Project 2	Semiconductor Physics.MOSFET; NMOS; PMOS;
Week 13				HW10	HW10	HW9	10. Project 3	MOSFET; Inverters; Q-point and loadline analysis; Binary numbers; Boolean Algebra;
Week 14				HW10	HW10	HW10	(optional)	Logic gates; MOSFET (small signal model)
Project Competition 5/12 6-8 p.m.								
Final Exam 12.30 -3.30 on 5/16								
SR stands for Supplementary Reader; SR-1 is SR chapter 1								
All tests include material covered in Lecture Notes								