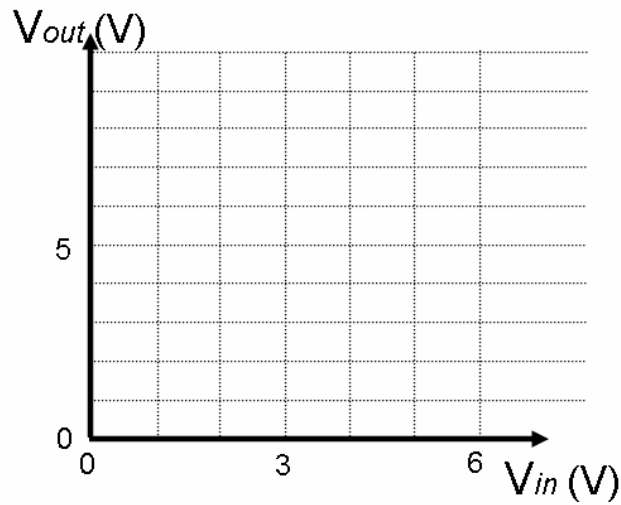


Name: \_\_\_\_\_  
Student ID: \_\_\_\_\_  
Name: \_\_\_\_\_  
Student ID: \_\_\_\_\_  
Section: \_\_\_\_\_  
Date: \_\_\_\_\_

**UNIVERSITY OF CALIFORNIA, BERKELEY**  
**EE40: Introduction to Microelectronic Circuits**  
**CS Amplifier Lab Report**

1. Adjust  $V_{in}$  from 0V to 5V, draw graph of  $V_{in}$ - $V_{out}$



2. Where is the optimal point (maximum slope) ?

$$V_{in,op} =$$

$$V_{in,op} =$$

Maximum slope:

3. If  $R_1=2.2k$ , find the right  $R_2$  to get the optimal bias for the amplifier

$$R_2 =$$

4. Find the gain of the amplifier for small signal

5. Draw the output waveform when the amplitude of the input signal is 3 volts. Explain why