

Lecture for EE 233

# Optical MEMS for Telecommunication Systems

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## OUTLINE

- Introduction
- Optical design considerations
- Space division switches
  - 2D MEMS optical switches
  - 3D MEMS optical switches
- Spectral domain processors
  - Wavelength-selective switches
- Planar lightwave circuits (PLC)-MEMS Integration
- Diffractive optical MEMS
- New directions
- Summary



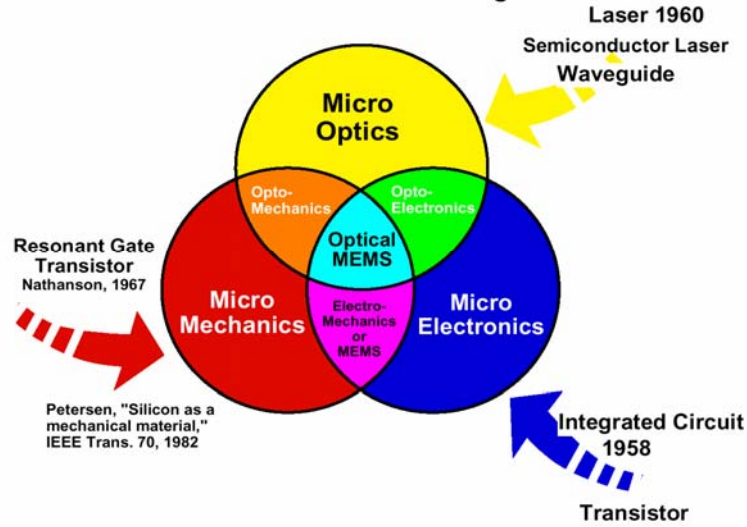
## Why Optical MEMS ?

- Optical MEMS offers
  - Low optical insertion loss
  - Low crosstalk
  - Transparency (wavelength, polarization, bit rate, data format)
  - Low power consumption
- Why ?
  - The effect of moving optical elements is stronger than electro-optic, thermal-optic effects
  - Very efficient beam steering devices
- What
  - Switches
  - Tunable devices (delay, dispersion, wavelength, bandwidth, dynamic gain equalization, etc)



# Optical MEMS

Fusion of Three Technologies

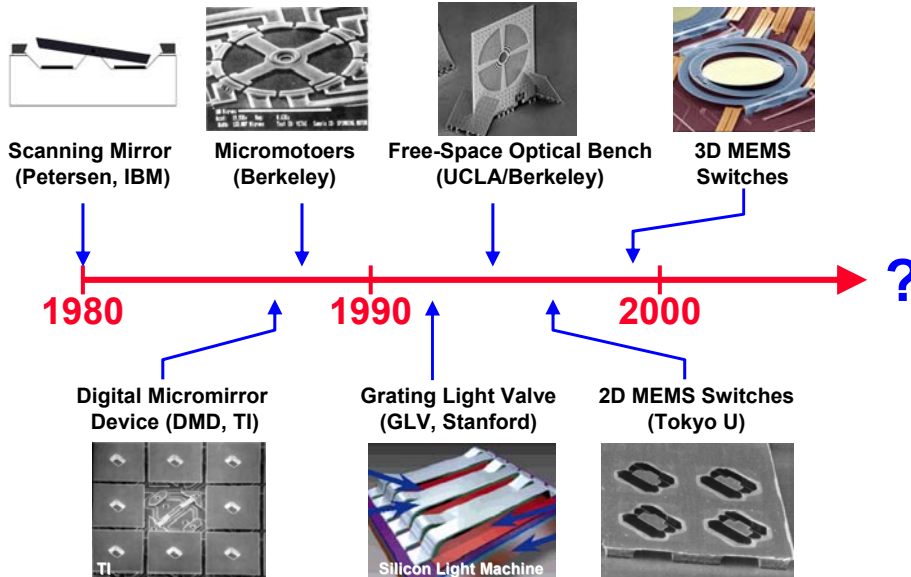


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# 25 Years of Optical MEMS

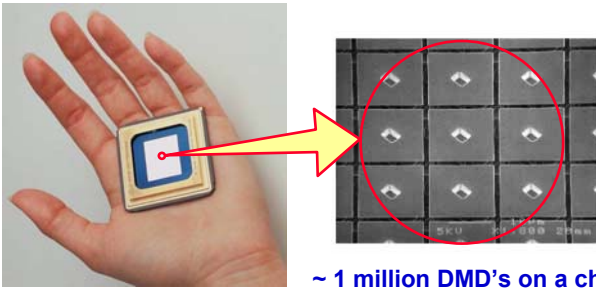
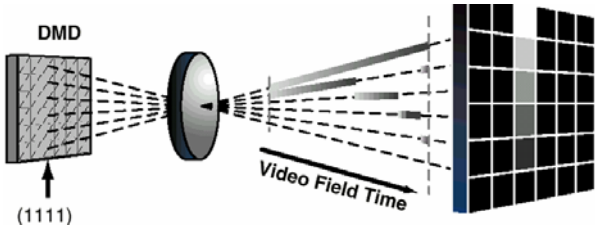


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# Digital Micromirror Devices (DMD)

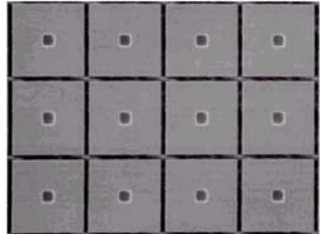
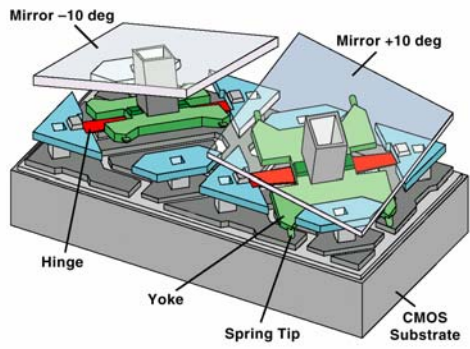


~ 1 million DMD's on a chip

<http://www.dlp.com/dlp/resources/dmmd.asp>



# Digital Micromirror Device (DMD) Texas Instruments

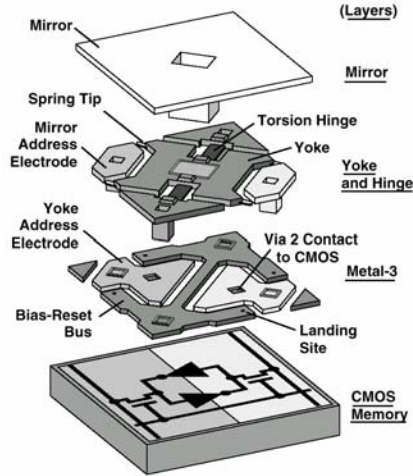


Top View of DMD

L. Hornbeck, Electronic Imaging, 1997

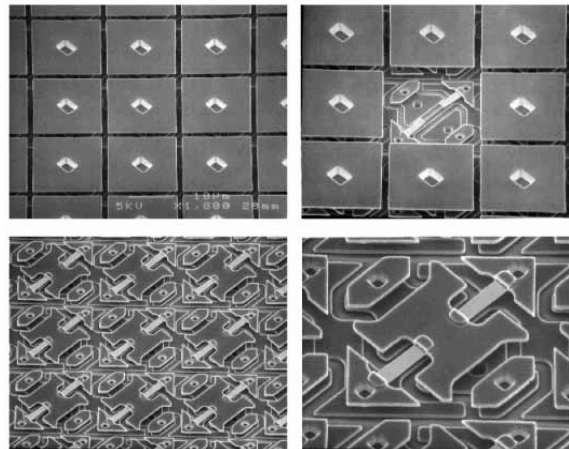


## Detailed Layer Structure of DMD



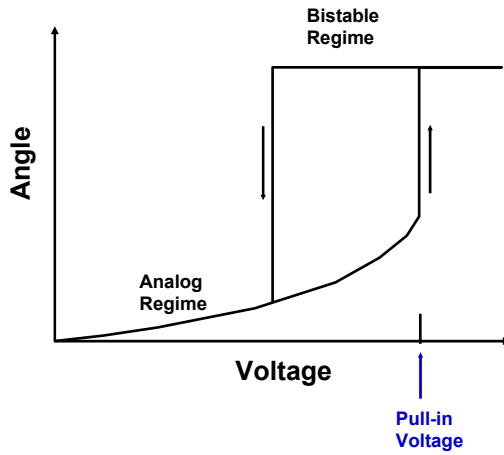
L. Hornbeck, Electronic Imaging, 1997

## TI's Digital Micromirror Devices (DMD)



(Texas Instruments, Digital Micromirror Device™)

## Transfer Characteristics of Torsion Mirrors

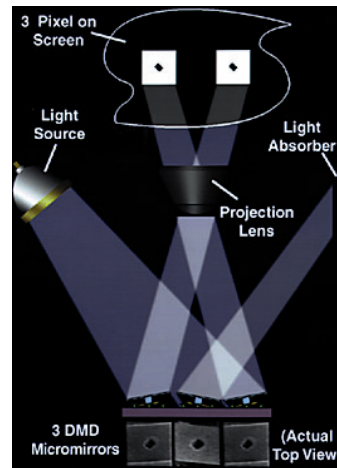
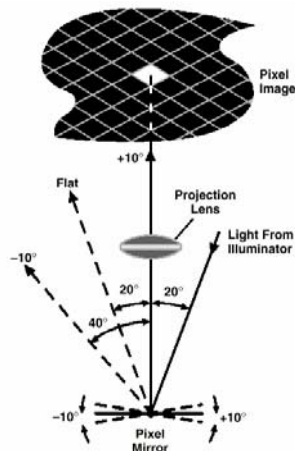


**Pull-in voltage  
(or Stiffness Voltage)**

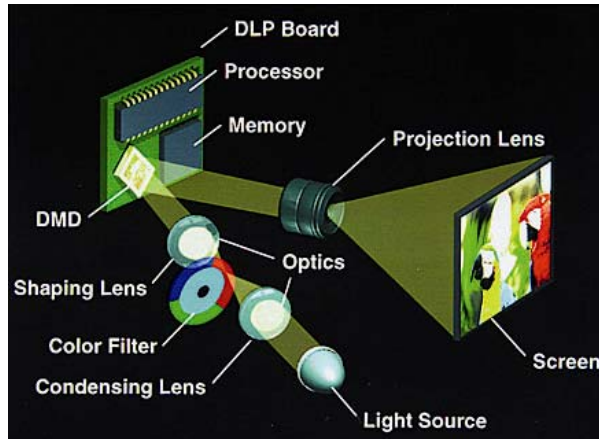
$$V_s = \sqrt{\frac{2 \cdot z_0^3}{\epsilon W L^3 C}}$$



## Principle of Projection System Using DMD



## Projection Display Using Digital Micromirror Display (DMD)



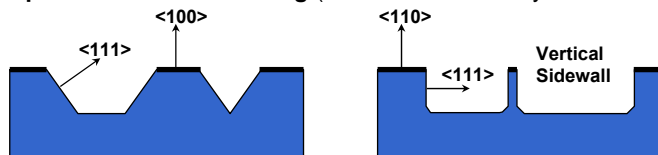
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## Bulk Micromachining

- **Anisotropic wet chemical etching** (restricted to fixed crystalline orientations)



- **Deep reactive ion etching (DRIE or ICP-RIE)**



- High aspect ratio ( $> 20:1$ )
- Independent of crystal orientation
- More efficient use of real estate of substrate (e.g., can produce closely spaced structures)

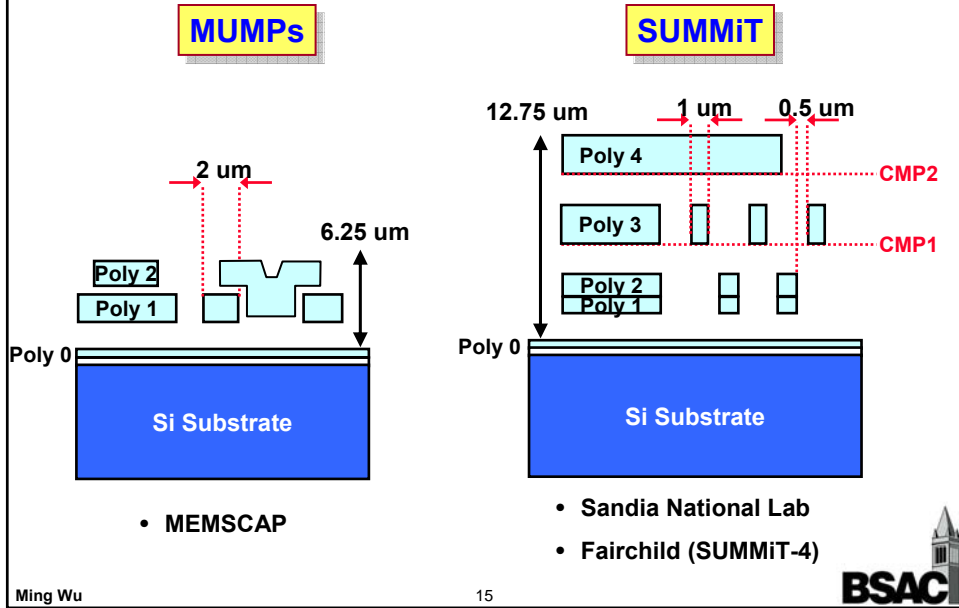
- Combine with silicon-on-insulator (SOI) or III-V epi wafer
- **Suspended structure in one-step etching + releasing**
- **Multi-layer structure by additional wafer bonding**

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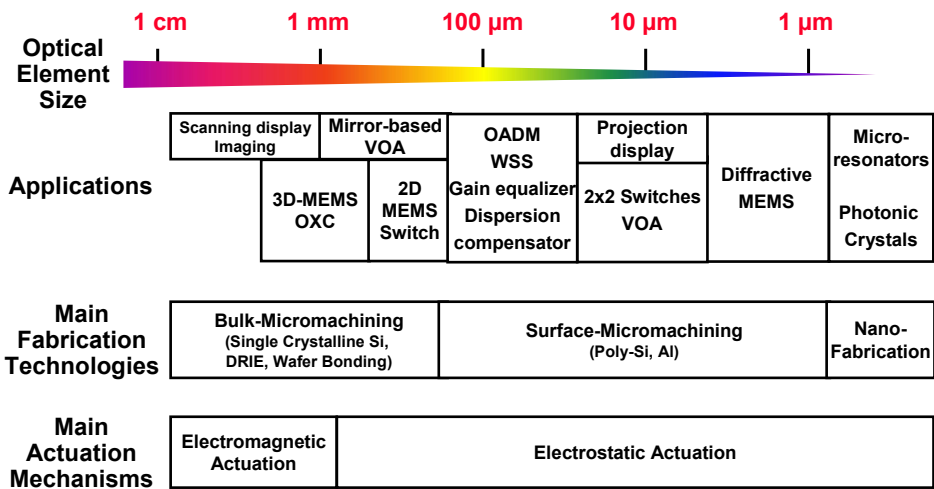
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## Surface-Micromachining: 2 “Standard” Foundry Process



## MEMS Technologies and Optical Element Size

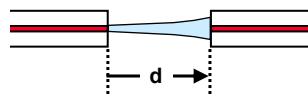




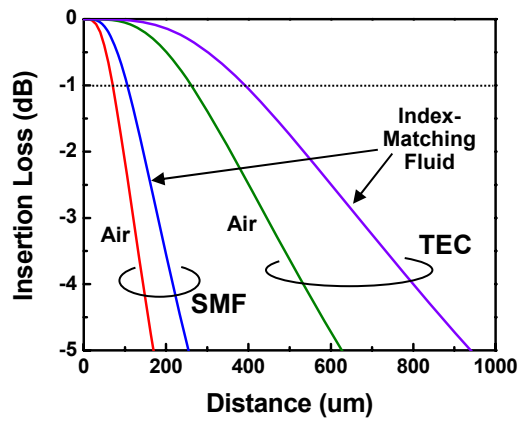
# Optical Designs

## Direct Coupling Without Lenses

Single Mode Fiber (SMF)

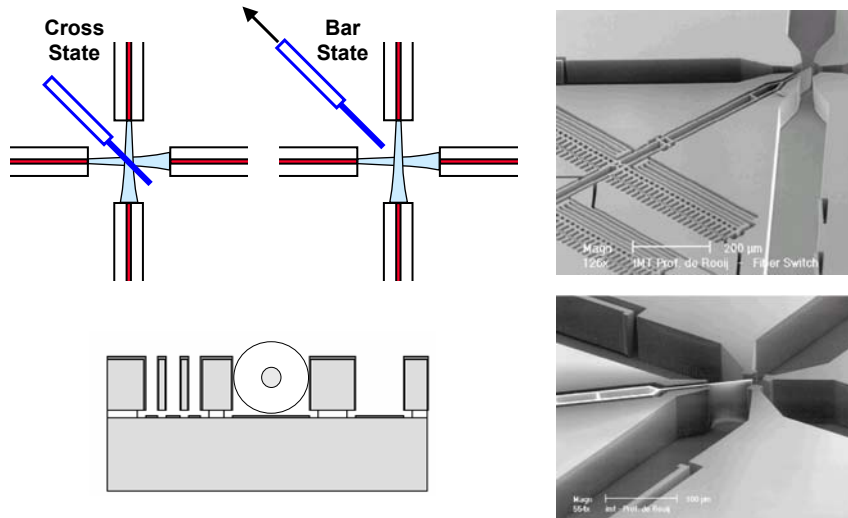


Thermally Expanded Core (TEC) Fiber



- Short propagation distance
- May be used for small switches or VOAs

## Example: 2x2 Switch



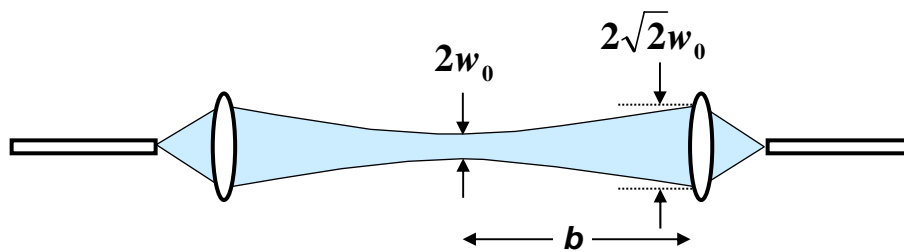
Marxer, et al., J-MEMS, vol.6, 1997. p.277-85.

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## Free-Space Optics: Gaussian Beam



$$w^2(z) = w_0^2 \left[ 1 + \left( \frac{z}{b} \right)^2 \right]$$

$$b = \frac{\pi w_0^2}{\lambda} \quad (\text{Confocal Parameter})$$

- Larger beam waist  $\rightarrow$  Long collimation length
- System size  $\sim 2b$
- Mirror diameter  $\sim 2aw_0$ ,  $a \sim 1.5$  to  $2$

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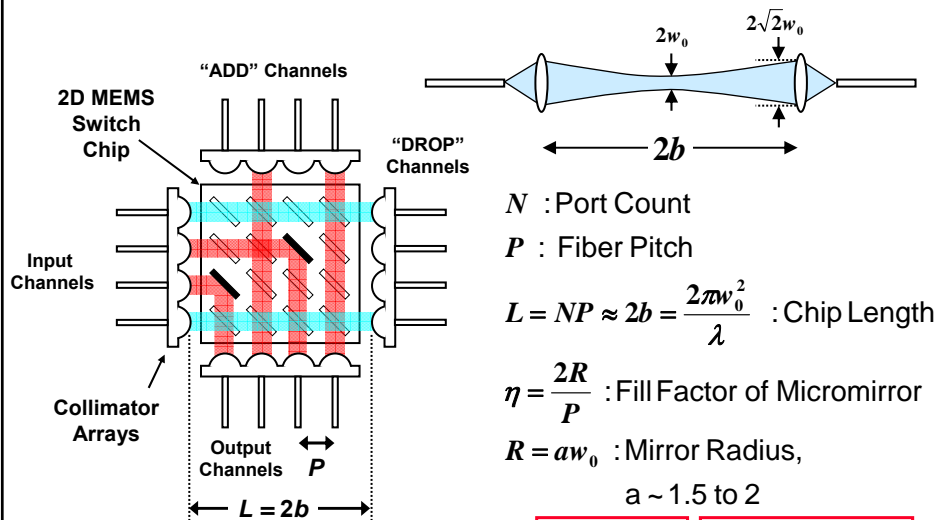


## Space Division Switches:

(1) 2D MEMS Optical Switches

(2) 3D MEMS Optical Switches

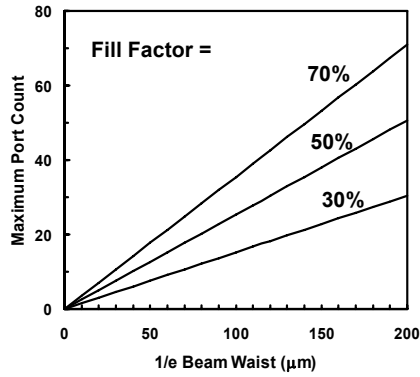
## Scaling of 2D MEMS Optical Switches



$$\Rightarrow N \approx \frac{\pi\eta}{a\lambda} w_0 \quad L = \left( \frac{2a^2\lambda}{\pi\eta^2} \right) \cdot N^2$$

## Port Count of 2D MEMS Switches

Port Count vs Beam Size



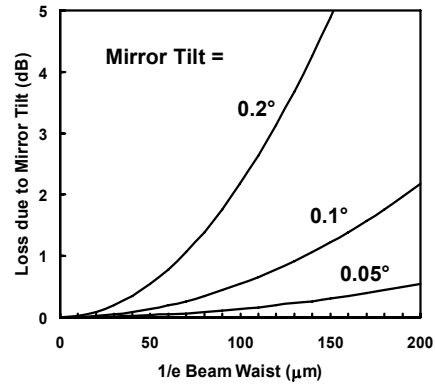
Port Count:  $N \approx \frac{\pi\eta}{a\lambda} w_0$

Chip Size:  $L = \left(\frac{2a^2\lambda}{\pi\eta^2}\right) \cdot N^2$

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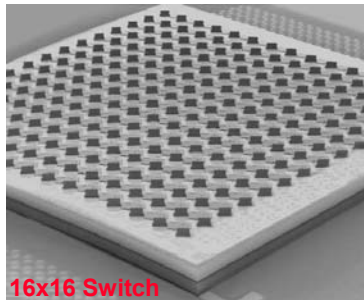
Loss Due to Mirror Tilt



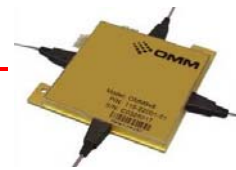
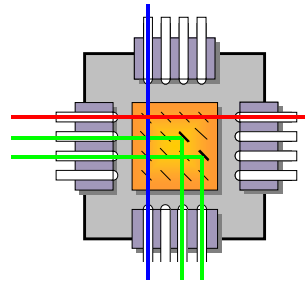
- Accuracy and uniformity of mirror angles impose a loss penalty, which limit the maximum port count



## Surface-Micromachined 2D MEMS Optical Switches (16x16)



L. Fan, et al., OFC 2002

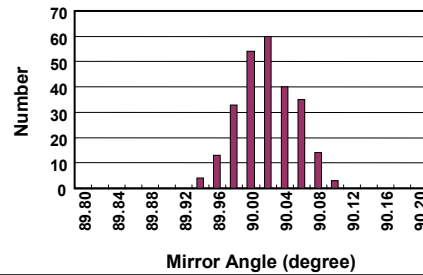


L. Fan, et al., OFC 2002

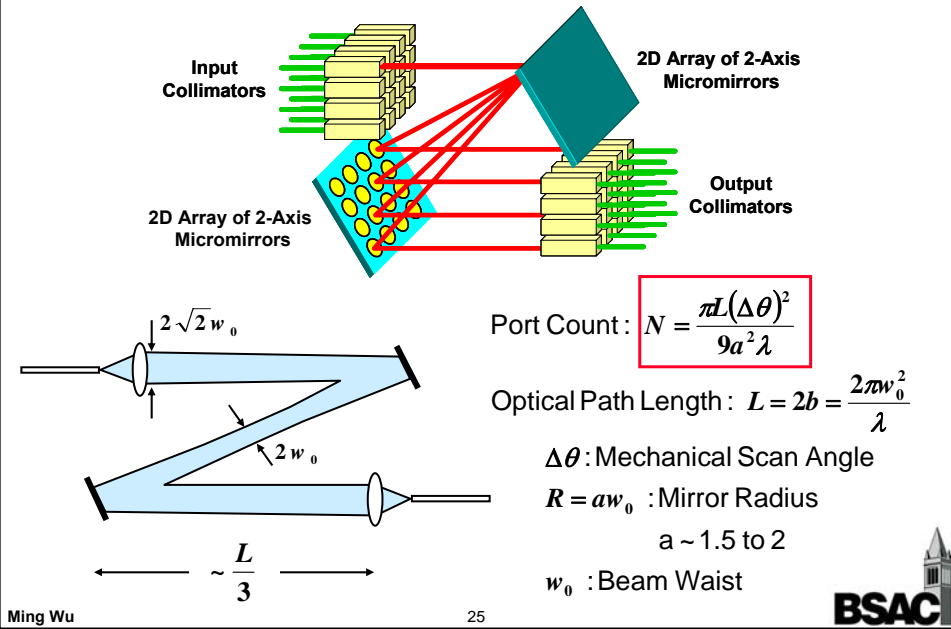


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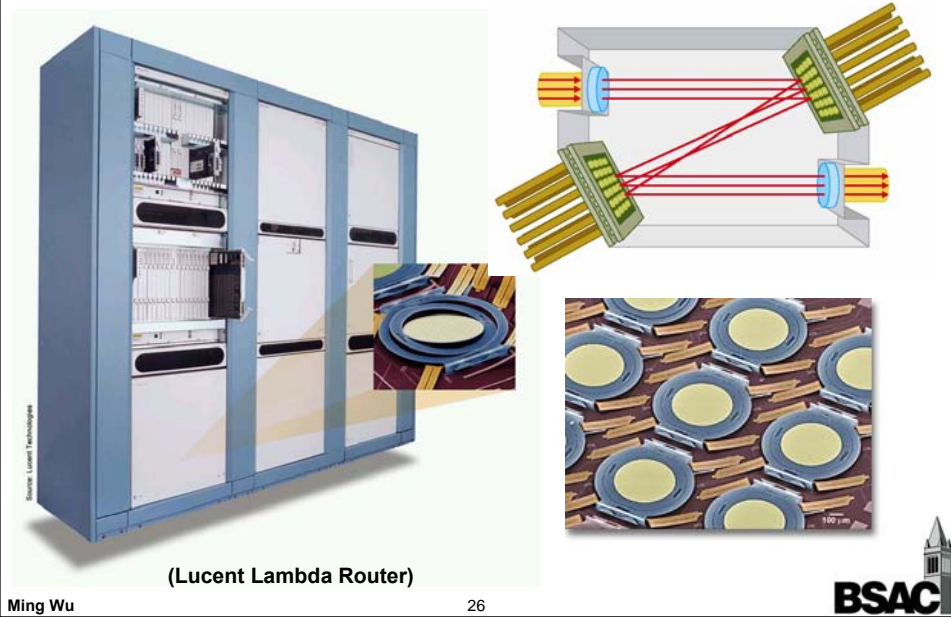
Absolute angular uniformity  $\sim \pm 0.05^\circ$



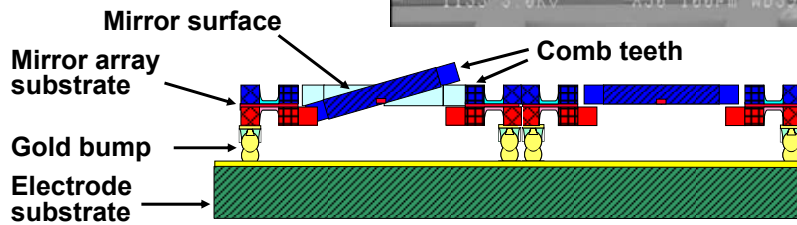
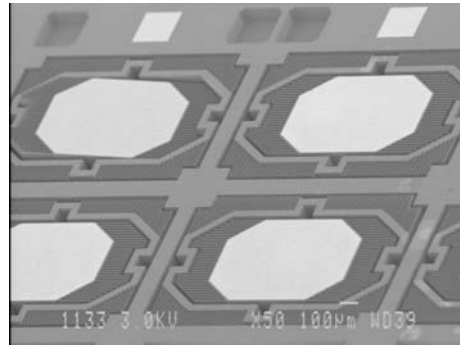
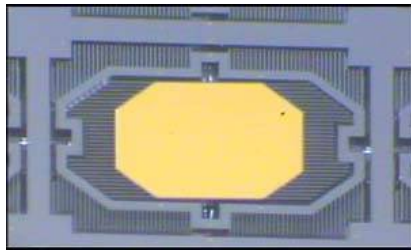
## Scaling of 3D MEMS OXC



## Lucent's Lambda Router



## 2D Scanners with Staggered Vertical Combdrives



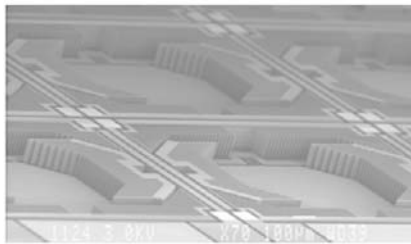
Fujitsu, 2002

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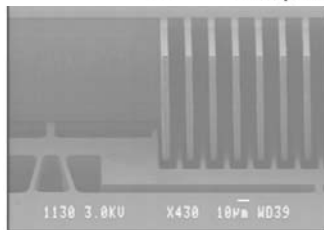
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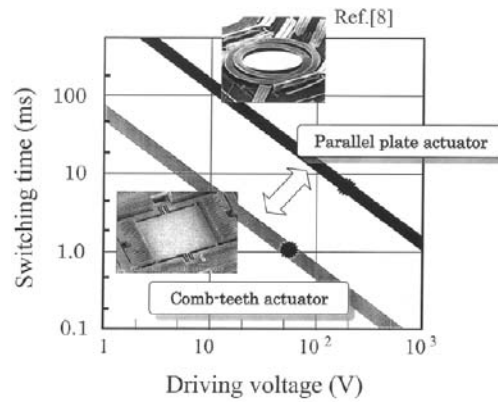
## Fujitsu Micromirror for 3-D MEMS Optical Switch



(b) 100  $\mu$ m



(a) 10  $\mu$ m

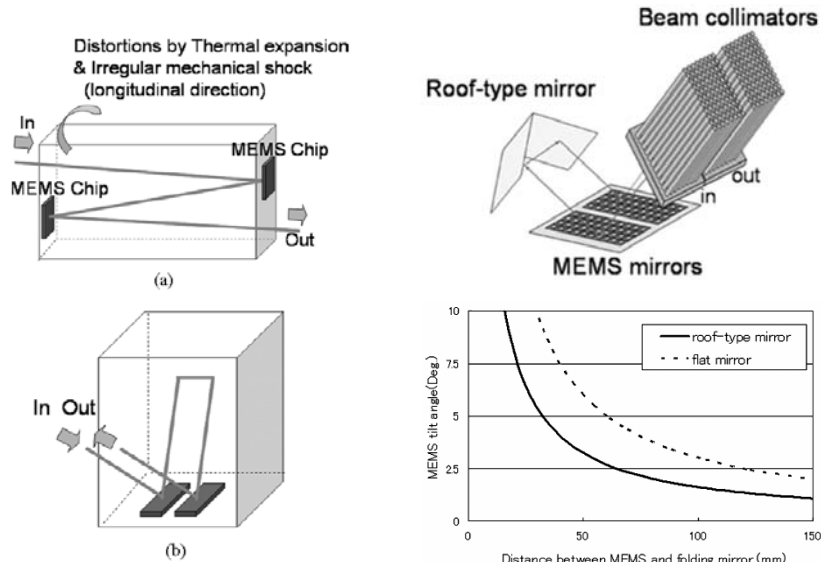


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## Fujitsu's 3-D MEMS Switch



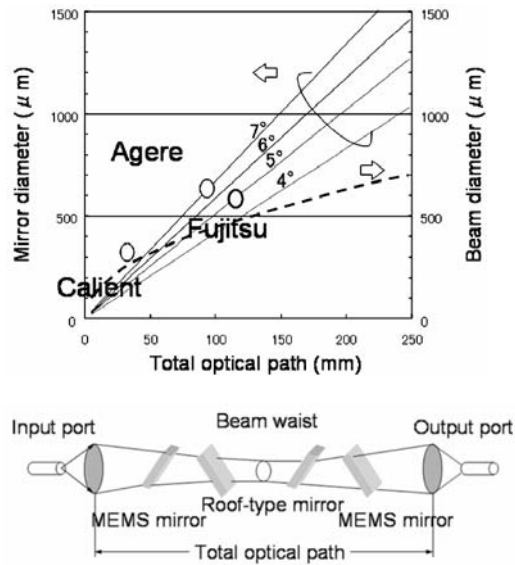
Mitsuhiro Yano, Fumio Yamagishi, and Toshitaka Tsuda, IEEE J. SELECTED TOPICS QUANTUM ELECTRONICS, VOL. 11, p. 383, MARCH/APRIL 2005

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## Fujitsu's 3-D MEMS Switch

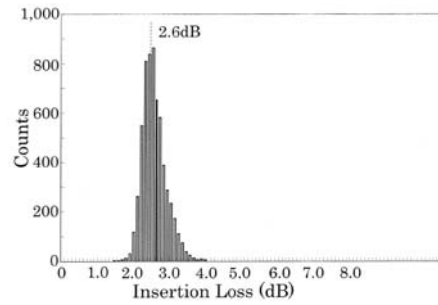
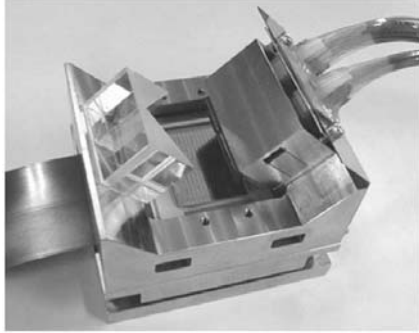


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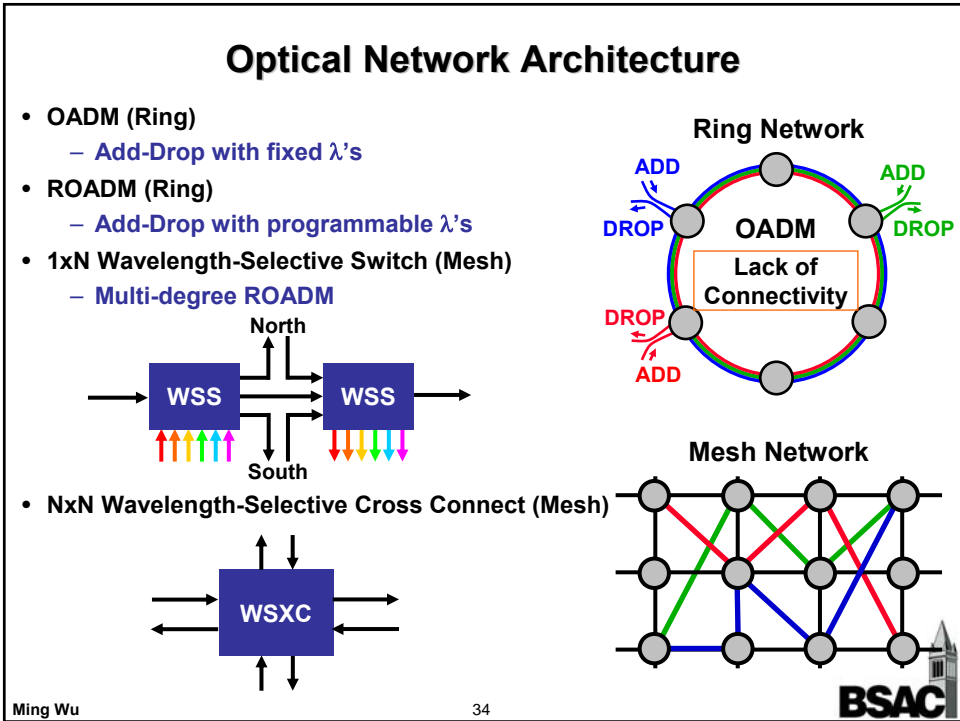
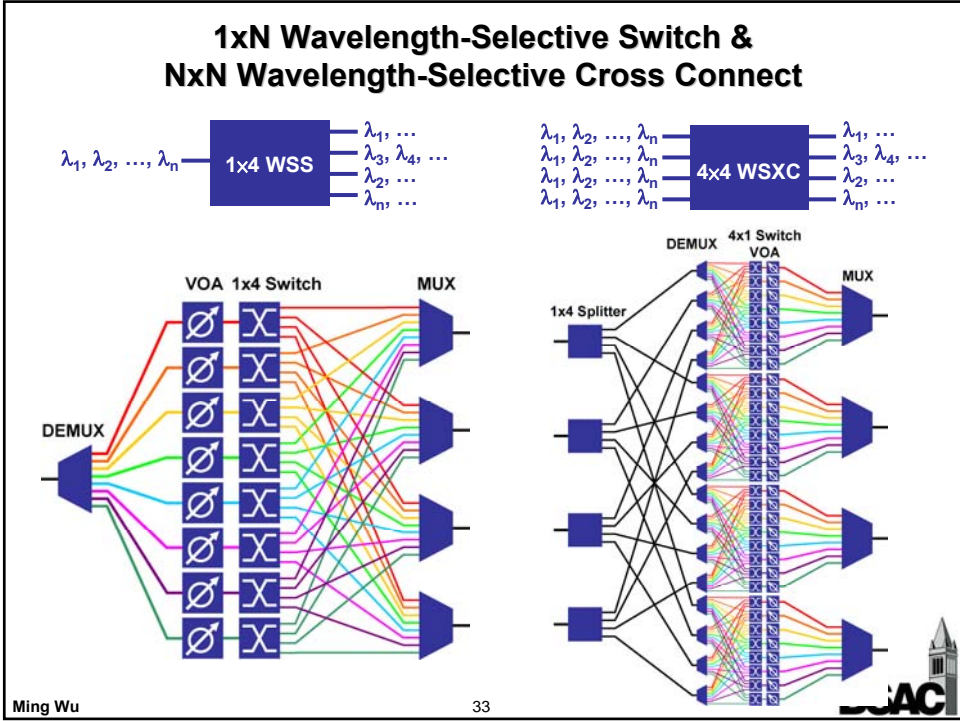
## Fujitsu's 3-D MEMS Switch



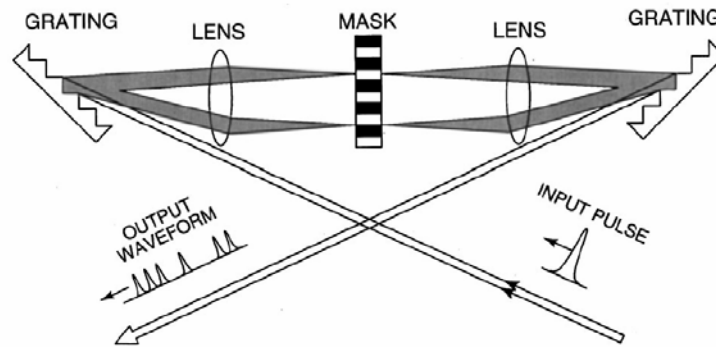
87x77x53 (mm<sup>3</sup>)

## Wavelength-Selective Switches (WSS)





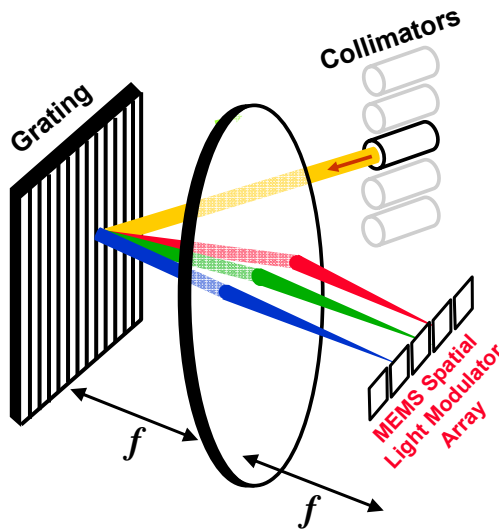
## Fourier Transform Pulse Shaper



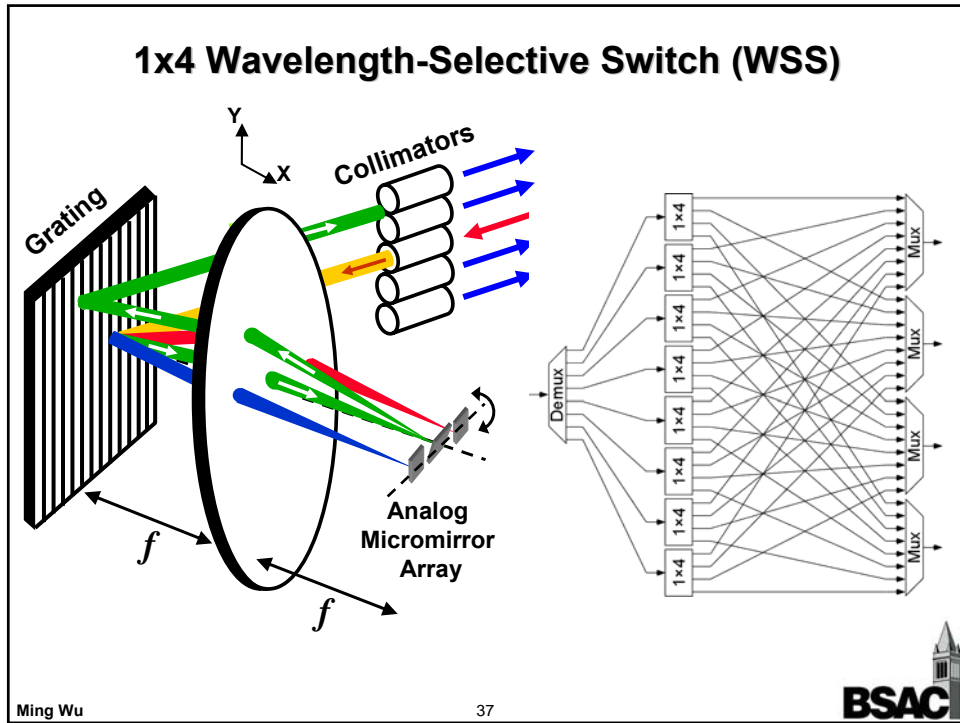
A. M. Weiner, J. P. Heritage, and E. M. Kirschner, J. Opt. Soc. Am. 1988

- Shaping femtosecond pulses by modulating the phases and amplitudes of their spectral components

## Dynamic WDM Functions



MEMS Spatial Light Modulator Array	Dynamic WDM Functions
Piston Mirrors	Femtosecond pulse shaper
ON-OFF reflectors	Wavelength blocker
Variable reflectivity mirror	Spectral (or gain) equalizer
1x2 Digital micromirrors	Optical add-drop multiplexer (OADM)
1xN analog micromirrors	Wavelength-Selective Switch (WSS)
Deformable mirrors	Tunable dispersion compensator

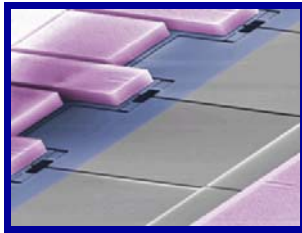


### 1x4 WSS

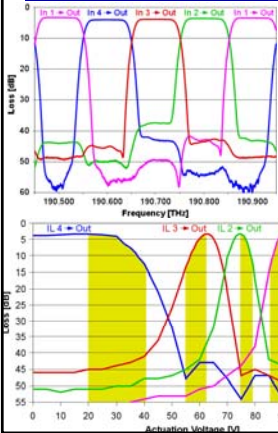
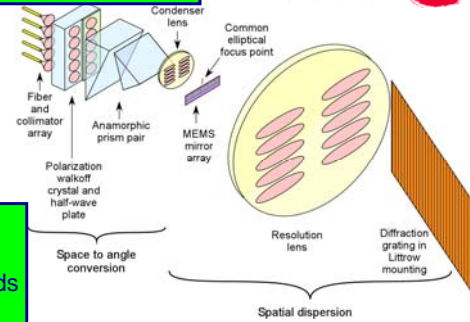
- **D. Marom et al. (Lucent), OFC 2002**
  - 1x4 WSS
  - Channel spacing: 50 or 100 GHz
  - MEMS performance: 12° (> 55 V)
- **T. Ducellier et al. (JDS-U), ECOC 2002**
  - 1x4 WSS
  - Channel spacing: 100 GHz
  - MEMS performance: ±2°

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# 64 Channel, Wavelength-Selective 4x1 Switch (D. Marom)



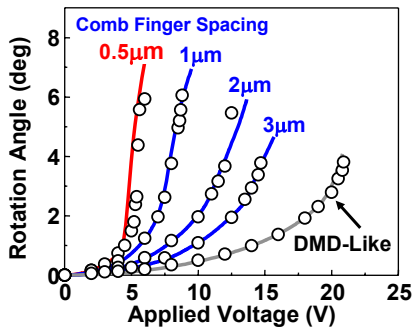
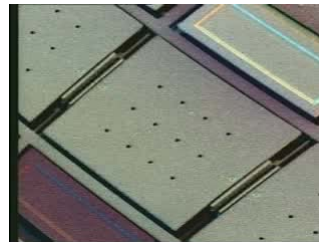
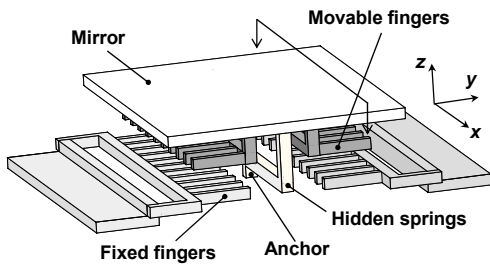
## Free-Space Implementation



- WSS provides:**
- Port switching
  - Wide passbands
  - 10 dB DSE
  - Blocking
  - Low insert loss
  - Low PDL, DGD



# Analog Micromirror Array (UCLA)

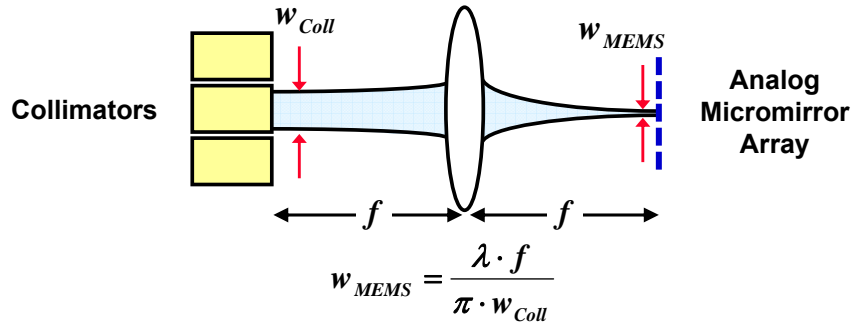


Scan Angles	+/- 6° (mechanical)
Voltage	6 V
Fill Factor	98%
Res. Freq.	3.4 kHz
Stability (3hr)	±0.00085°
System (3hr)	±0.0035dB

• D. Hah, et al (UCLA) J. MEMS, 2004, p. 279  
 • J.C. Tsai, et al (UCLA) IEEE PTL 2004, p. 1041



## Scaling of WSS

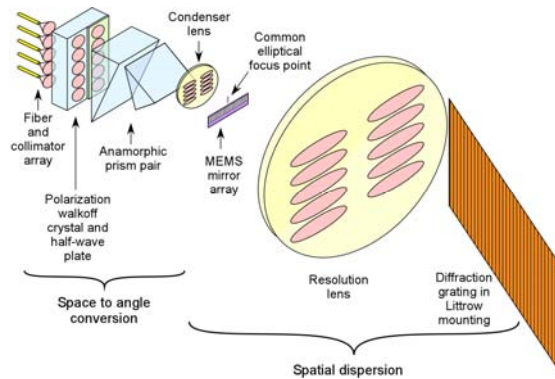


$$\frac{N_{spatial}}{\Delta\lambda} = \frac{\pi}{22\lambda} \frac{f}{f\#} \cdot \left( \frac{\partial\theta}{\partial\lambda} \right)_{Grating}$$

$$N_{spatial} \cdot N_{\lambda} = \frac{\pi \cdot BW_{\lambda}}{22\lambda} \frac{f}{f\#} \cdot \left( \frac{\partial\theta}{\partial\lambda} \right)_{Grating}$$

- System size  $\sim 2f$
- Total capacity ( $N_{spatial} \times N_{\lambda}$ ) is constant
- Proportional to  $f$

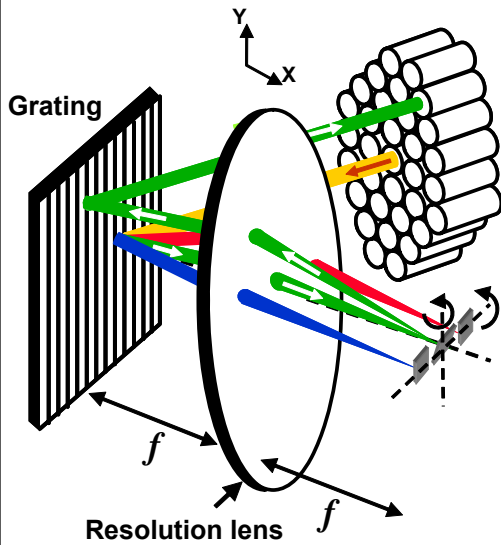
## Approach for Increasing Port Count (1)



D. Marom  
(Lucent)

- Use anamorphic prism pair to compress lateral beam size on MEMS micromirrors
- Elliptical beams on MEMS mirrors  $\rightarrow$  Rectangular micromirror

## Approach for Increasing Port Count (2)



- $1 \times N^2$  WSS:
  - 2D collimator array
  - 1D array of 2-axis micromirror array
- Port count is increased from  $N$  to  $N^2$ 
  - $N$  is the diffraction-limited linear port count
- High port count WSS
  - $1 \times 32$  WSS has been demonstrated

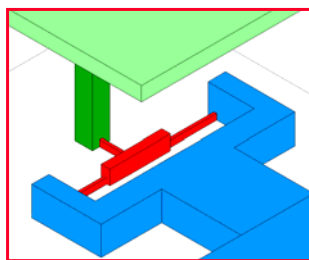
• J.-C. Tsai, et al., (UCLA) ECOC 2004, Paper Tu1.5.2

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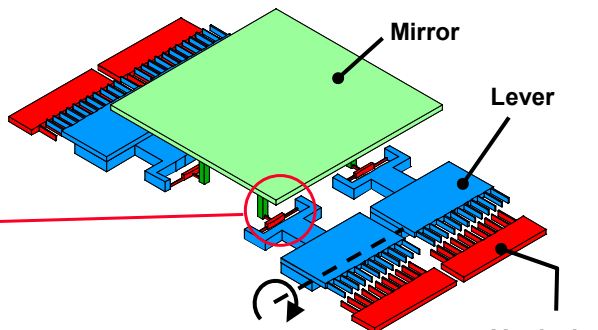
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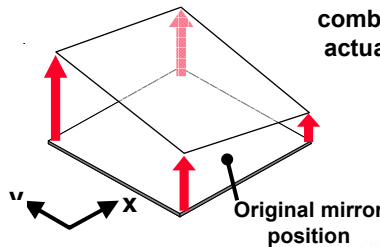
## High-Fill Factor 2-Axis Micromirror Array



2-DOF mirror joint



Vertical combdrive actuators



Original mirror position

- Gimbal-less
  - High Fill factor ( $> 98\%$ )
- Large scan angle
  - $3 \times$  angle amplification by leverage
- Low voltage
  - Powerful vertical combdrive actuators

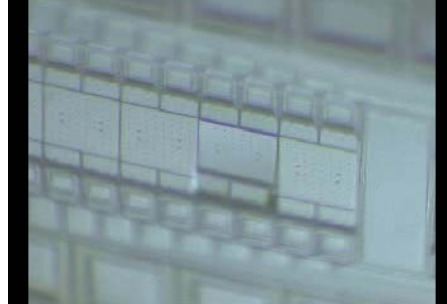
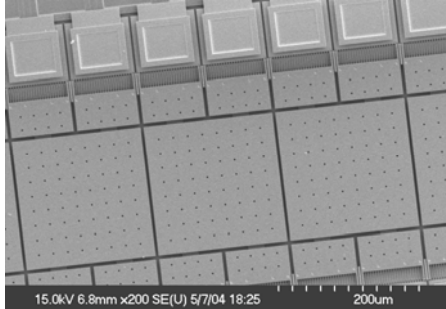
J.C. Tsai, L. Fan, D. Hah, and M.C. Wu, IEEE LEOS International Conference on Optical MEMS 2004

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## SEM of Gimbal-less 2-Axis Analog Micromirror Array



- SUMMiT-V 5-layer surface micromachining process
- Mirror pitch: 200  $\mu\text{m}$
- Large scan angles:  $\pm 6.7^\circ$  (mechanically) @ 75 V
- Fill factor: 98%
- Resonant frequency = 5.9 kHz

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J.C. Tsai, L. Fan, D. Hah, and M.C. Wu, IEEE LEOS  
International Conference on Optical MEMS 2004



## Planar Lightwave Circuit (PLC) MEMS

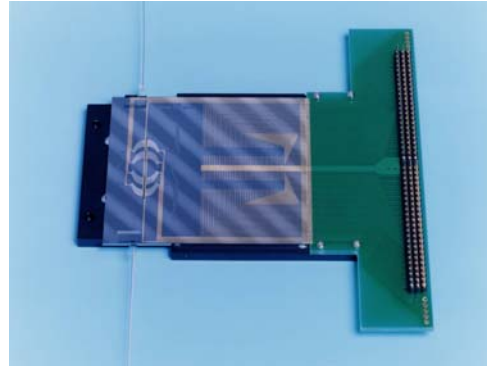
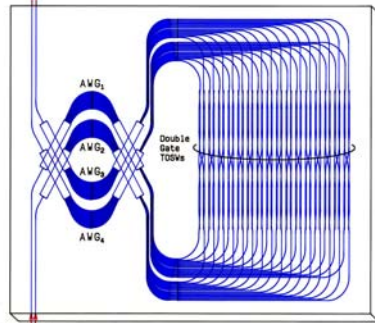
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## Reconfigurable Optical Add/Drop Multiplexer (ROADM)

Drop port  
Main output port



Main input port

(a) Configuration of 16ch-100GHz OADM

(b) Photograph of OADM

K. Okamoto et al., *Electron. Lett.*, vol. 31, pp.723-724, 1995

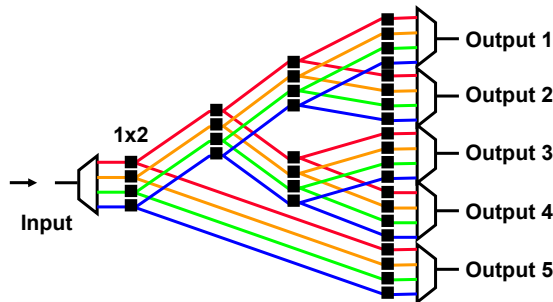
(VG courtesy of K. Okamoto)

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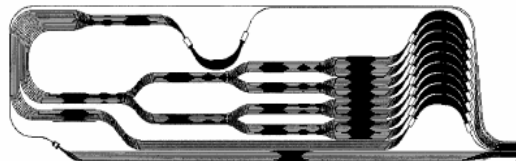
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## PLC 1x9 WSS



- 1x9 WSS
- Thermal optical switch
  - 450 mW / switch
  - Total power ~ 14W
- Loss ~ 5.4 dB
- Isolation > 46 dB



C.R. Doerr, et al. (Lucent), OFC 2002 Postdeadline Paper, FA3

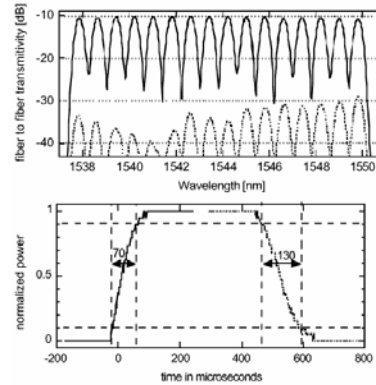
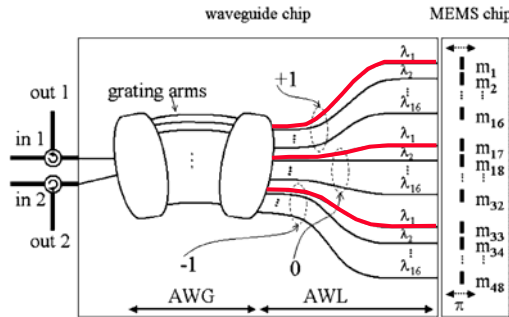
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## 2x2 MEMS Waveguide WSXC



- 3 diffraction orders by AWG
- Optical phases of (+1, 0, -1) orders modulated by MEMS piston mirrors
- Chip ~ 5 x 9 mm<sup>2</sup>

- 100 GHz channel spacing
- 10.6 dB insertion loss
- 20 dB extinction ratio

D.T. Fuchs, et al (Lucent) IEEE PTL, Jan. 2004

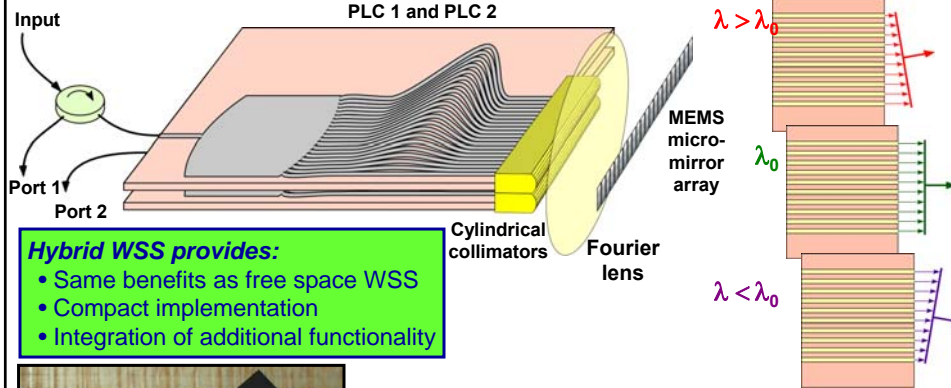
Ming Wu

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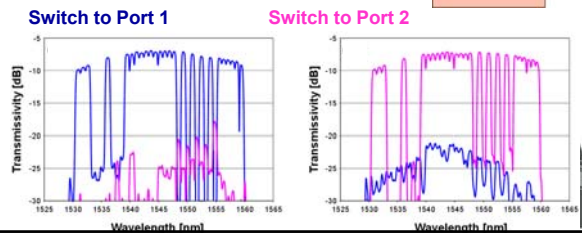
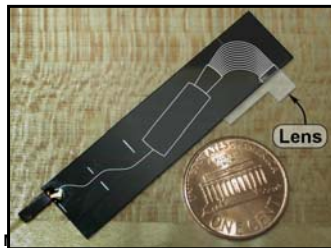


## 40 Channel, Wavelength-Selective 1x2 Switch (D. Marom)

### Hybrid PLC and Free-Space Implementation

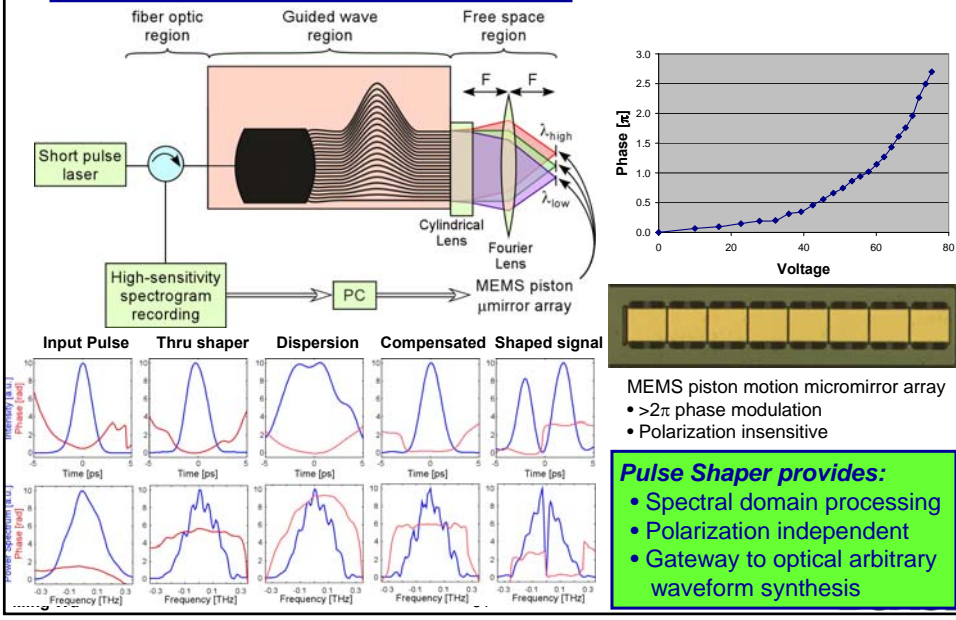


- Hybrid WSS provides:**
- Same benefits as free space WSS
  - Compact implementation
  - Integration of additional functionality

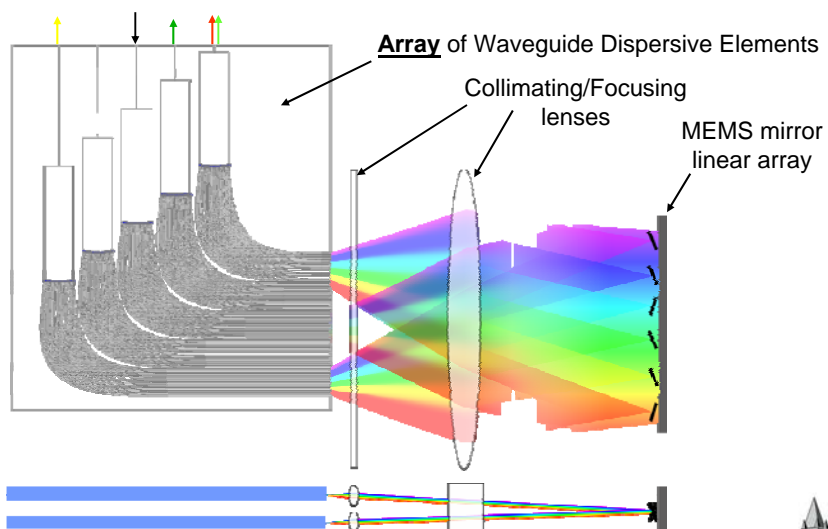


# Compact Spectral Pulse Shaper (D. Marom)

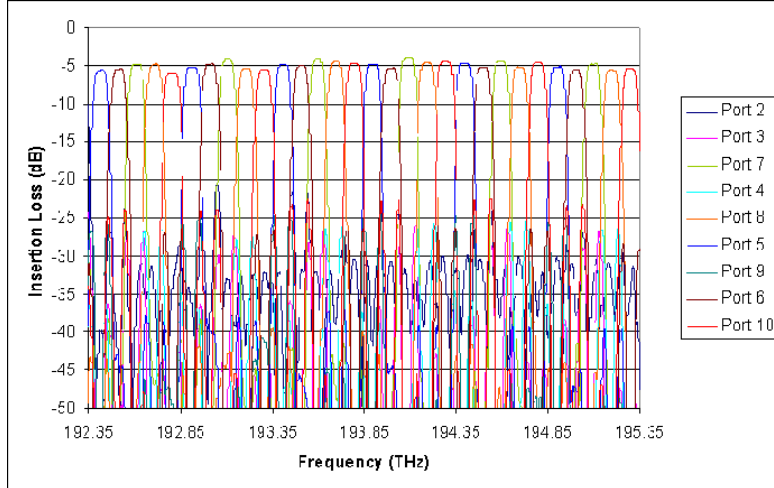
## Hybrid PLC and Free-Space Implementation



## 2D arrangement of ports for scalable 1x9 WSS



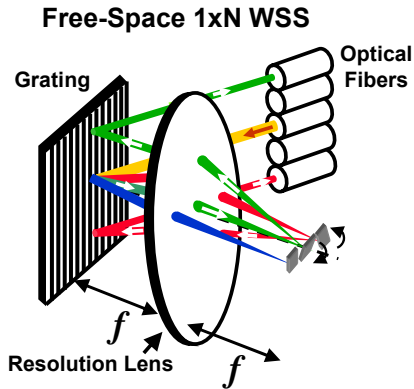
## Interleaved spectrum switched to all output ports



**metconnex**

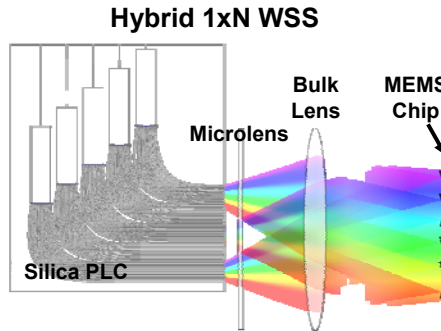
**ALCATEL**

## Free-Space and Hybrid Integrated 1xN WSS



- Large space
- Complicated alignment

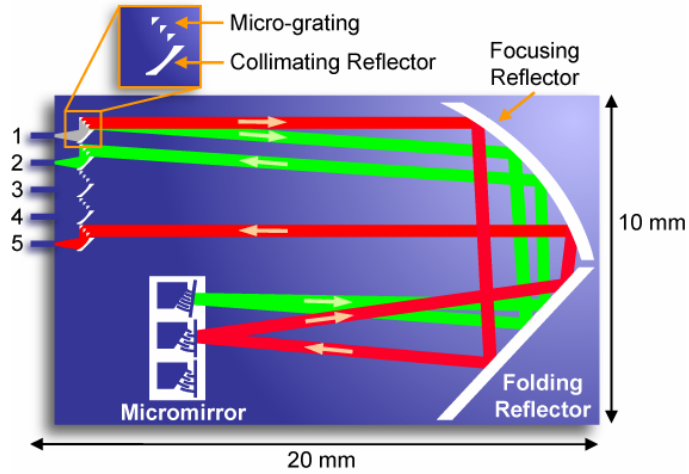
*D. Marom, et al. (Lucent), OFC 2002.*  
*T. Ducellier, et al. (JDS-U), ECOC 2002.*  
*S. Huang, et al. (UCLA), O-MEMS 2002.*



- Silica PLC has low insertion loss
- Hybrid integration requires external collimator and lens

*T. Ducellier et al. (Metconnex), ECOC, 2004*  
*D.M. Marom et al. (Lucent), OMEMS 2004*  
*D.M. Marom et al. (Lucent), ECOC 2005*

## Silicon-Based Monolithic 1x4 Wavelength-Selective Switch



- Silicon PLC is compatible with SOI-MEMS technologies
- Excess Insertion loss can be reduced with AR coating and smoothed sidewall (e.g. hydrogen annealing)

*C.H. Chi et al., CLEO 2005*

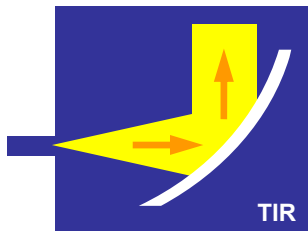
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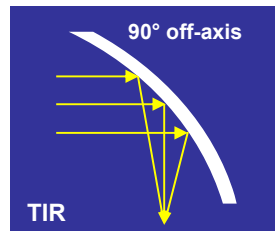
**BSAC**

## Integrated Optical Components

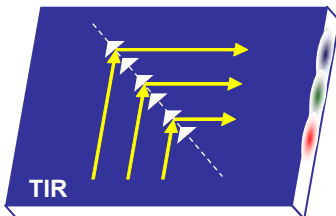
Parabolic Collimator



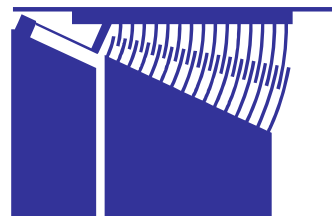
Parabolic Focusing Reflector



Blazed Micro-grating



Electrostatic Micromirror



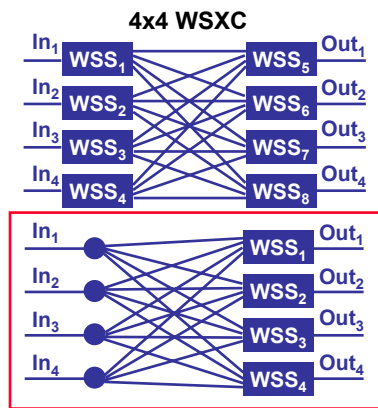
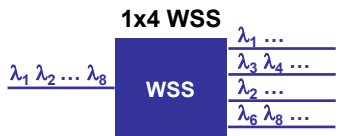
Ming Wu

*C.H. Chi et al., OFC 2006*

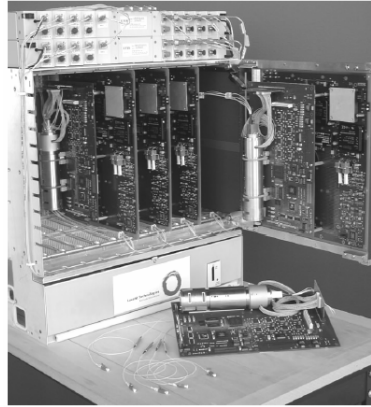
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**BSAC**

## 4x4 Wavelength-Selective Cross Connect



## Free-Space WSXC



*D. M. Marom et al. (Lucent), ECOC 2003*

- Four passive 1x4 splitters (6.5 dB loss)
- Four 4x1 WSS (4 dB loss)

Ming Wu

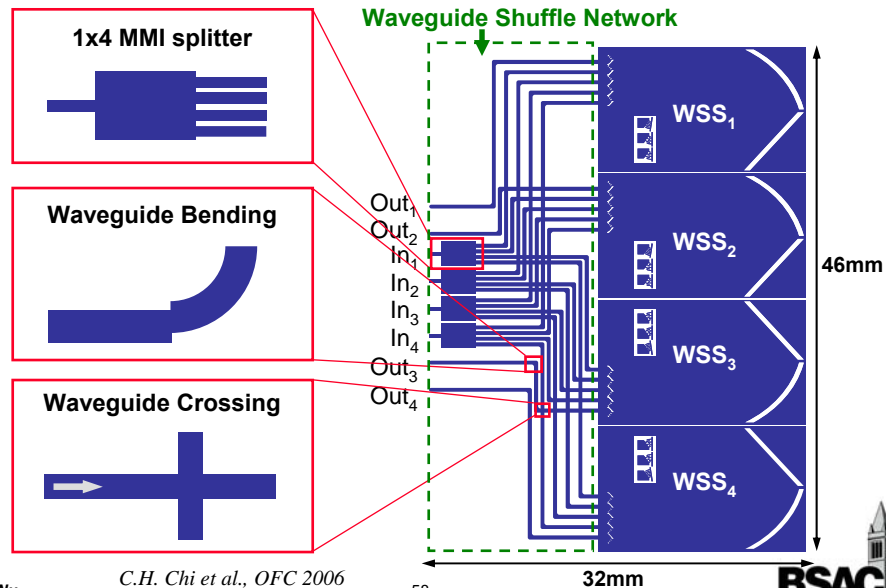
*C.H. Chi et al., OFC 2006*

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## Monolithic 4x4 WSXC – Planar Integration

Support **Broadcast** and **Multicast** Functions



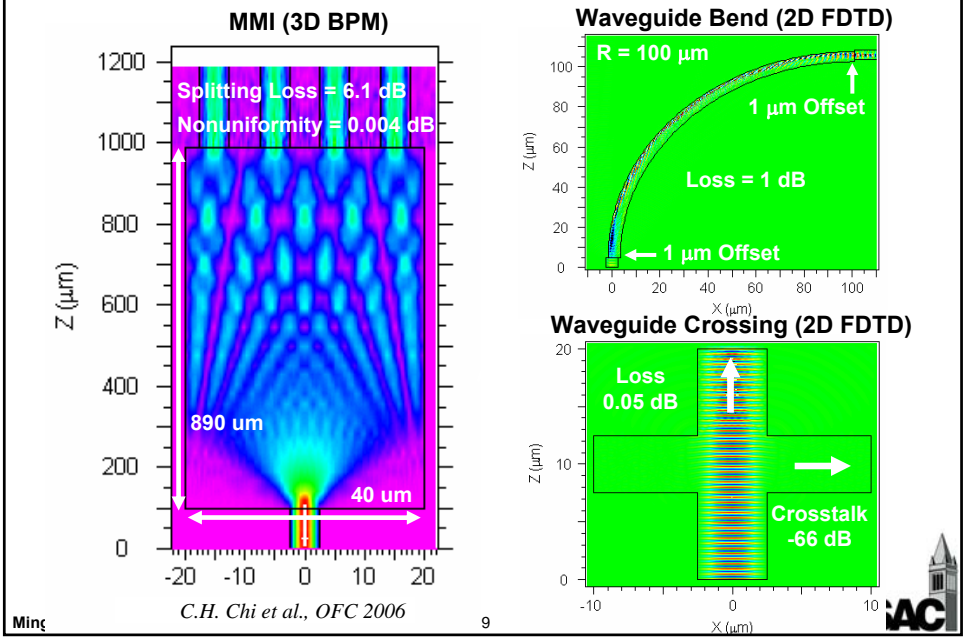
Ming Wu

*C.H. Chi et al., OFC 2006*

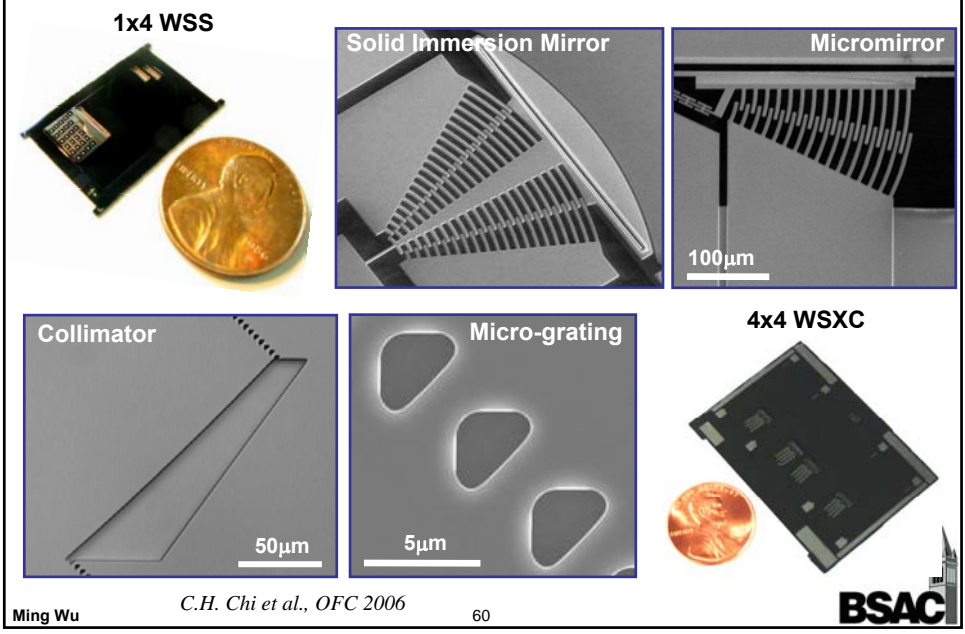
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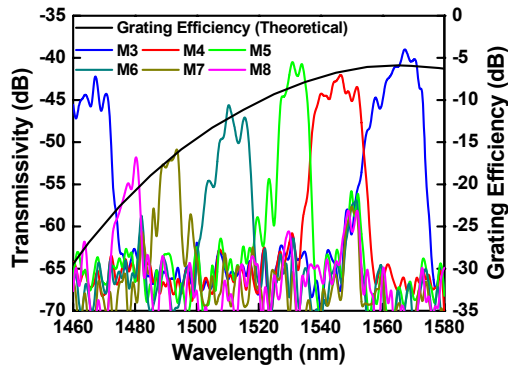
# Simulation



# Fabricated Device



## Spectral Response of 4x4 WSXC



- Six micromirrors tested with available tuning range: 1460-1580 nm
- 3 dB passbands:  
 1477-1482 nm, 1488-1494 nm, 1507-1517 nm  
 1527-1535 nm, 1539-1553 nm, 1561-1573 nm
- Additional passband from adjacent grating order

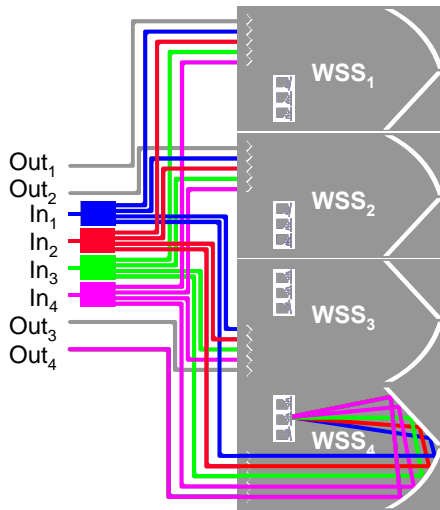
Ming Wu

*C.H. Chi et al., OFC 2006*

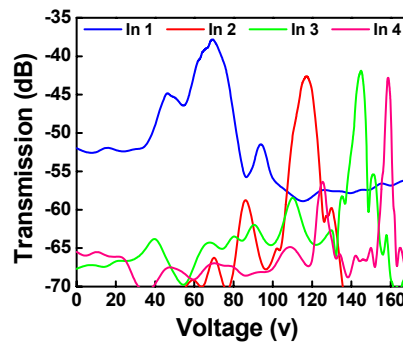
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## Transfer Curve of 4x4 WSXC



- Signals are selected from each input port and transported to the designated output port
- Extinction ratio from  $In_1$  is lower due to imperfect AR coating



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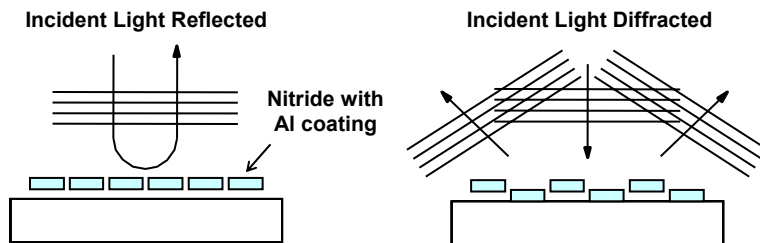
*C.H. Chi et al., OFC 2006*

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## Diffractive Optical MEMS

## Grating Light Valve



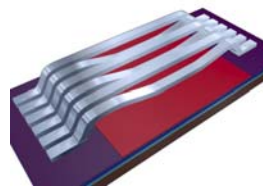
O. Solgaard, F. S. A. Sandejas, D. M. Bloom, "A deformable grating optical modulator", *Optics Letters*, vol. 17, no. 9, pp. 688-690, 1 May 1992.

- **Applications**

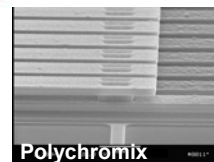
- Projection display
- Variable optical attenuators (VOA)
- Gain equalizers
- Wavelength blockers

- **Companies**

- Silicon light machine (Cypress), Lightconnect, Polychromix, Kodak



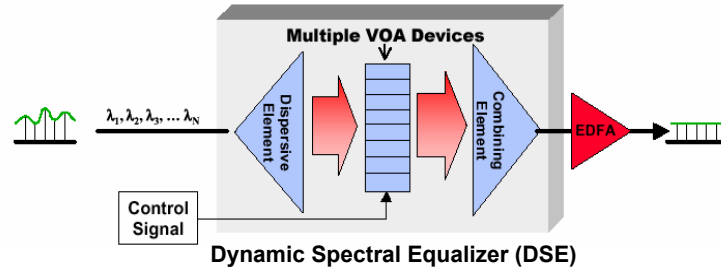
Silicon Light Machine



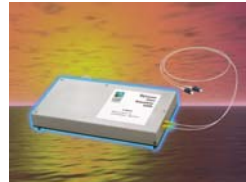
Polychromix



# Telecommunications Applications



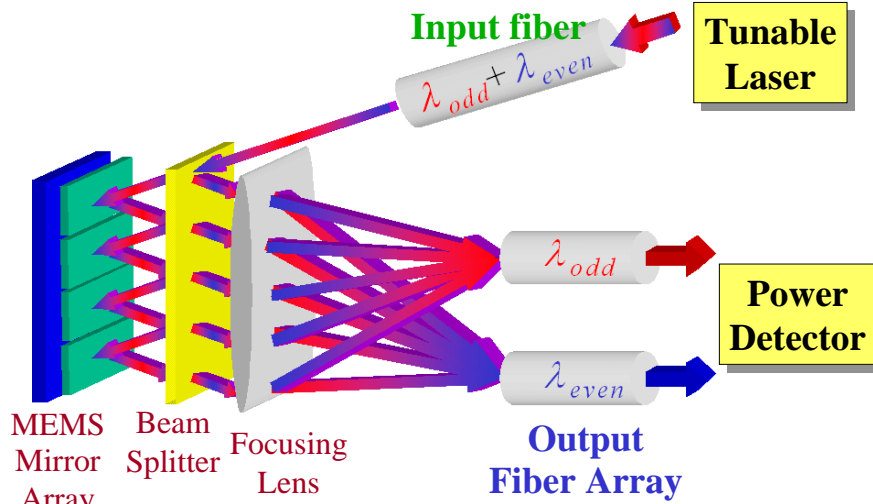
Reconfigurable Channel Blocking Filter



Dynamic Gain Equalizer

# MEMS Switchable WDM Deinterleaver Based on Gires-Tournois Interferometer

Olav Solgaard, Stanford University



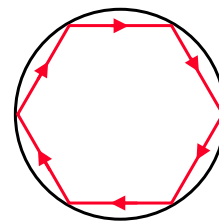
[http://wdm.stanford.edu/snrc/kyoungsik12\\_10\\_01.ppt](http://wdm.stanford.edu/snrc/kyoungsik12_10_01.ppt)

## Nanophotonic MEMS

## Whispering Gallery Mode (WGM)

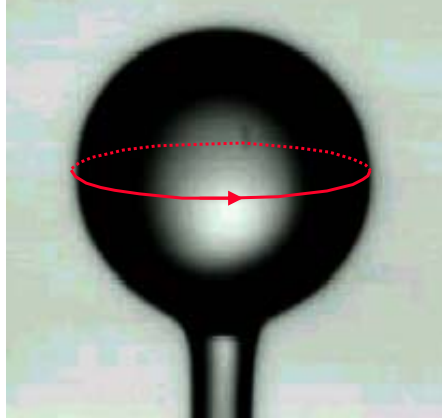


**St Paul's Cathedral, London**



- WGM first explained by Lord Rayleigh in 1910
- Used in
  - Acoustic waves
  - Microwaves
  - Optical waves

## WGM in Silica Microsphere



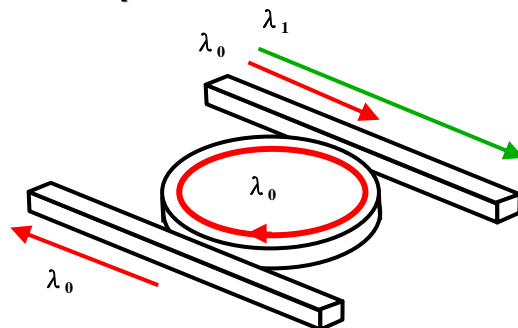
- Made by melting a fiber tip
- First demonstrated by Braginsky, et al, 1989
- Extremely high Q  
 $\sim 2 \times 10^{10}$ 
  - Maleki, et al, (JPL) 2004

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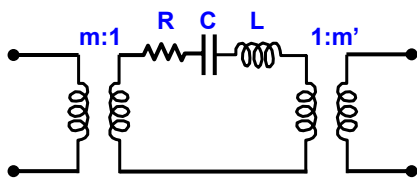
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## Optical Microresonators



### Equivalent Circuit



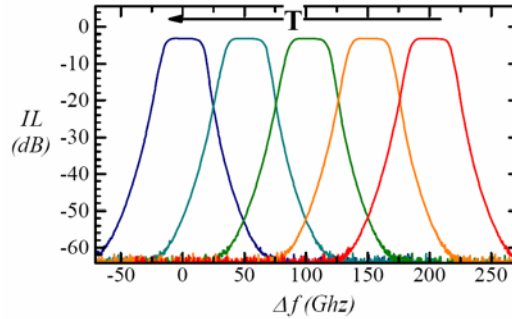
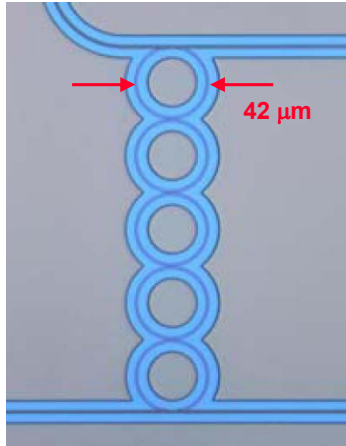
- Filter proposed by Marcatili (Bell Labs), 1969
- Channel dropping filter
- Needs high Q  $> 10^5$
- Surface roughness  $< 1$  nm

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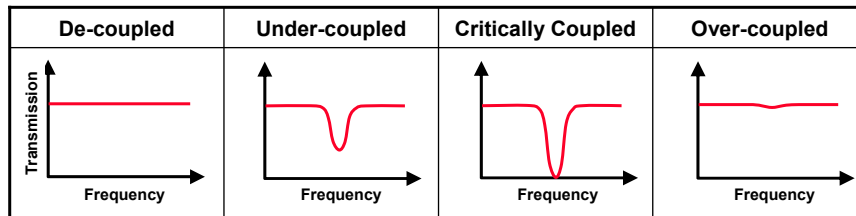
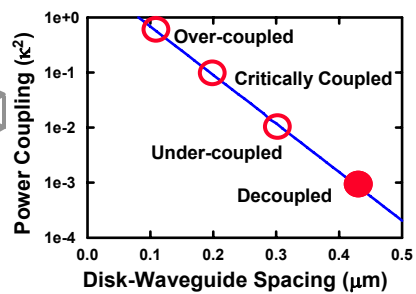
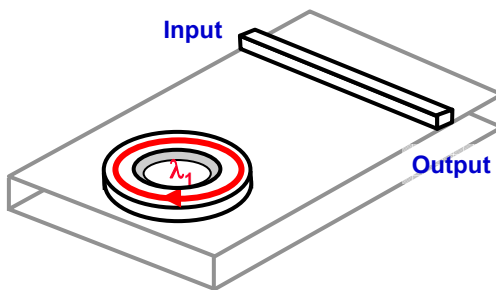
## Microring Resonator-Based PIC

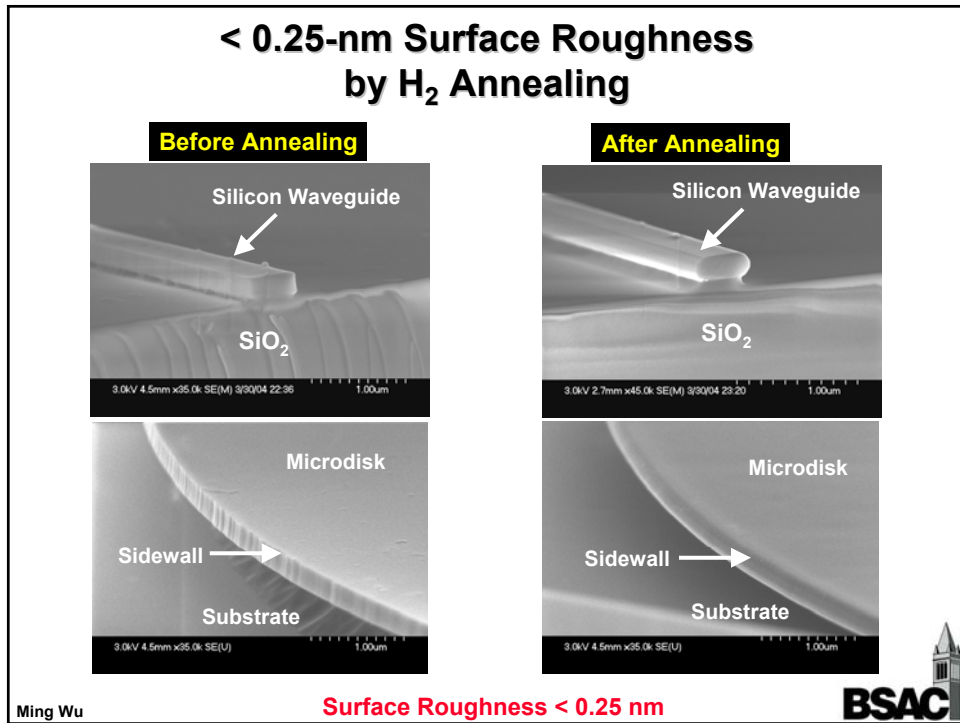
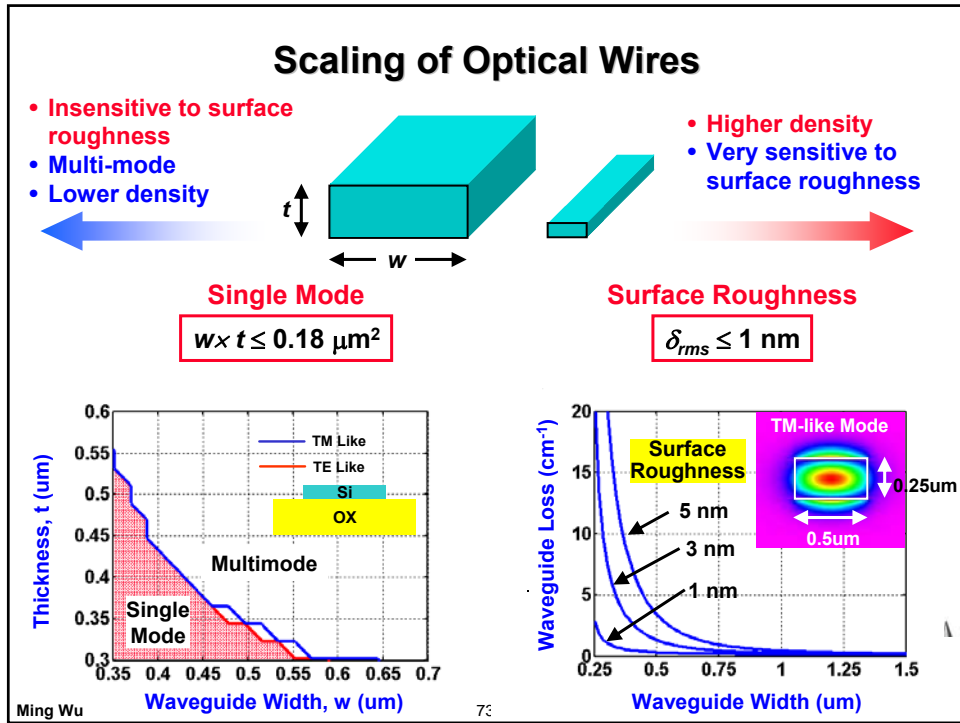


Thermally Tuned  
with Vernier Architecture

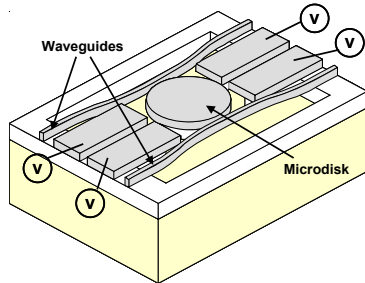
S. T. Chu, B. E. Little, V. Van, J. V. Hryniewicz, P. P. Absil, F. G. Johnson, D. Gill, O. King, F. Seiferth, M. Trakalo and J. Shanton (Little Optics) OFC 2004

## Variable Quality Factor





## Microdisk Resonator with MEMS Tunable Coupler (First Generation Device)



- Successfully demonstrated
  - Switchable notch filter (O-MEMS '03)
  - Tunable dispersion, 0 ~ 400 ps/nm (CLEO '04)
- Q ~ 10,000 due to vertical offset

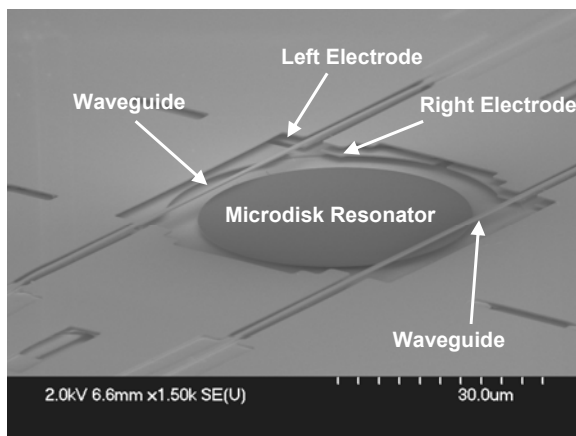


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## Second-Generation MEMS Microdisk Resonator



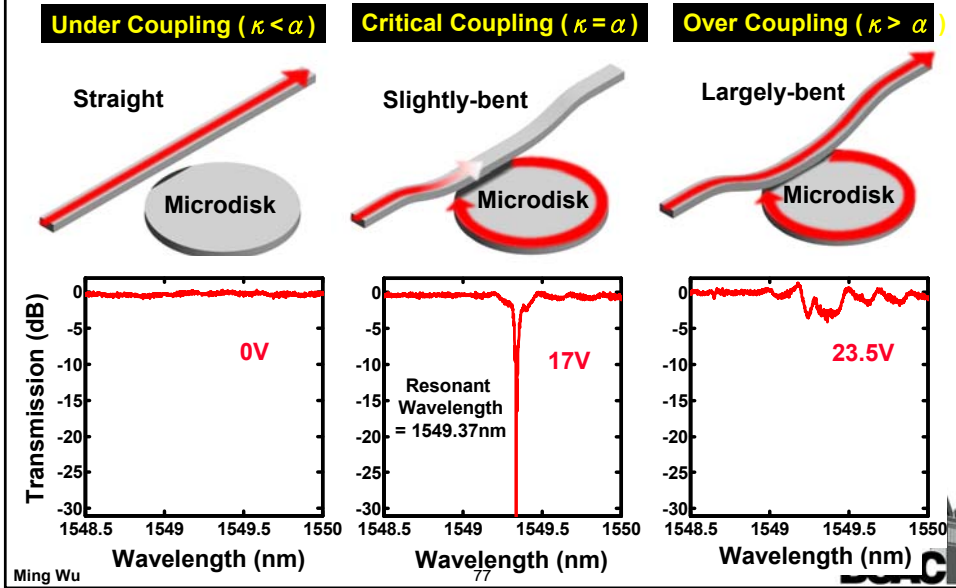
- Microdisk
  - Radius: 20  $\mu\text{m}$
  - Thickness : 0.25  $\mu\text{m}$
- Suspended deformable waveguides
  - Width: 800 nm
  - Thickness: 250 nm
  - Length: 100  $\mu\text{m}$
- Initial gap spacing
  - WG/Disk: 1  $\mu\text{m}$

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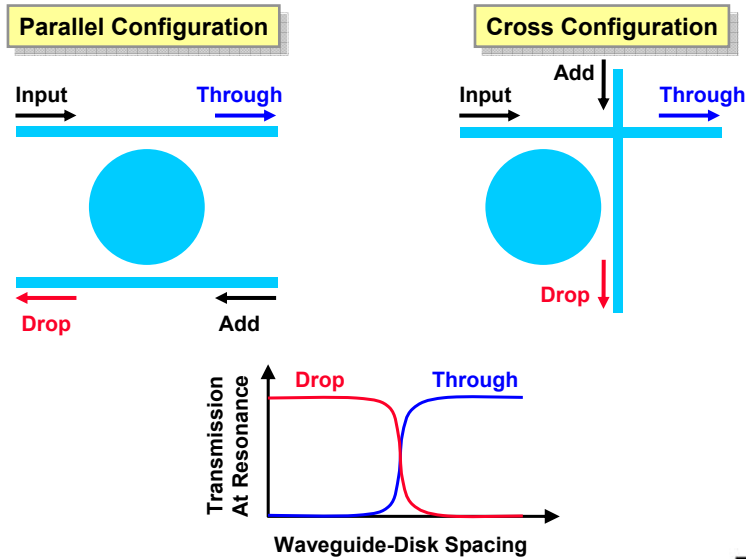
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## Under- to Over-Coupling



## Reconfigurable Optical Add-Drop Multiplexer (ROADM)

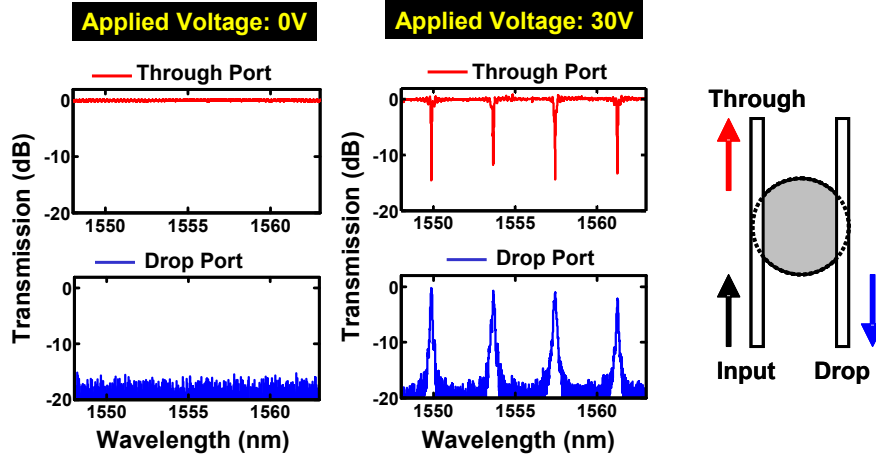


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## Dynamic Wavelength Add-Drop



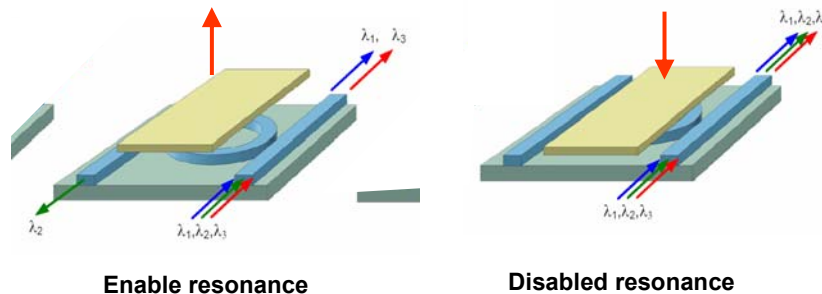
- Cascadable, hitless operation
- Almost 0 dB insertion loss is observed without bias

Min:



## Spoiling Q by MEMS Metal Membrane

- Use a metal membrane to spoil the Q of microring resonator
  - Low loss  $\rightarrow$  resonant wavelength sent to “Drop” port
  - High loss  $\rightarrow$  all wavelengths transmitted to “Through” port



Gregory N. Nielson, et al., (MIT) “MEMS based wavelength selective optical switching for integrated photonic circuits”, CLEO 2004

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## SUMMARY

- Tremendous progresses have been made in
  - MEMS devices and manufacturing
  - Micro-optics
  - Packaging
  - Control
- New trends in Optical MEMS -- Integration
  - Higher level of integration, less free-space alignment
  - MEMS-PLC integration
  - MEMS-nanophotonics integration
  - Electronics integration
  - Single-chip optical MEMS system