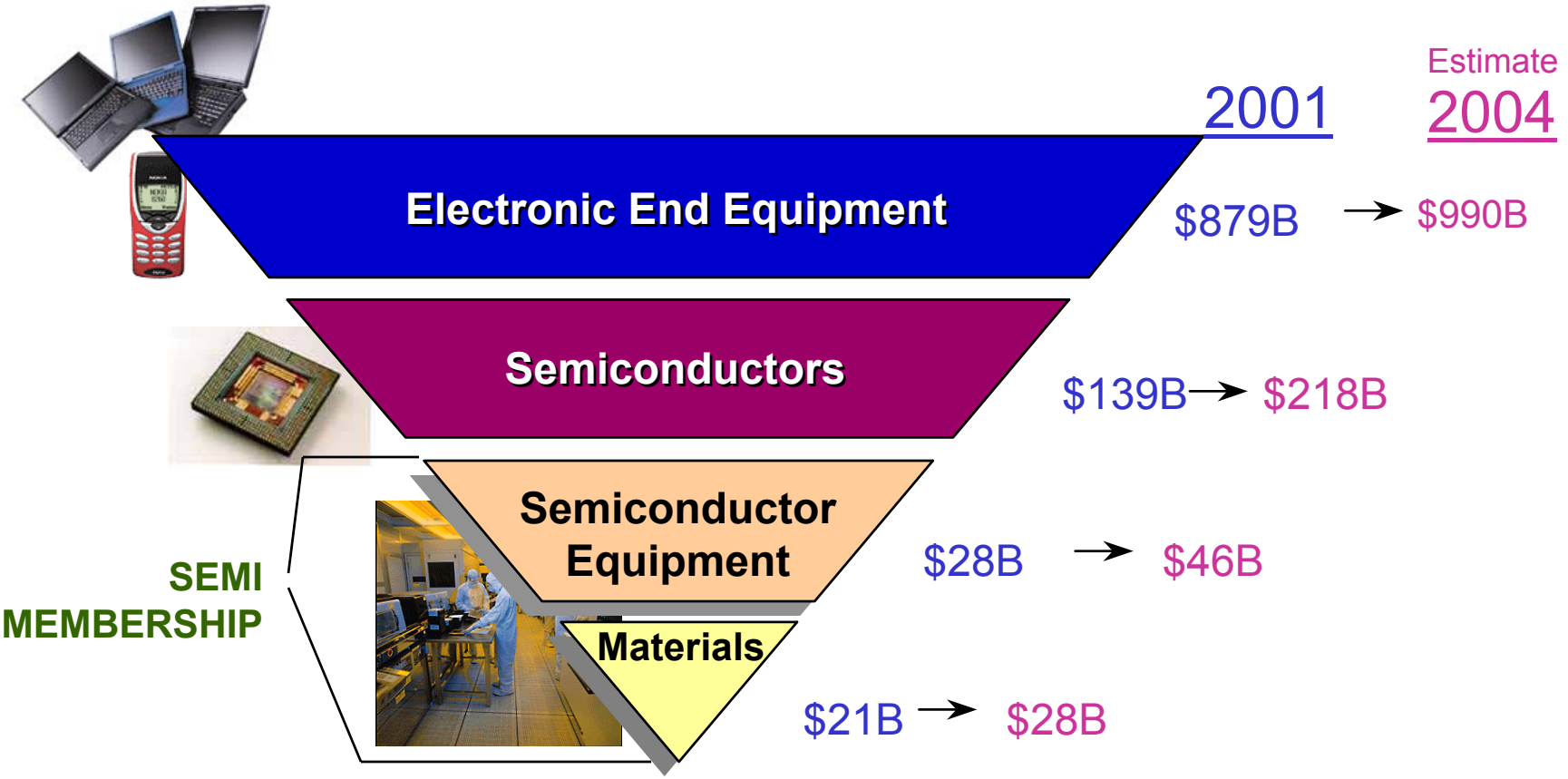


Trends of IC Microfabrication



Source: SEMI, SIA, IC Insights; Rev. January 14, 2002

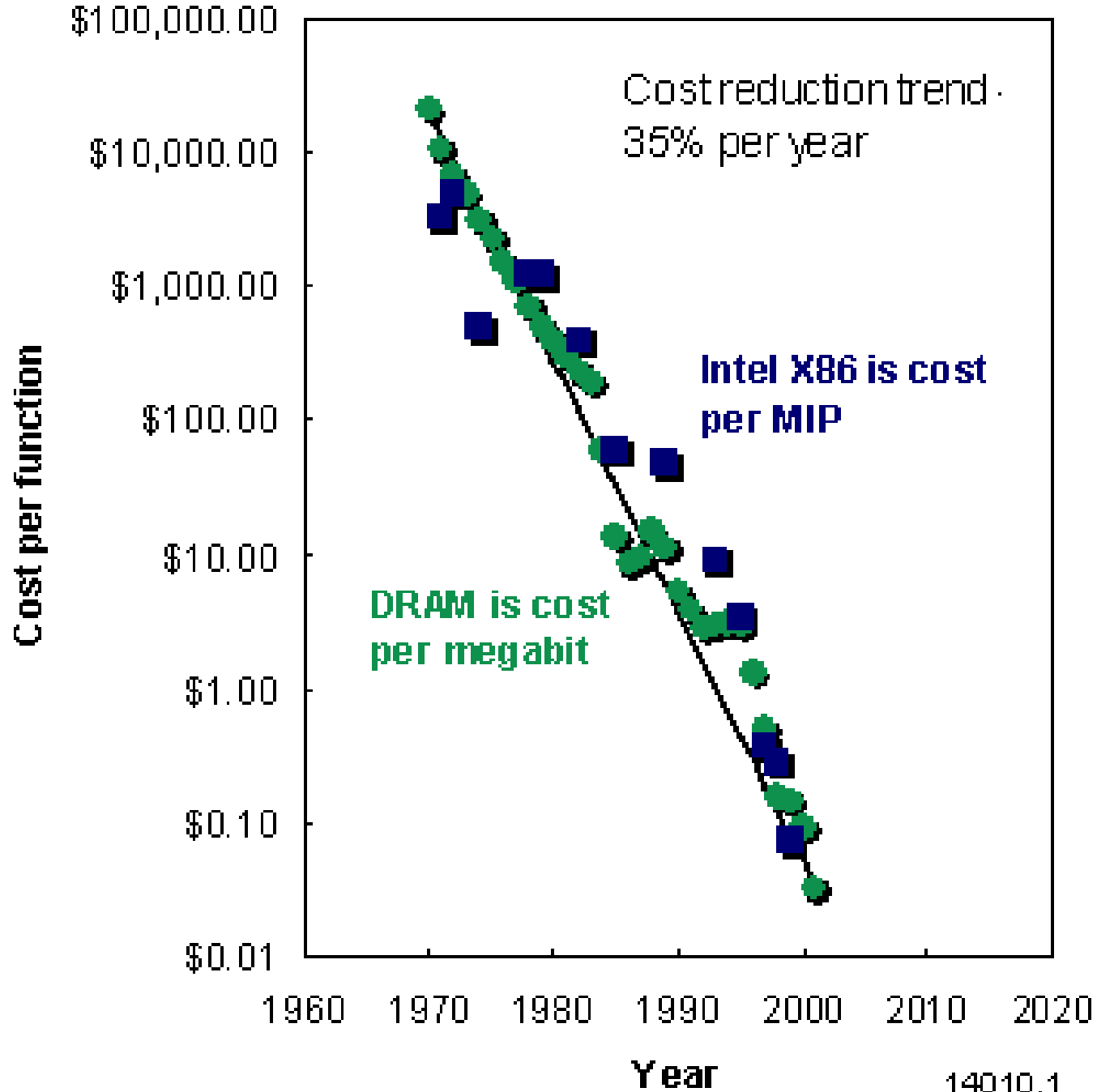
The beauty of silicon

For four decades, the semiconductor industry has steadily reduced the unit cost of IC components by

SCALING

1. Scaling device dimensions downward
2. Scaling wafer diameter upward

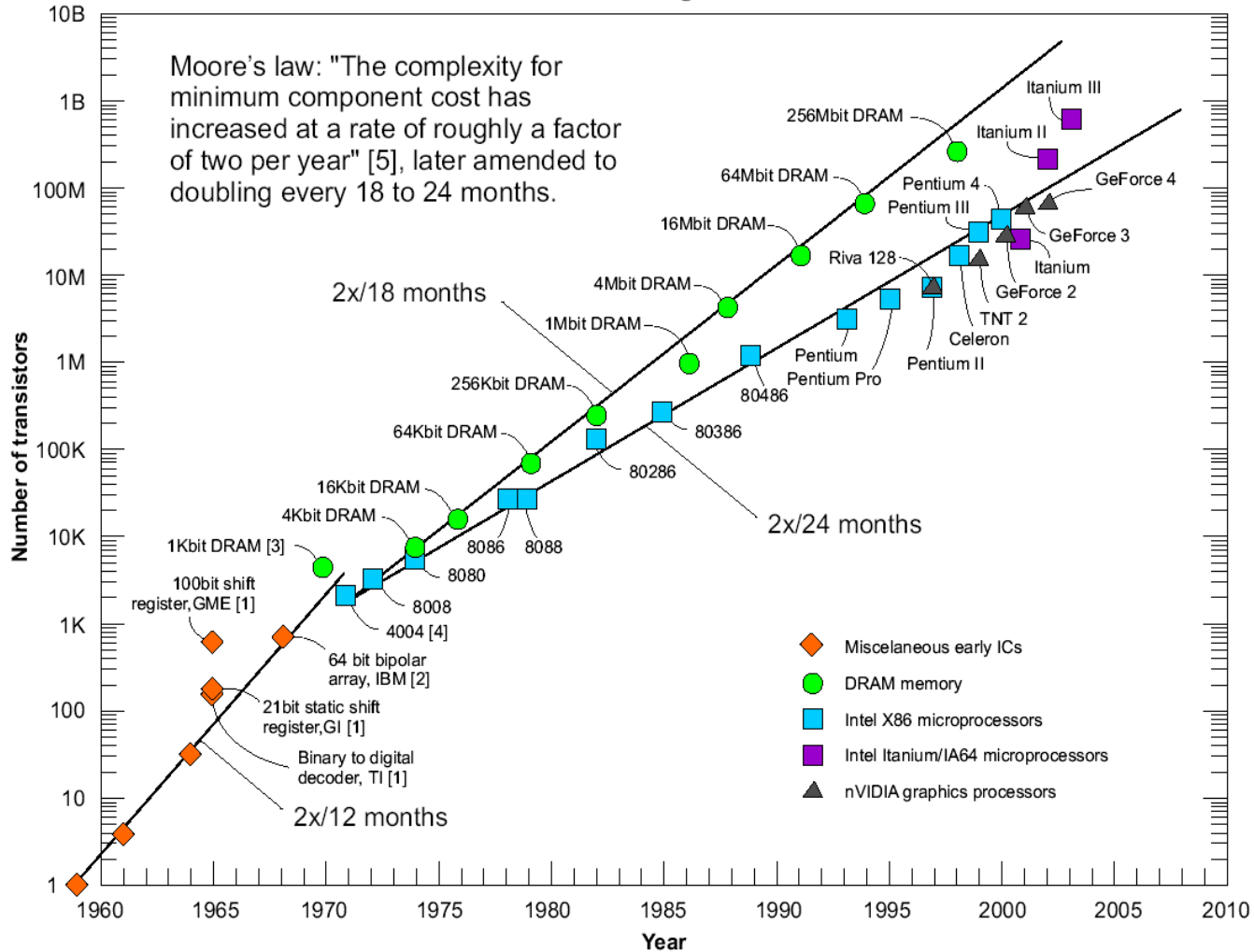
	1990	1995	2000
DRAMs	4 MB	64 MB	1 GB
Feature size	0.8 μm	0.35 μm	0.15 μm
Wafer diameter	6"	8"	12"
Cost per Megabit	\$6.50	\$3.14	\$0.10



14010.1

Transistor Per Integrated Circuit Trends

www.icknowledge.com



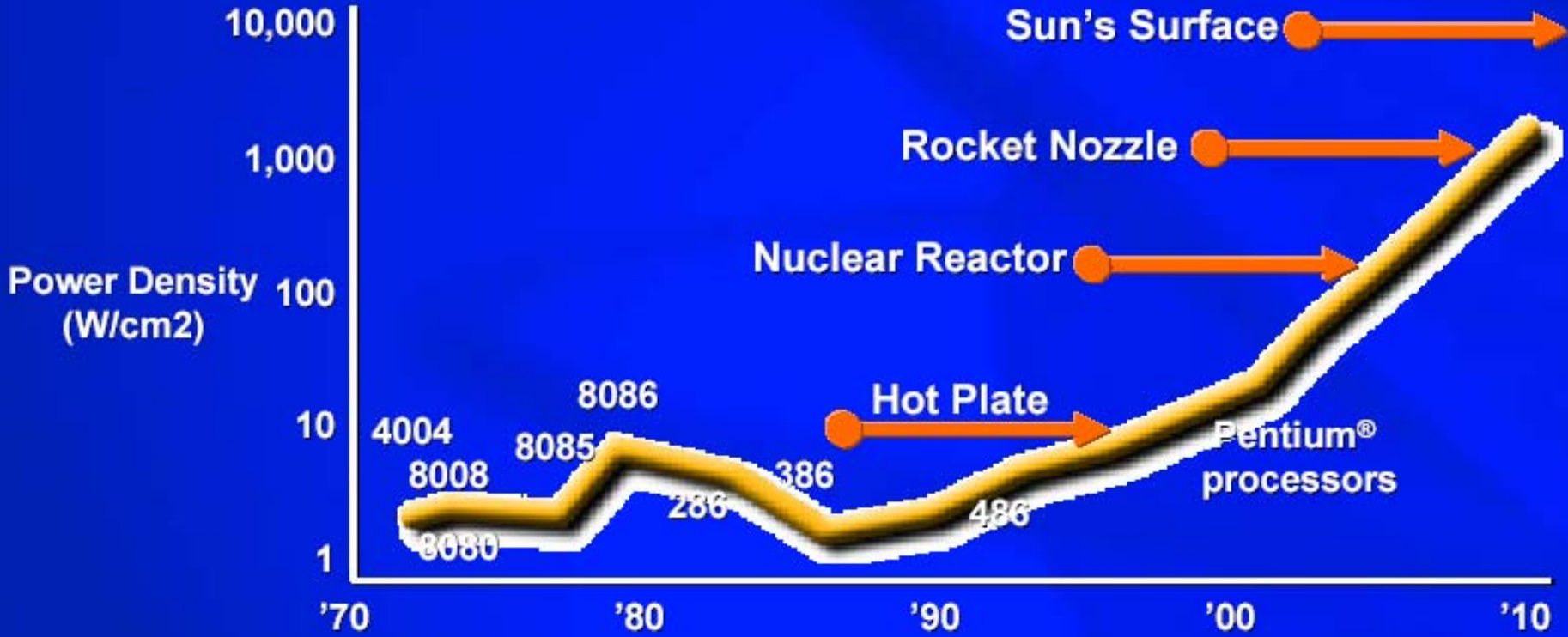
[1] Stanley Mazor, "The History of the Microcomputer - Invention and Evolution" http://www.dotpoint.com/xnumber/Microcomputer_invention.htm.
 [2] Bob Donlan and David Pricer, "Pushing the Limits: Looking Forward...Looking Back," Microelectronic Design, Vol. 1., (1987).
 [3] "Inventions of the Modern Computer: Intel 1103 The World's First Available DRAM Chip," <http://inventors.about.com/science/inventors/library/weekly/aa100898.htm>.
 [4] Jonathan Cassell, "Who Really Invented the Microprocessor," http://www.ebnonline.com/25year/25_microprocessor2.html.
 [5] Gordon E. Moore, "Cramming more components onto integrated circuits," Electronics, Vol. 38, N. 8, Apr. (1965).

SIA roadmap

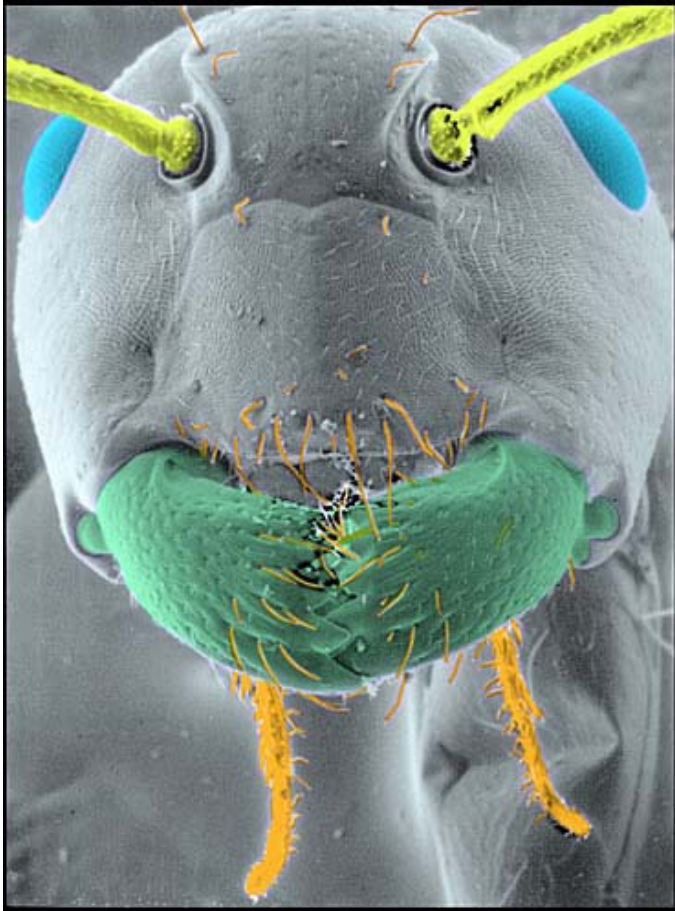
Memory and Logic Technology Requirements

Year of First Product Shipment	1999	2001	2003	2006	2009	2012
Technology Generation	180 nm	150 nm	130 nm	100 nm	70 nm	50 nm
Min. Logic V_{dd} (V)	1.8-1.5	1.5-1.2	1.5-1.2	1.2-0.9	0.9-0.6	0.6-0.5
T_{ox} Equivalent (nm)	3-4	2-3	2-3	1.5-2	<1.5	<1.0
Equivalent Maximum E-field (MV/cm)	5	5	5	>5	>5	>5
Nominal I_{on} @ 25°C (mA/mm) (NMOS/PMOS)	600/280	600/280	600/280	600/280	600/280	600/280
S/D Extension Junction Depth, Nominal (nm)	36-72	30-60	26-52	20-40	15-30	10-20

Power Density



Another Perspective on Moore's Law

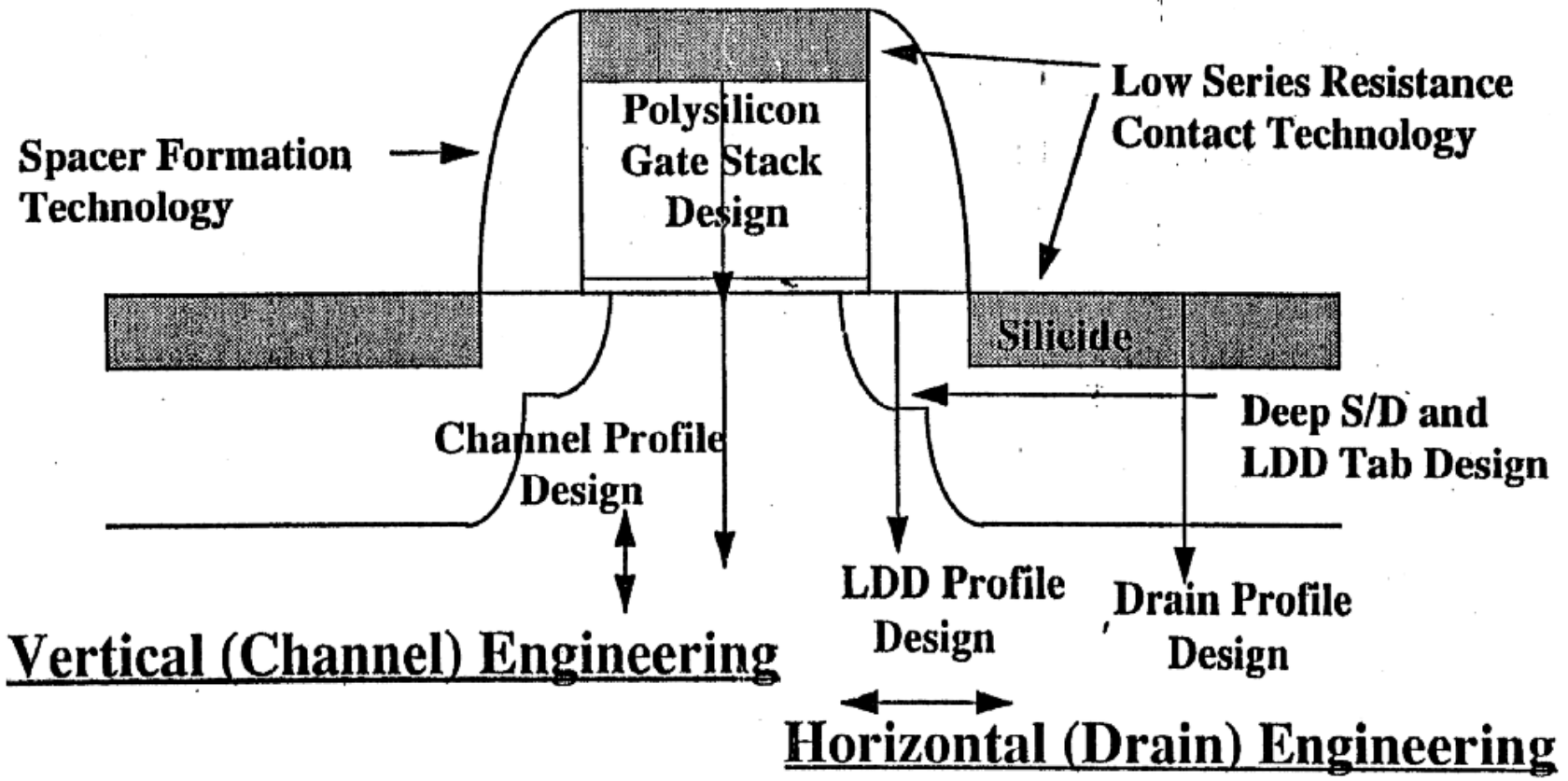


... we are already producing 10^{18} transistors per year. Enough to supply every ant on the planet with ten transistors.

Twenty years from now, if the trend continues, there will be more transistors than there will be cells in the total number of human bodies on Earth.

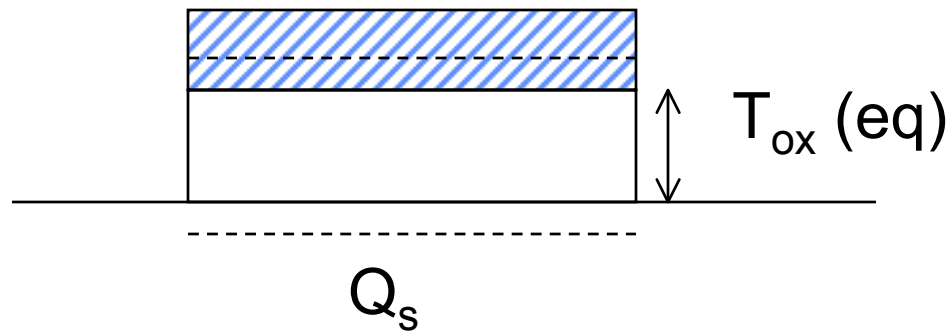
Channel Engineering

SUBMICRON DEVICE STRUCTURE AND DESIGN



Gate Stack Technology

Equivalent Oxide Thickness

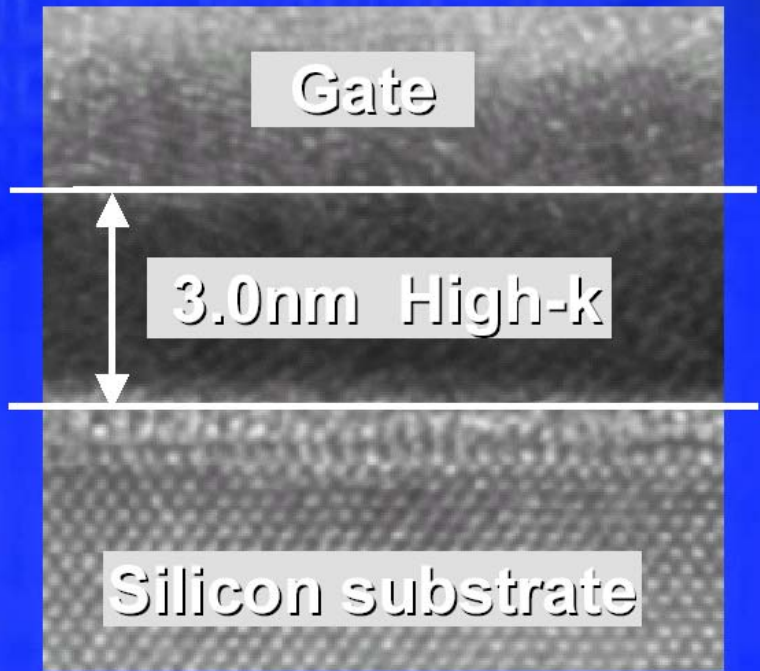
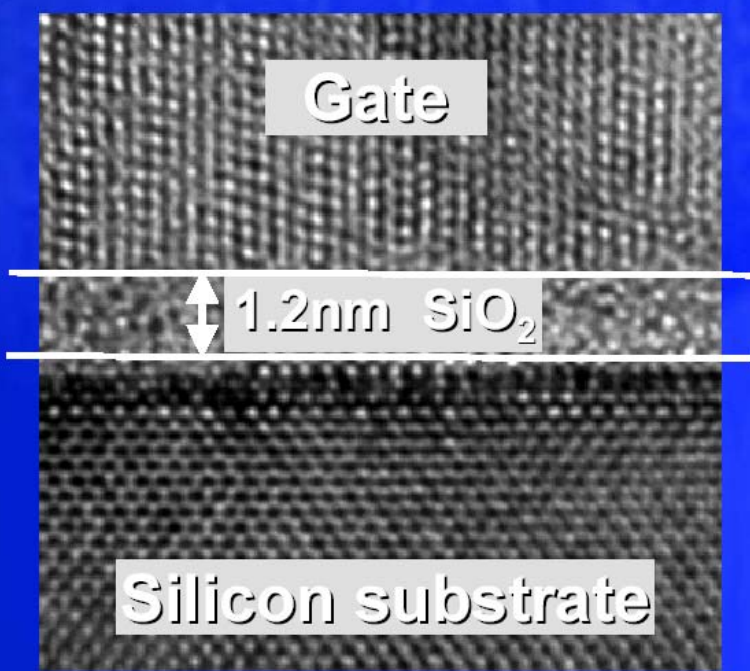


→ **high k-dielectric**
gives lower electrical thickness.

Materials:
Ta205
BZT
Al2O3

$$C_{ox} \propto \frac{\epsilon}{X_{ox}}$$

Nanotechnology for Gate Dielectrics



Source: Intel

90nm process

Experimental high-k

Capacitance

Leakage

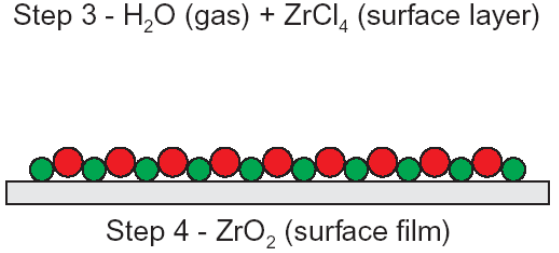
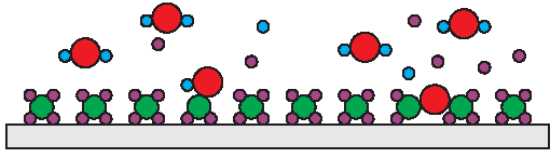
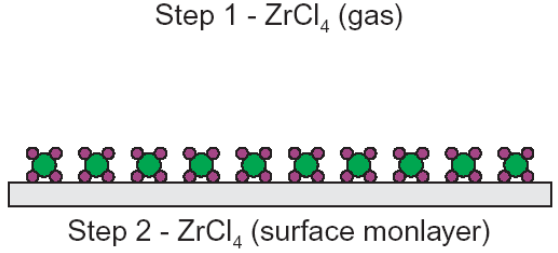
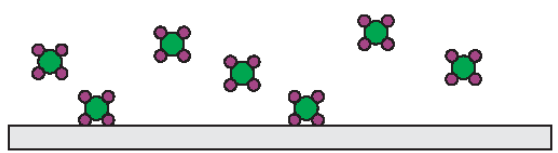
1X

1X

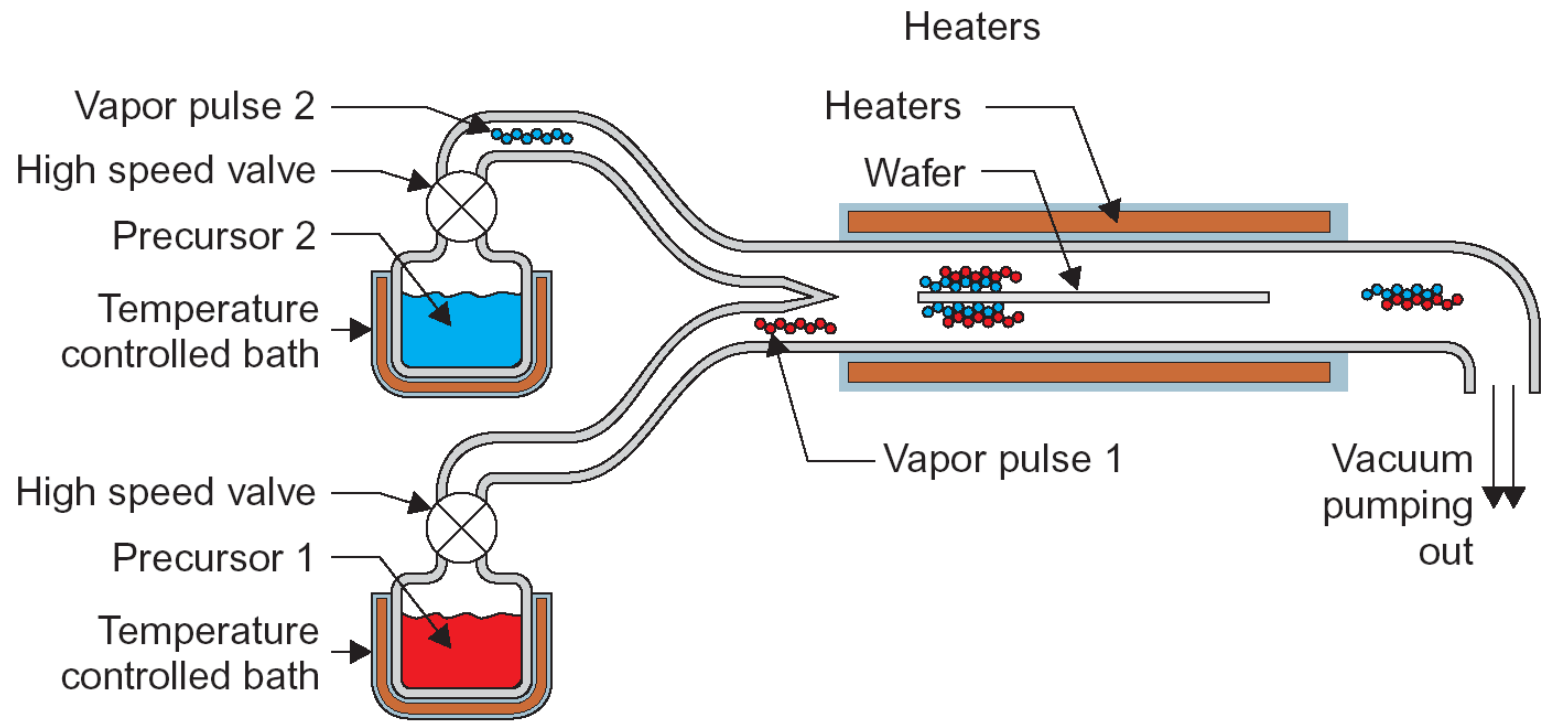
1.6X

< 0.01X

Atomic Layer Deposition (ALD)



- Zirconium (Zr)
- Chlorine (Cl)
- Oxygen (O)
- Hydrogen (H)



Step Coverage (Al₂O₃)

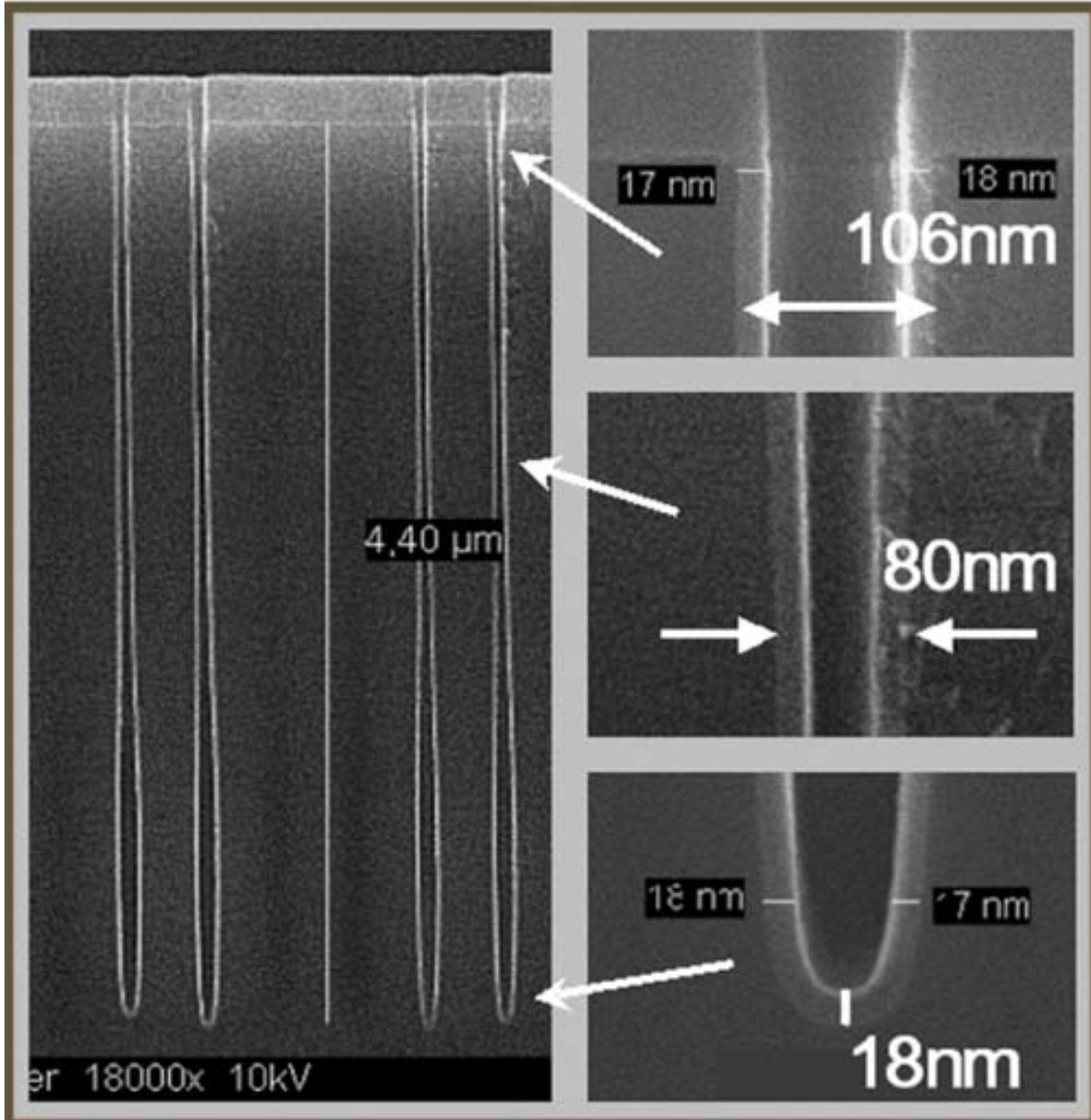
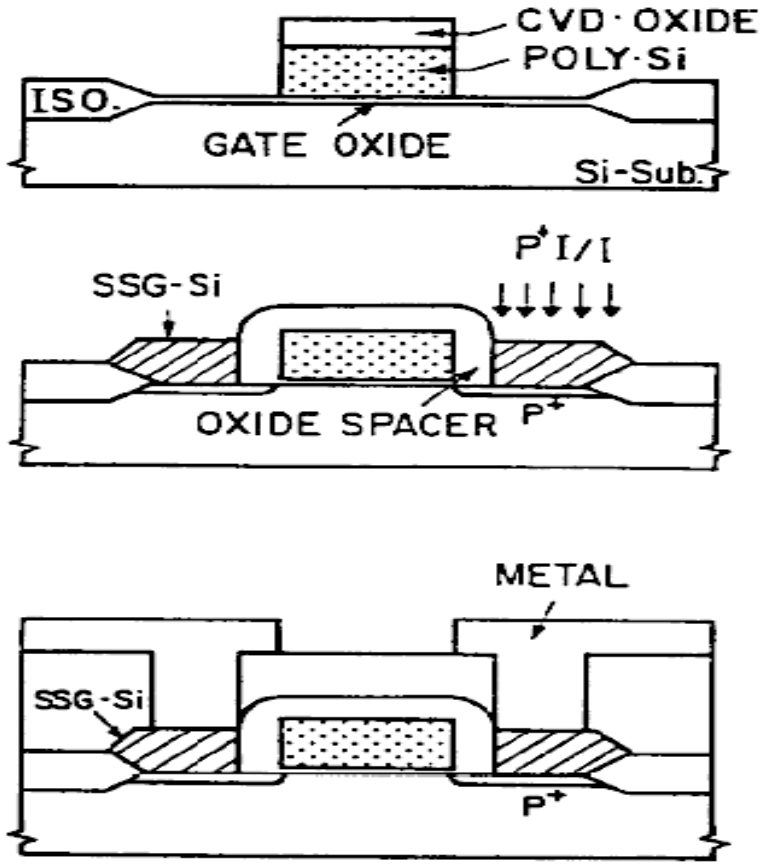
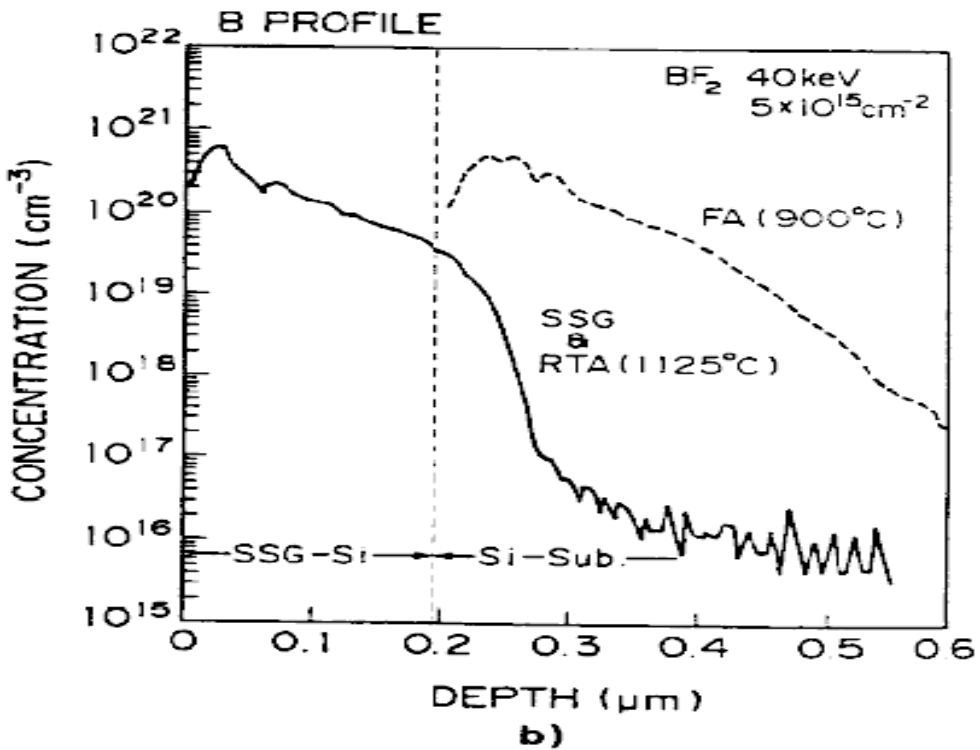


Figure 3. ALD features superb step coverage performance. The SEM images show close to 100% conformality for an 18nm thick Al₂O₃ film which was deposited by ALD into high aspect ratio trenches with a minimum lateral dimension of 80nm and a final aspect ratio of ~ 60.

Elevated Source/Drain



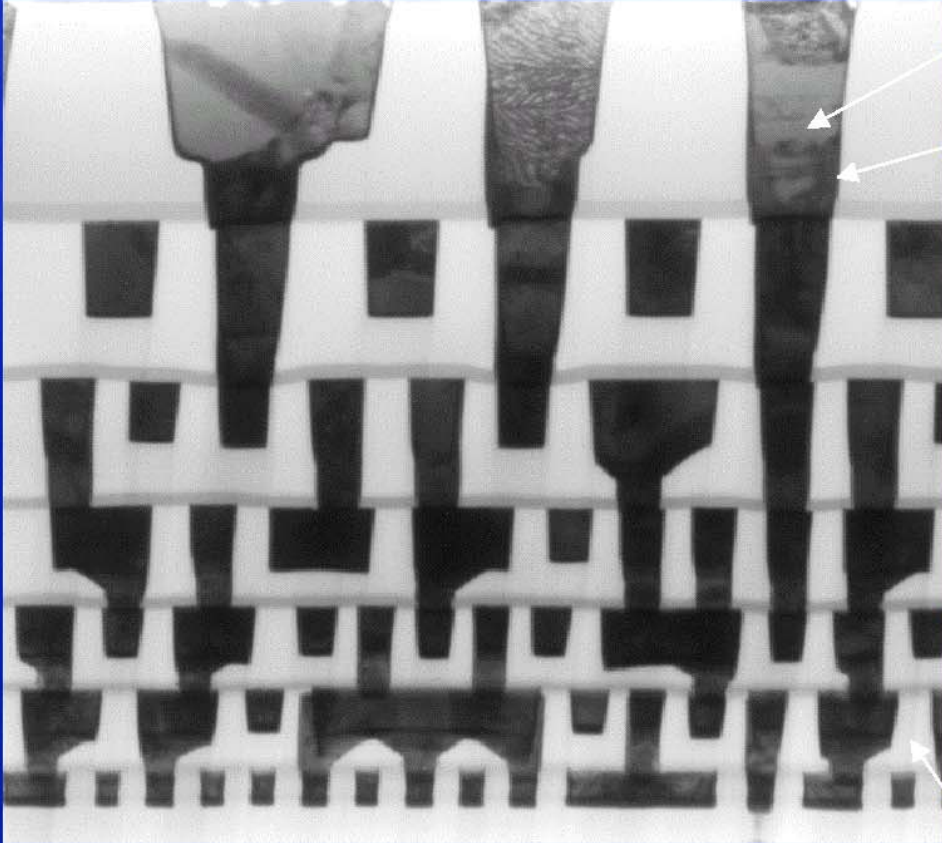
a)



b)

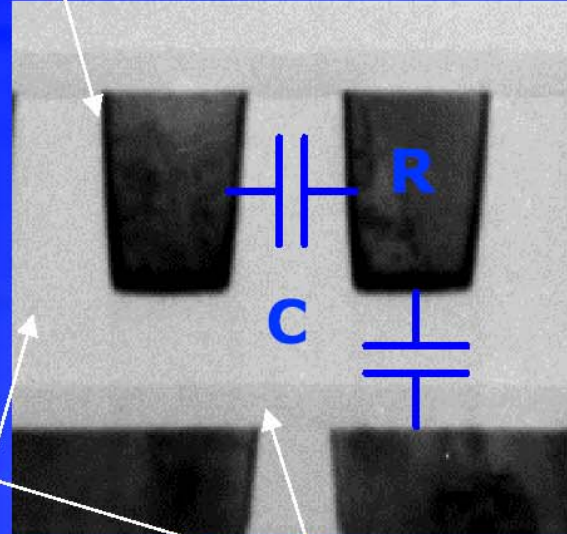
Fig. 6-41 Elevated source/drains. (a) Key process steps in forming elevated source/drains using SEG. (b) Comparison of the doping profile in source/drain regions of a PMOS device after a BF₂⁺ implant directly into the regions (and a conventional furnace anneal) versus a BF₂⁺ implant into the SEG regions and an RTP anneal.⁷⁹

90nm Interconnects



● copper interconnects

● thin barriers

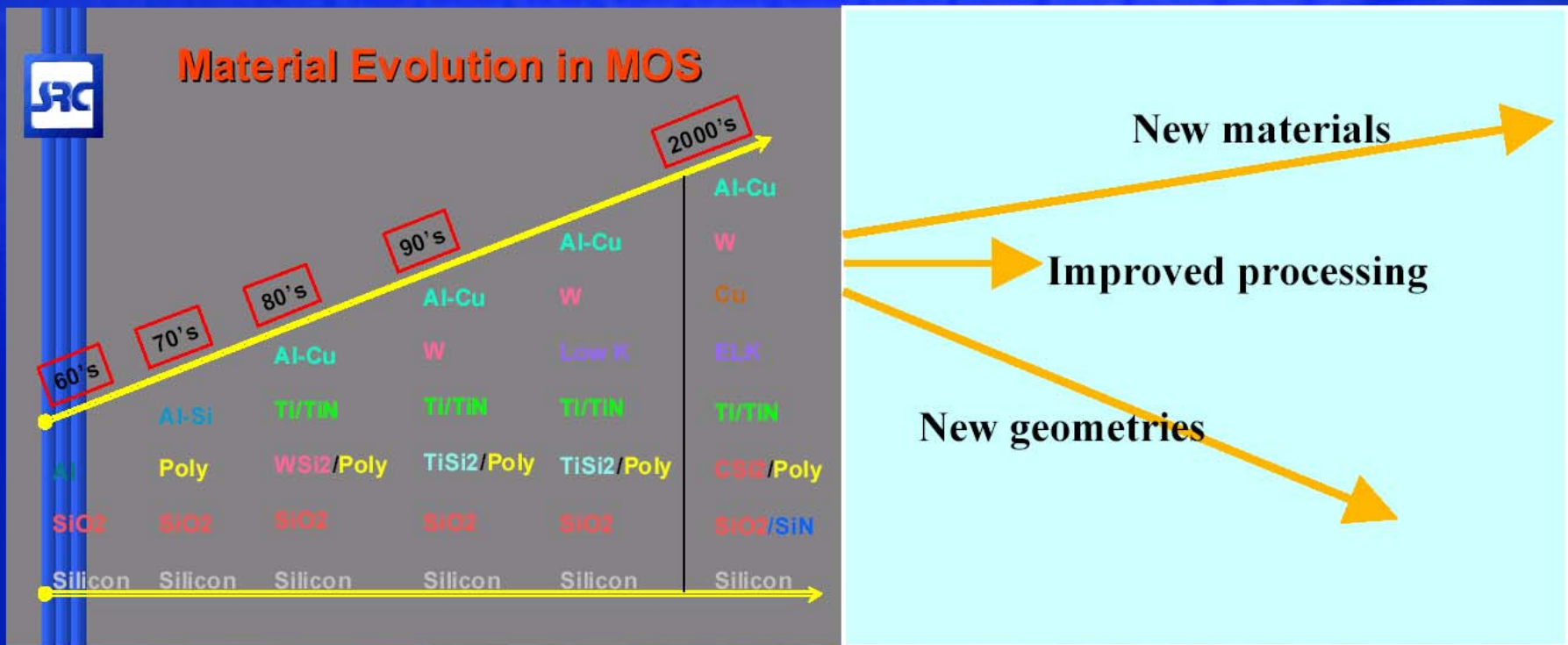


etch stop

● low K dielectrics

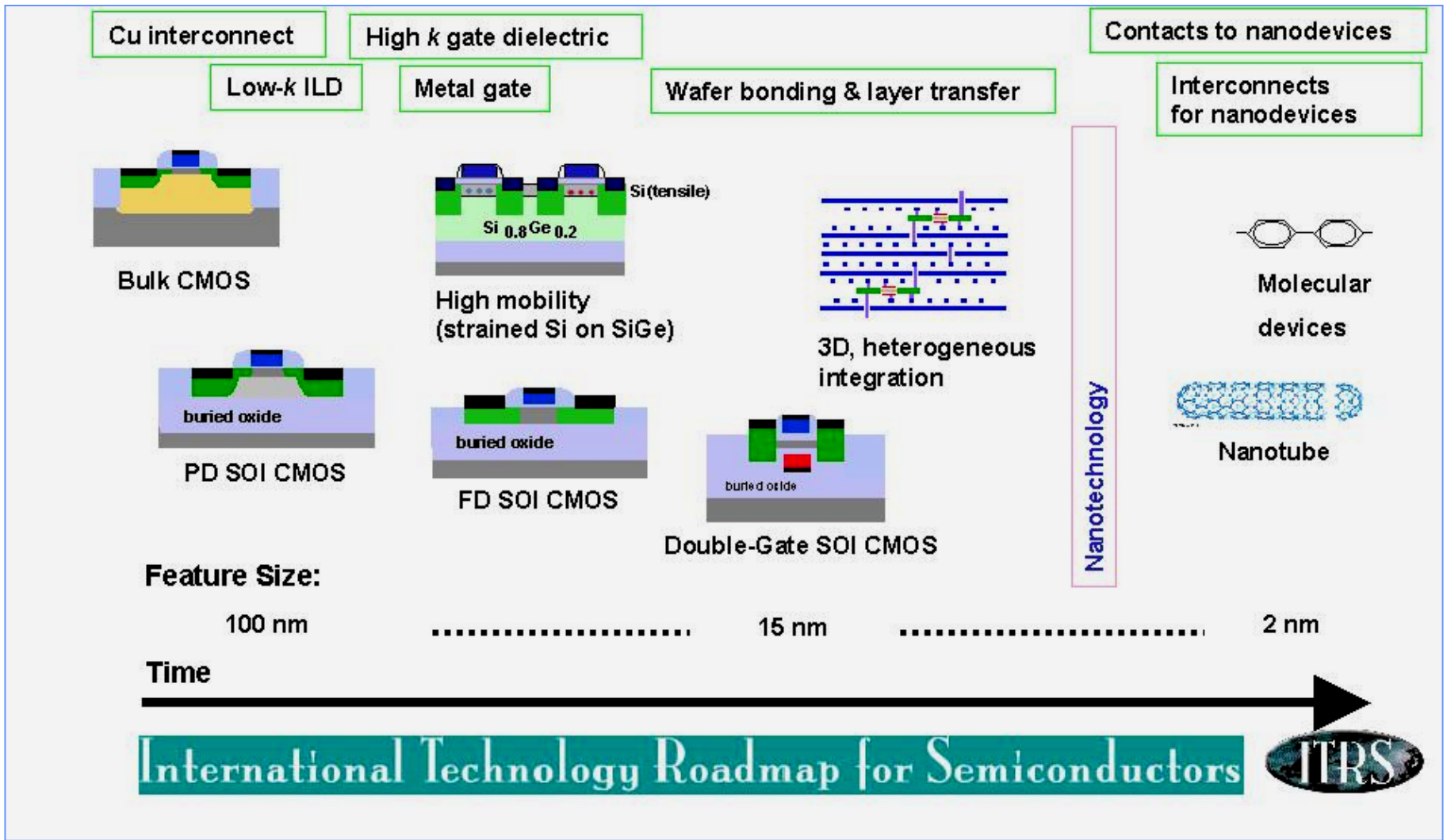


The ingredients of scaling



Scaling Will continue as long as

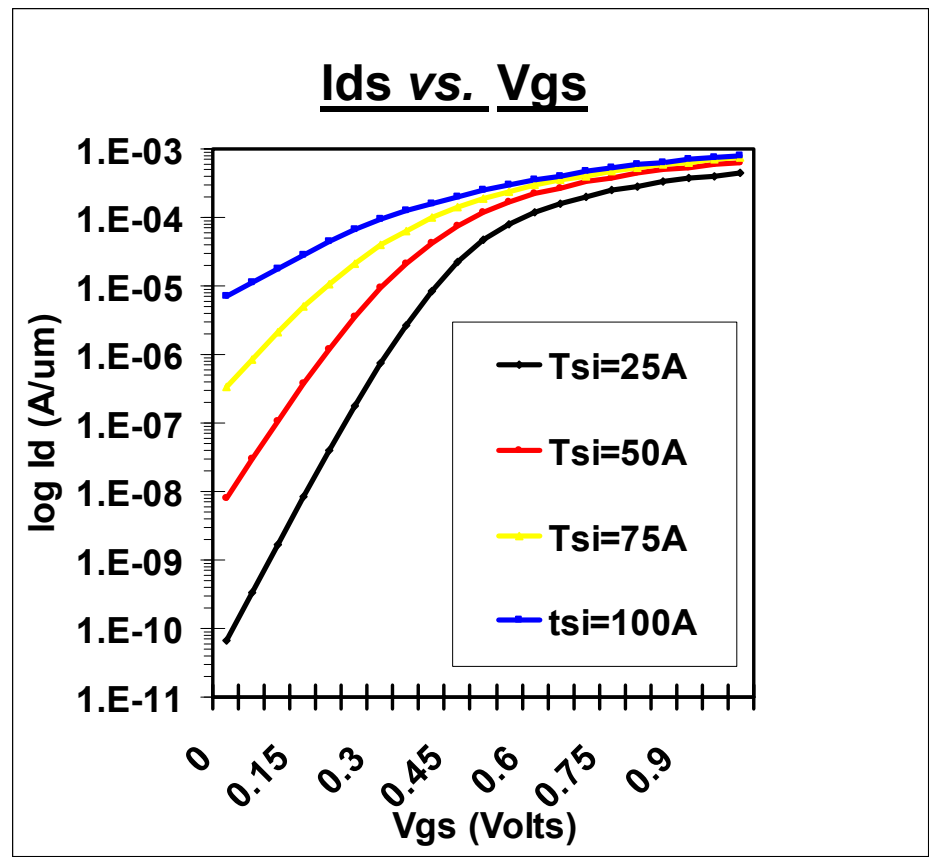
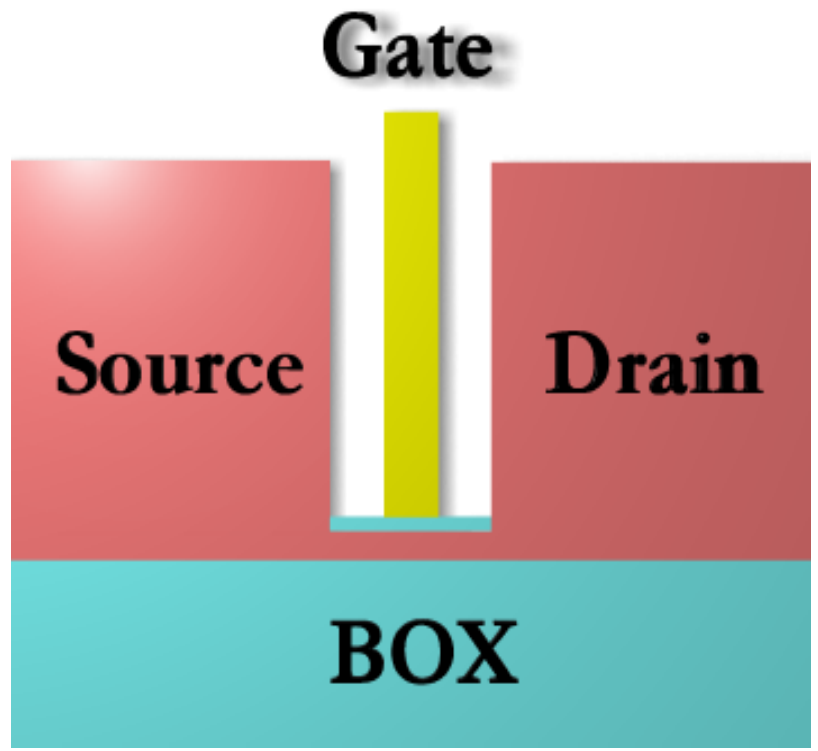
$(\delta \text{ cost}) / (\delta \text{ performance}) < \text{alternate technologies}$



Thin-Body MOSFET

- $T_{ox} = 2 \text{ nm}$
- $L_{gate} = 25 \text{ nm}$
- $V_{dd} = 1 \text{ V}$

- Thin body to control short-channel effects
- Elevated S/D to reduce R_{sd}



Dual Gate MOSFET

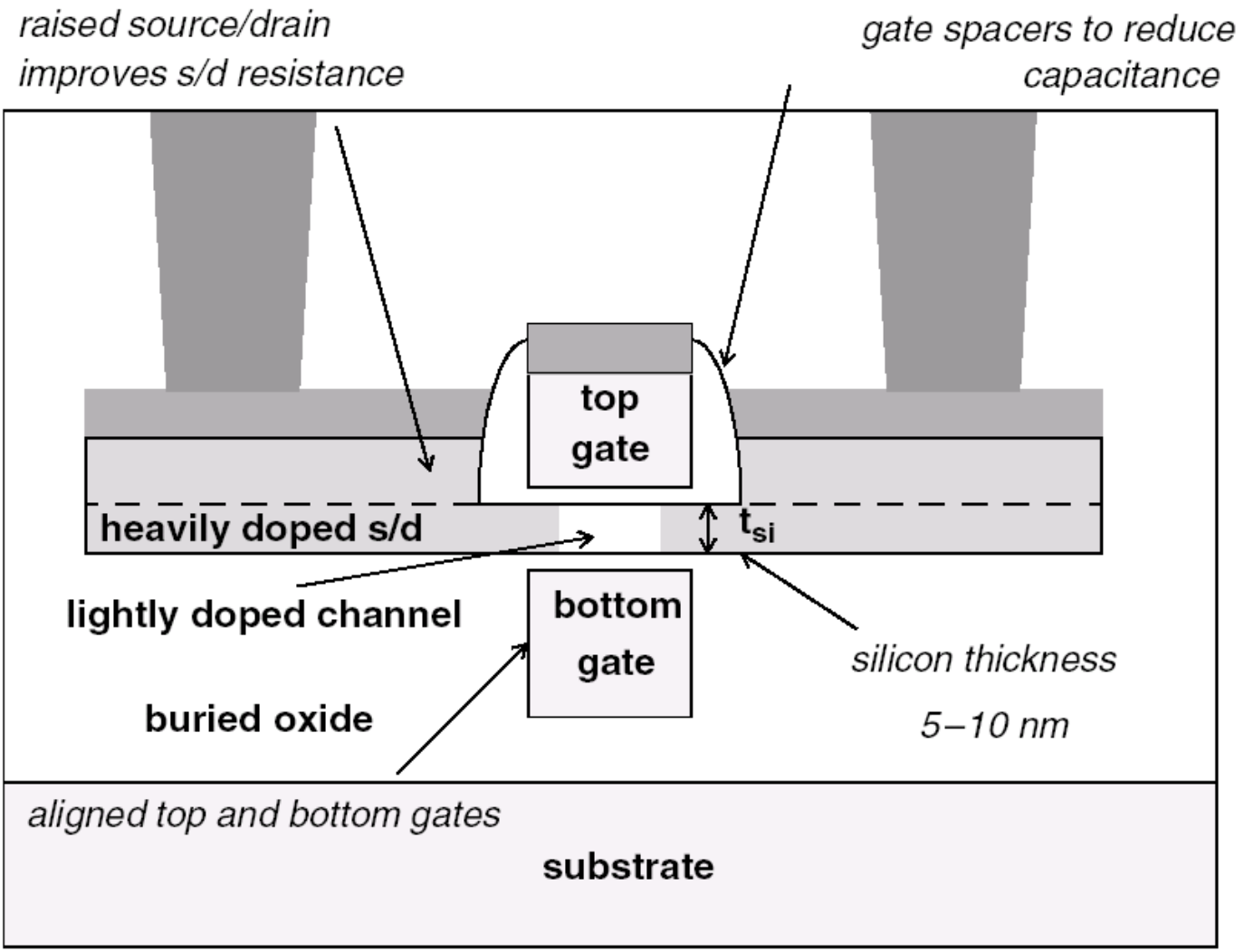
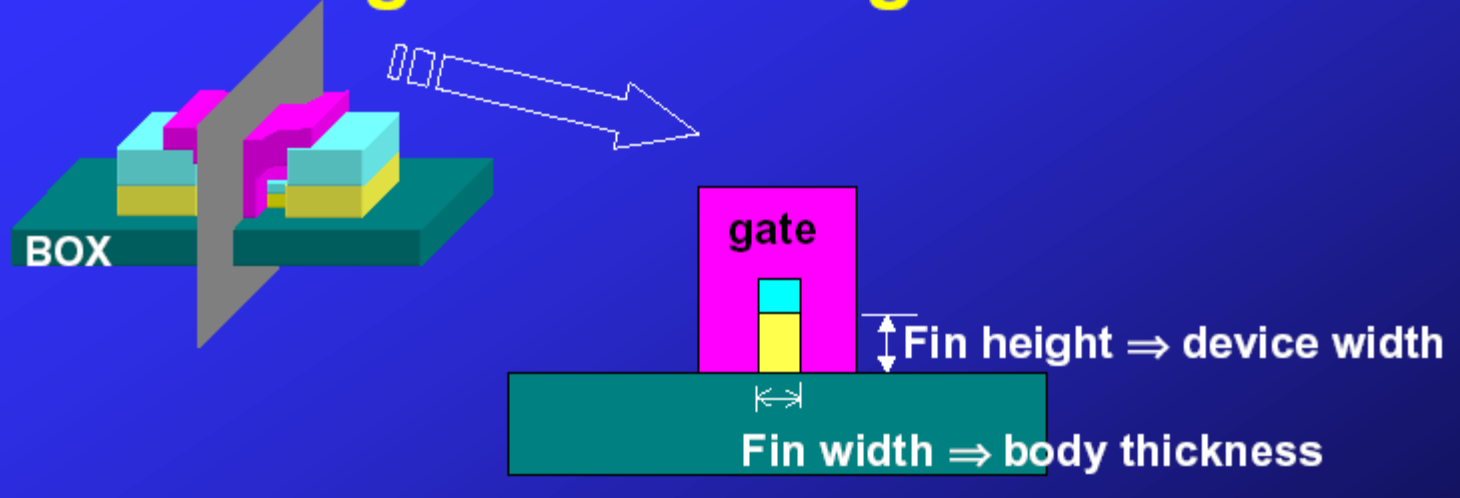


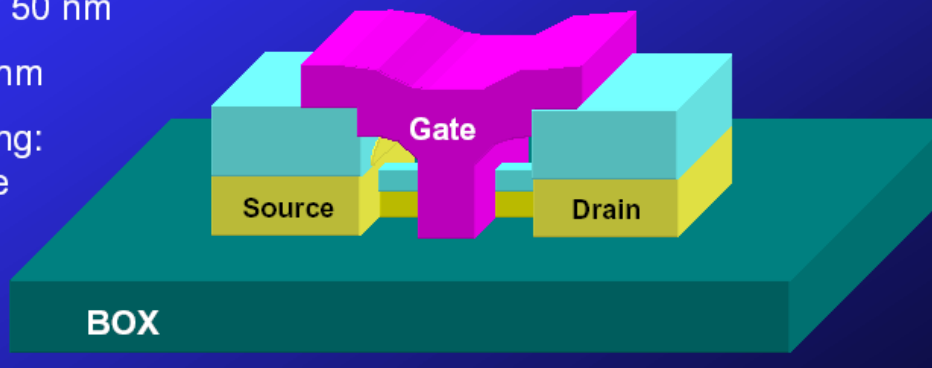
FIGURE 6.2 Generic features of double gate MOSFET device.

FinFET

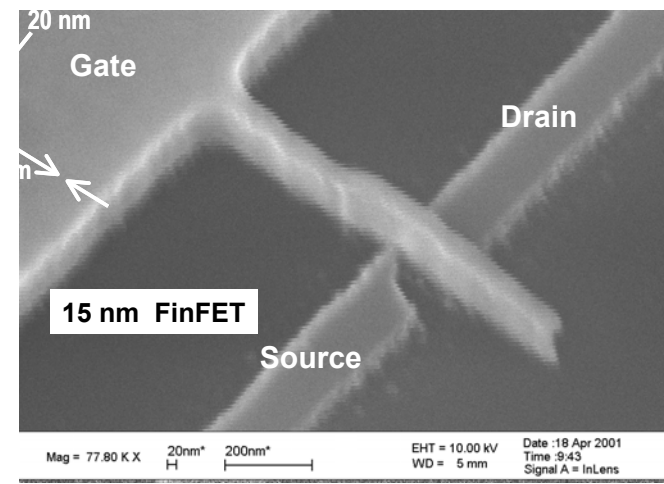
* self-aligned double-gate structure *



- $L_g \sim (10 \text{ nm} - 45 \text{ nm})$
- Fin width $\sim (<10 \text{ nm} - 120 \text{ nm})$
- Fin height: 50 nm
- $T_{ox} \sim 2.5 \text{ nm}$
- Body doping: 10^{16} n-type

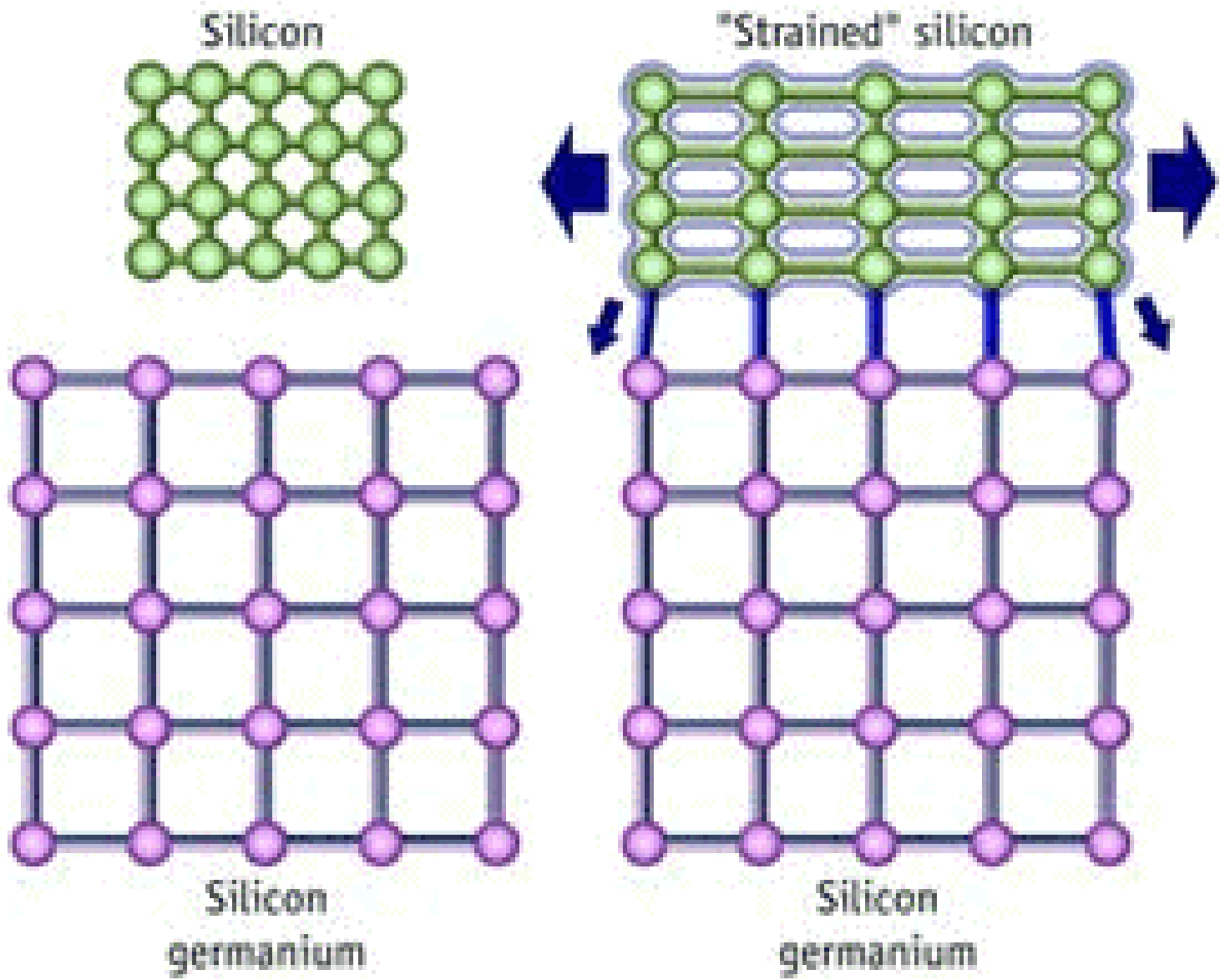


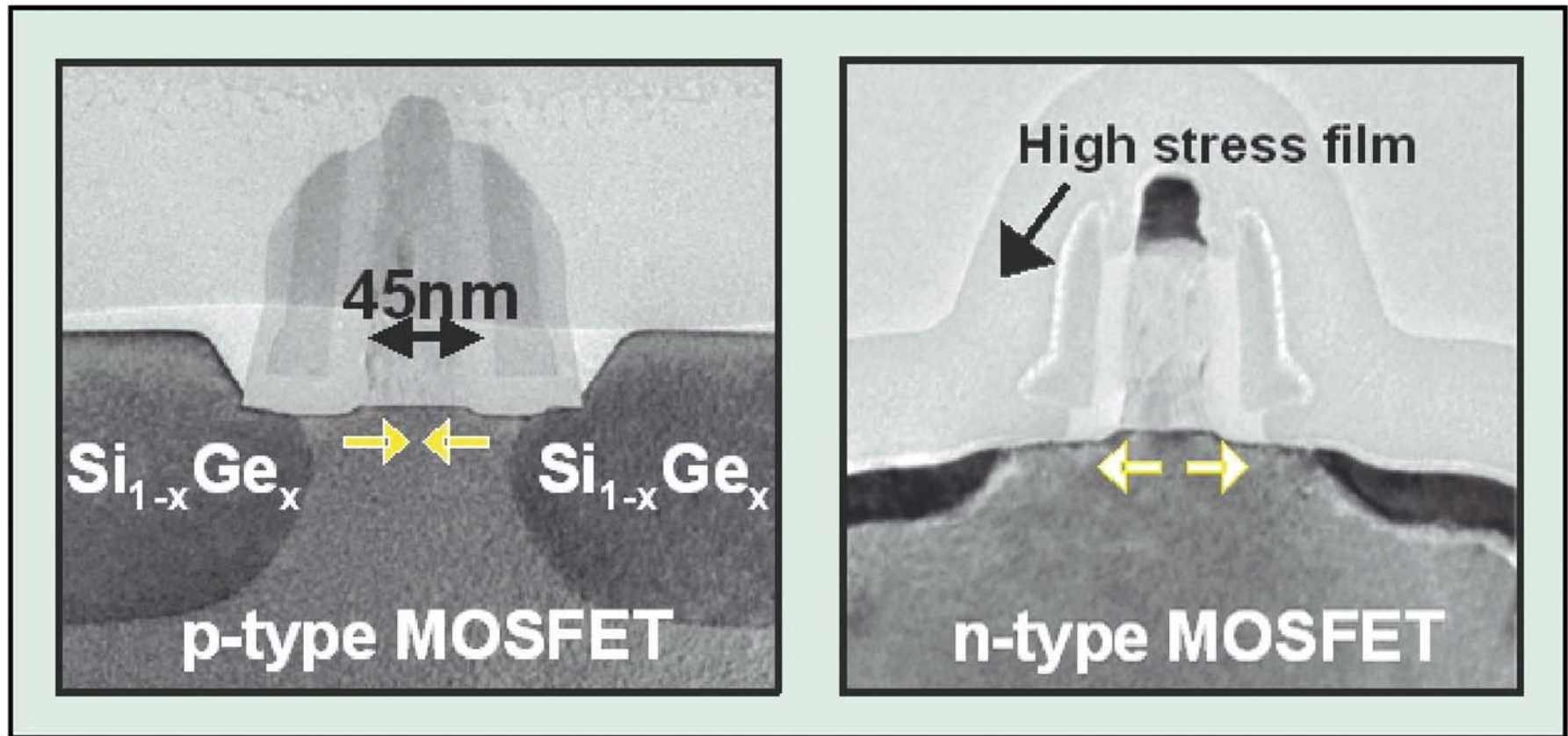
Scanning Electron Micrograph



Huang et al, IEDM, 1999

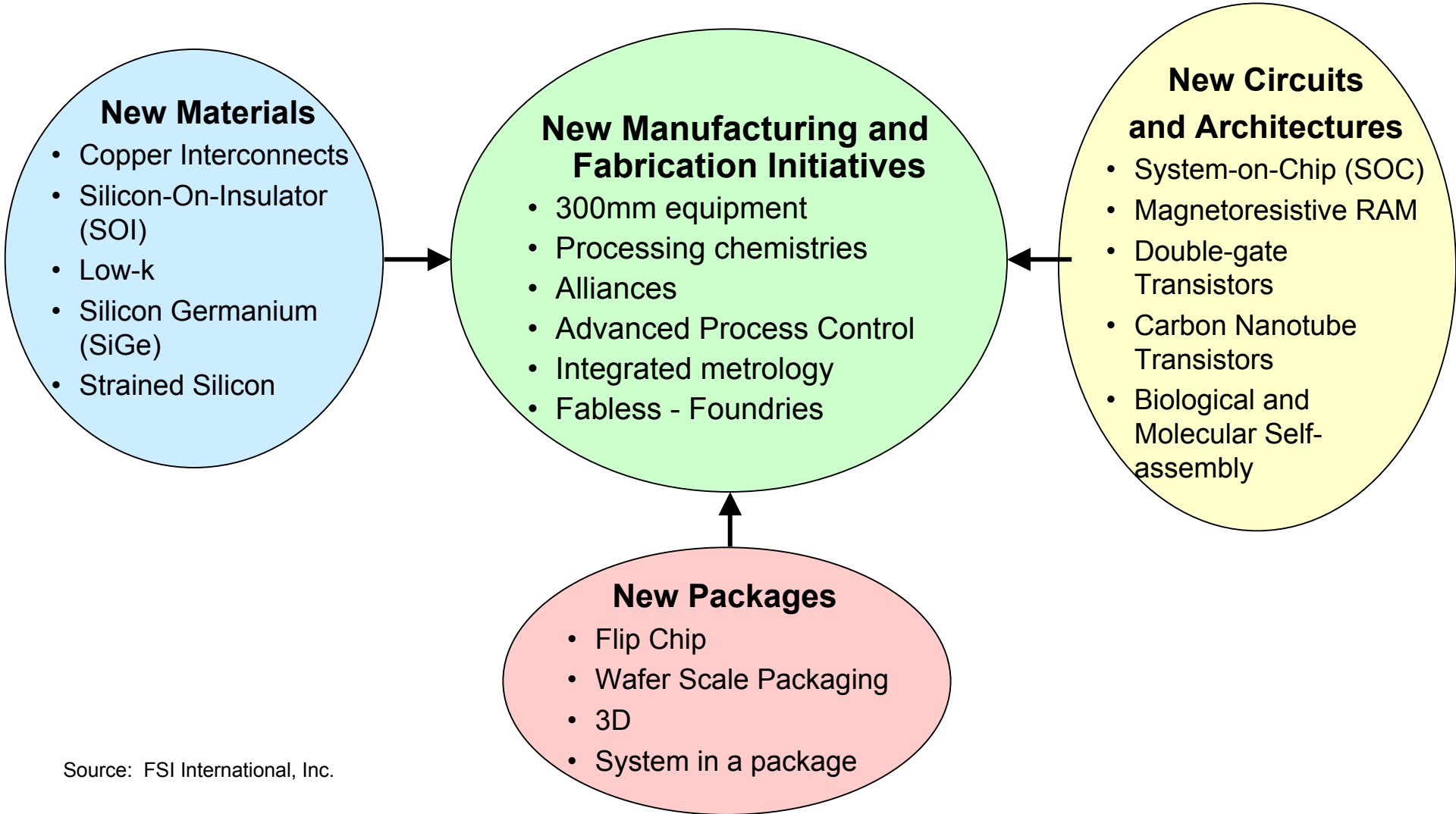
Strained Silicon





TEM micrographs of 45 nm p-type and n-type MOSFET. Image courtesy of ScottThompson, Intel.

New Technologies



Source: FSI International, Inc.

Environmental Impact of the Semiconductor Industry

Impact per square inch of Si

Output from the Fab		Input to the Fab	
Liquid Waste	75 Gal/in ²	Water	30 gal/in ²
Hazardous Waste	0.1 Kg/in ²	Electricity	10 KWhr/in ²
Toxic Releases	0.01 Kg/in ²	Chemicals	0.2 kg/in ²

Silicon Integrated Circuit Periodic Table

www.icknowledge.com

TI₀₃
k 20-30
BV 1.7x10⁷

Metal alloys
Melting temperature - °C
Resistivity - μΩ-cm

Silicides
Formation temperature - °C
Max temperature on silicon - °C
Resistivity - μΩ-cm
Barrier to N-type and P-type - silicon - eV

Al P
m 1.07
D 7x10⁻¹³

Type - P or N
Mismatch - radius divided by silicon radius
Diffusivity in silicon @1,000°C - cm²/s

Si
Eg 1.12
μn 1,450
μp 505

Energy gap - Eg eV@300K
Mobility - μ cm²/V-s@300K except Sn @100K

AN/ION
ArF 193
Ar 126

Use - see key
Excimer laser dimer and wavelength - nm

Al₂O₃
k 11.4/
13.2

Si₃N₄
k 7.5
BV 1x10⁷

SiO₂
k 3.9
B V 1x10⁷

Inert
Nitride
Dielectric constant
Breakdown voltage - V/cm

Metal Silicides

Al₂O₃ k 11.4/ 13.2 SiO₂ k 3.9 B V 1x10 ⁷	Al P m 1.07 D 7x10 ⁻¹³ Al OI Cu OI	Si Eg 1.12 μn 1,450 μp 505 Si₃N₄ k 7.5 BV 1x10 ⁷ SiO₂ k 3.9 B V 1x10 ⁷	AN/ION ArF 193 Ar 126 He ^{DI}
---	--	---	---

OX/DP
H
TL -253
V 113.6

MI
Be
Ea -0.45
Mg
ρ 4.46
Tm 650

MI
Na
μ 1x10⁻⁶

MI
K
μ 6x10⁻¹⁴

MI
Ca
Ea 0.55
Sr
ρ 13
Tm 777

MI
Y
Ea 1.15
Zr
ρ 50
Tm 1,526

MI
La
Ea 0.67
Hf
ρ 61
Tm 920

MI
Ti
Ea 1.45
ρ 42
Tm 1,668

MI
V
Ea -1.4
ρ 26
Tm 1,900

MI
Cr
Ea -1.3
ρ 12.9
Tm 1,875

MI
Mn
Ea -0.9
ρ 185
Tm 1,245

MCL OI
Fe
Ea -1.8
ρ 7.1
Tm 2,334

MI
Co
Ea 1.5
ρ 6.24
Tm 1,493

MI
Ni
Ea -2.1
ρ 6.84
Tm 1,453

MI
Cu
Ea -1.35
ρ 1.673
Tm 1,083

MI
Zn
Ea -0.95
ρ 1.59
Tm 961

MI
Ga
Eg 0.78
m 1.07
D 1x10⁻¹⁴

MI
Ge
Eg 0.94
μn 3,900
μp 1,800

MI
As
Ea -0.4
m 1.0
D 1x10⁻¹⁵

MI
Se
Ea -0.4
m 1.15
D 8x10⁻¹⁶

MI
Br
Ea -0.4
m 1.15
D 8x10⁻¹⁶

MI
Kr
KrF 248

MI
Rb
Ea 0.55
Sr
ρ 13
Tm 777

MI
Y
Ea 1.15
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KrF 248

MI
Cs
Ea 0.67
Ba
ρ 30
Tm 2,130

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MI
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D 8x10⁻¹⁶

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Tm 1,493

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Ni
Ea -2.1
ρ 6.84
Tm 1,453

MI
Cu
Ea -1.35
ρ 1.673
Tm 1,083

MI
Zn
Ea -0.95
ρ 1.59
Tm 961

MI
Ga
Eg 0.78
m 1.07
D 1x10⁻¹⁴

MI
Ge
Eg 0.94
μn 3,900
μp 1,800

MI
As
Ea -0.4
m 1.0
D 1x10⁻¹⁵

MI
Se
Ea -0.4
m 1.15
D 8x10⁻¹⁶

MI
Br
Ea -0.4
m 1.15
D 8x10⁻¹⁶

MI
Kr
KrF 248

MI
Rb
Ea 0.55
Sr
ρ 13
Tm 777

MI
Y
Ea 1.15
Zr
ρ 50
Tm 1,526

MI
La
Ea 0.67
Hf
ρ 61
Tm 920

MI
Ti
Ea 1.45
ρ 42
Tm 1,668

MI
V
Ea -1.4
ρ 26
Tm 1,900

MI
Cr
Ea -1.3
ρ 12.9
Tm 1,875

MI
Mn
Ea -0.9
ρ 185
Tm 1,245

MCL OI
Fe
Ea -1.8
ρ 7.1
Tm 2,334

MI
Co
Ea 1.5
ρ 6.24
Tm 1,493

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