

# **EE105**

## **Microelectronic Devices and Circuits: MOSFET**

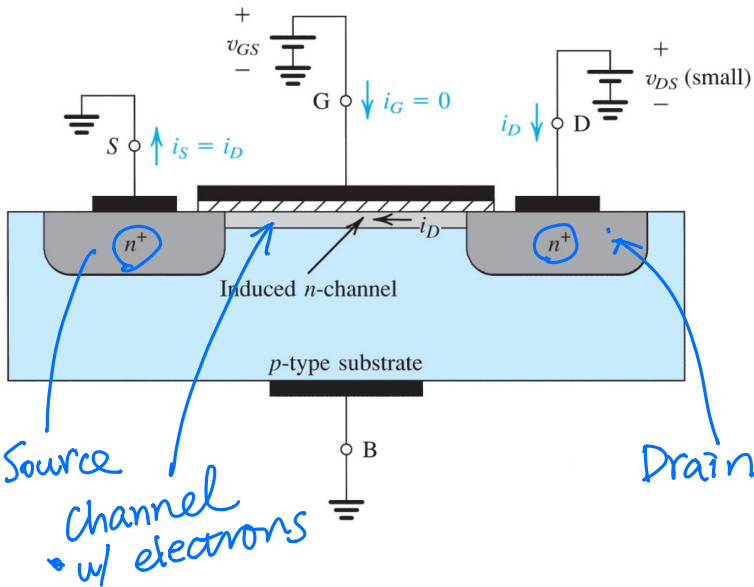
**Prof. Ming C. Wu**

**wu@eecs.berkeley.edu**

**511 Sutardja Dai Hall (SDH)**

# Creating a "Channel" for Current Flow

$$V_{GS} > V_t \Rightarrow \text{Inversion}$$



MOS is a capacitor across an insulator (oxide)  
 When a positive voltage is applied at Gate, electrons are induced under the gate.

At "threshold", sufficient number of electrons form a "channel" between Source and Drain, forming a conductive channel.

Total charge in the channel:

$$|Q| = C_{ox} \cdot WL \cdot (v_{GS} - V_t)$$

where  $C_{ox} = \frac{\epsilon_{ox}}{t_{ox}}$  is oxide capacitance

per unit area

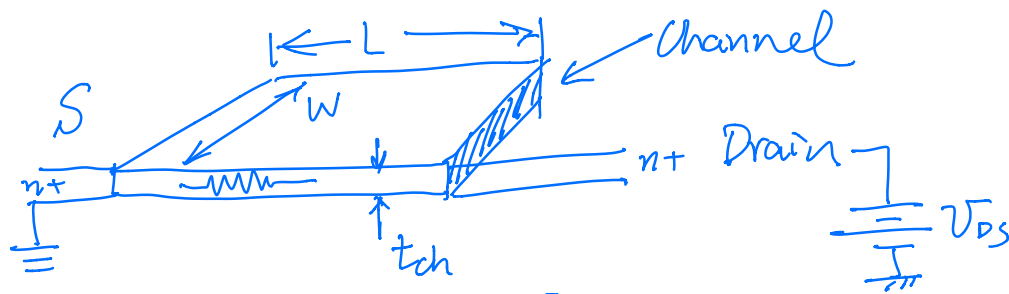
$$\epsilon_{ox} = 3.9\epsilon_0 = 3.9 \times 8.854 \times 10^{-12} \text{ F/m}$$

$W$  : gate width

$L$  : gate length

$V_t$  : Threshold voltage

$v_{GS} - V_t \equiv v_{OV}$  is called "Overdrive Voltage"



$$J = \sigma \cdot E = (nq \mu n) \frac{V_{DS}}{L}$$

$$\frac{V_{DS}}{L}$$

MOS Capacitance

$$n: \quad Q = CV = \frac{C_{ox} (V_{GS} - V_t) \cdot W \cdot L}{\text{per unit area} \quad \text{area}}$$

$$nq = \frac{Q}{W \cdot L \cdot t_{ox}}$$

$$I_{DS} = J \cdot t_{ox} \cdot W$$

$$= nq \mu n \frac{V_{DS}}{L} \cdot t_{ox} \cdot W$$

$$= \frac{C_{ox} (V_{GS} - V_t) \cdot W \cdot L}{W \cdot L \cdot t_{ox}} \cdot \mu n \cdot t_{ox} \cdot W \cdot \frac{V_{DS}}{L}$$

$$I_{DS} = \mu n C_{ox} (V_{GS} - V_t) \left(\frac{W}{L}\right) \cdot V_{DS}$$

Equivalent resistance

$$R_{ch} = \frac{V_{DS}}{I_{DS}} = \frac{1}{\mu n C_{ox} (V_{GS} - V_t) \left(\frac{W}{L}\right)}$$

gate bias      Geometry