EE 105 Discussion Welcome!

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Today

- Exercise 1: S&S 2.93
- Exercise 2
- Exercise 3
- Exercise 4

S&S 2.93

D **2.93 Derive the transfer function of the circuit in Fig. P2.93 (for an ideal op amp) and show that it can be written in the form

$$\frac{V_o}{V_i} = \frac{-R_2/R_1}{[1 + (\omega_1/j\omega)][1 + j(\omega/\omega_2)]}$$

where $\omega_1 = 1/C_1R_1$ and $\omega_2 = 1/C_2R_2$. Assuming that the circuit is designed such that $\omega_2 \gg \omega_1$, find approximate expressions for the transfer function in the following frequency regions:

$$\begin{array}{ccc}
\underline{(a) \ \omega \ll \omega_1} \\
\underline{(b) \ \omega_1 \ll \omega \ll \omega_2} \\
\underline{(c) \ \omega \gg \omega_2}
\end{array}$$

Use these approximations to sketch a Bode plot for the magnitude response. Observe that the circuit performs as an amplifier whose gain rolls off at the low-frequency end in the manner of a high-pass STC network, and at the high-frequency end in the manner of a low-pass STC network. Design the circuit to provide a gain of 40 dB in the "middle-frequency range," a low-frequency 3-dB point at 200 Hz, a high-frequency 3-dB point at 200 kHz, and an input resistance (at $\omega \gg \omega_1$) of 2 k Ω .

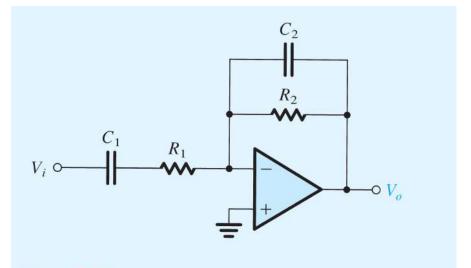


Figure P2.93

S&S 2.93

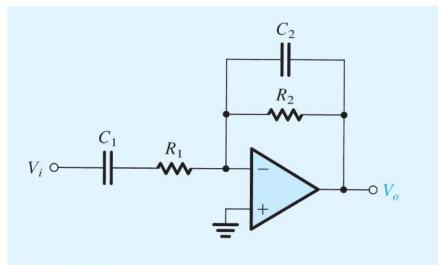
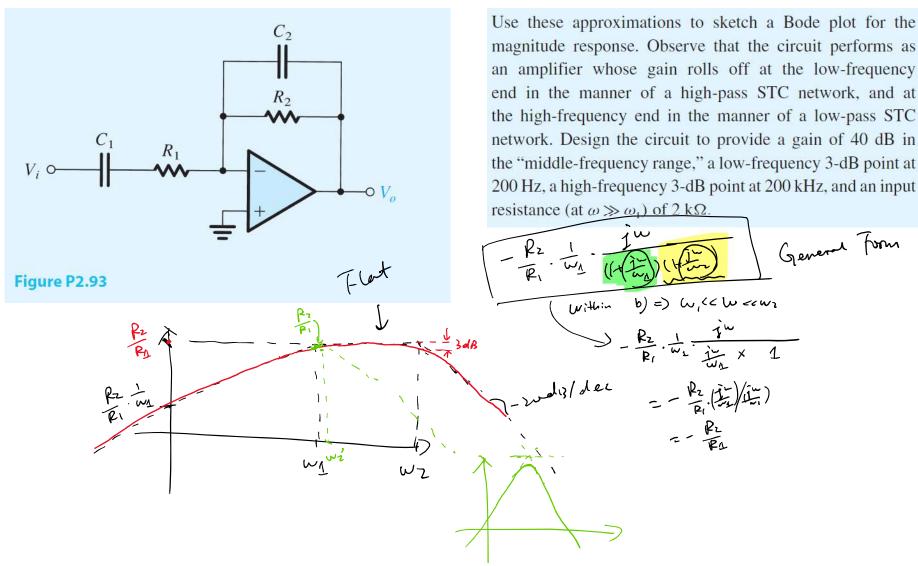


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$$\frac{V_o}{V_i} = \frac{-R_2/R_1}{[1+(\omega_1/j\omega)][1+j(\omega t\omega_2)]} - \frac{R_2}{R_1} \cdot \frac{1}{(+\frac{\omega_1}{j\omega_1} - \frac{R_2}{R_1} \cdot \frac{1}{(+\frac{\omega_1}{j\omega_2})})} - \frac{R_2}{R_1} \cdot \frac{1}{(+\frac{\omega_1}{j\omega_2} - \frac{1}{R_1} \cdot \frac{1}{(+\frac{\omega_1}{j\omega_2} - \frac{1}{(+\frac{$$

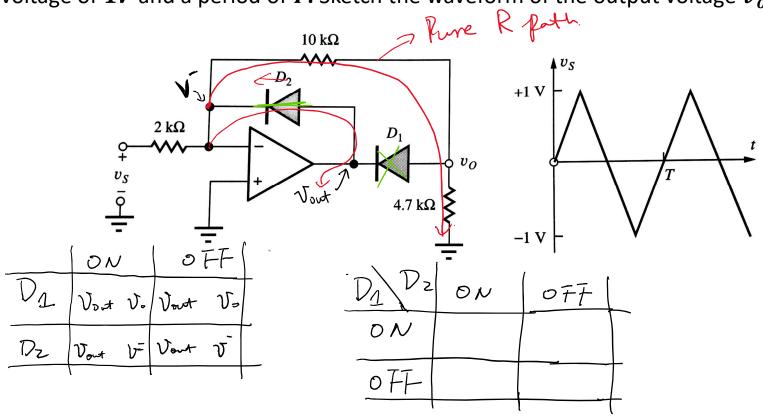
S&S 2.93

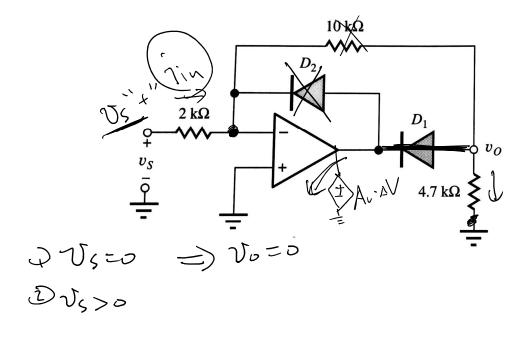


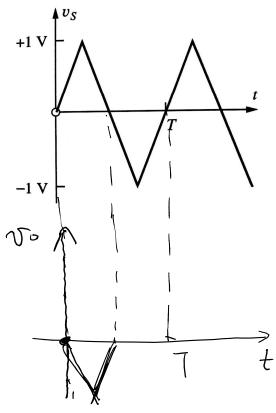


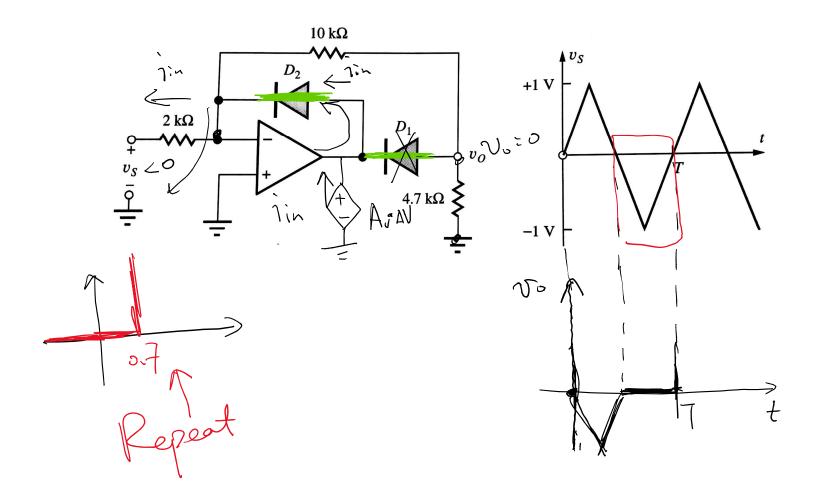
Assume ideal divole model

The waveform of the input voltage v_s to the circuit below is a triangle wave with a peak voltage of $\mathbf{1}V$ and a period of T. Sketch the waveform of the output voltage v_o .



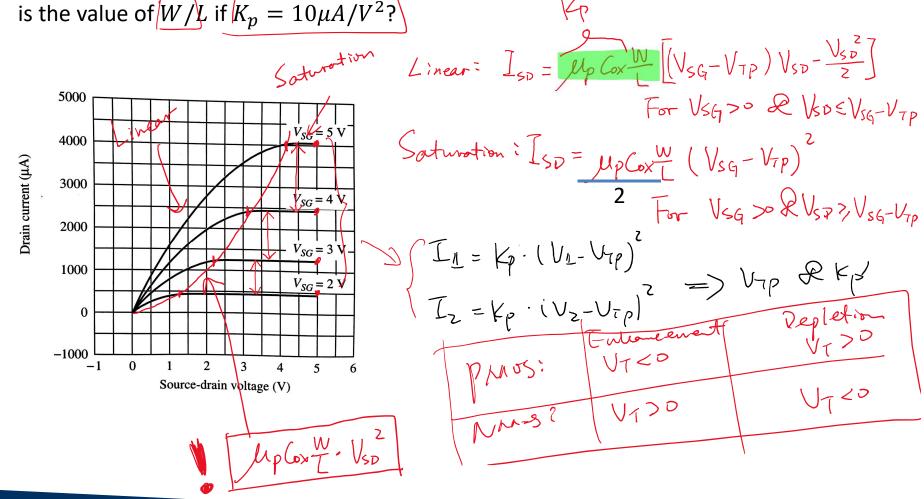






Kp = halos [Kp = prodoso ?

The output characteristics for a PMOS transistor are given. What are the values of K_p^i and V_{TP} for this transistor? Is this an enhancement-mode or depletion-mode transistor? What



The output characteristics for a PMOS transistor are given. What are the values of K_p and V_{TP} for this transistor? Is this an enhancement-mode or depletion-mode transistor? What is the value of W/L?

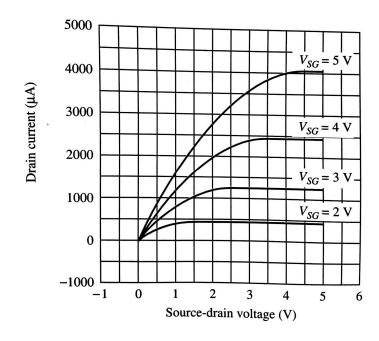
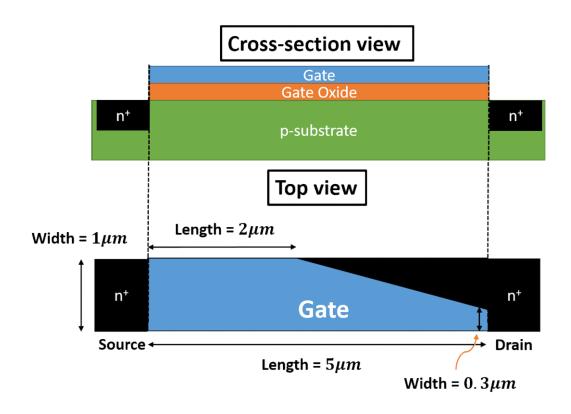
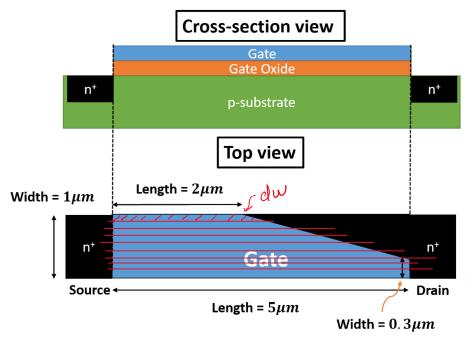


Figure below presents an NMOS transistor cross-section and its top-view. Derive the expression for the device current when operating in the saturation region as a function of μ_n , C_{ox} , V_{GS} , V_{DS} and V_{th} . Assume $\lambda=0$.





$$I_{tot} = \int_{0}^{V_{0}S} I(w)dw$$

$$I = \int_{0}^{V_{0}S} I(w)dw$$

(2) => Solving for current within each segment v/ W= du (2) => Summing current within all segments v/ W= du