EECS 105: Microelectronic Devices and Circuits

Fall 2001 Schedule R. T. Howe

WEEK	LECTURE TOPICS	READING	LAB
Week 1			no la
Phasors a	and the Frequency Domain		
	Lec. 1 (8/27) course overview: sinusoidal sources Lec. 2 (8/29) phasor representation; impedance Lec. 3 (8/31) transfer functions, time/frequency domain	Reader: OS excerpt Reader: OS excerpt HS 10.1	
Week 2		/ · · · · · · · · ·	no lab
	$L_{\rm r}$ have $D_{\rm res}$ $H_{\rm r}$ $i d_{\rm res}$ (0/2)	(read HP 4155 manual se	ctions)
	Lec. 4 (9/5) Bode plots: first-order transfer functions Lec. 5 (9/7) Second-order circuits: time domain	HS 10.1 Reader: OS excerpt	
Week 3			Frn
WEER J	Lec. 6 (9/10) Second-order circuits: frequency domain Lec. 7 (9/12) Higher-order transfer functions	Reader: OS excerpt HS 10.1	Цлр.
IC Two-	Ferminal Circuit Elements: Resistors and Capacitors		
	Lec. 8 (9/14) donors and acceptors in Si; drift current	HS 2.1-2.3	
Week 4			Exp.
	Lec. 9 (9/17) Ohm's law, IC resistors, sheet resistance Lec. 10 (9/19) metal-metal capacitor Lec. 11 (9/21) pn junction (equilibrium, reverse bias)	HS 2.6 HS 3.1 HS 3.4.1-2, 3.5	
W 1 5			Г
weeк 5	Lec. 12 (9/24) pn junction small-signal capacitor Lec. 13 (9/26) MOS small-signal capacitor	HS 3.6 HS 3.7, 3.9	Exp.
The MOS	SFET		
	Lec. 14 (9/28) MOSFET large-signal overview	HS 4.1	
Week			Ewr
W EEK D	Lec. 15 (10/1) MOSFET large-signal model Lec. 16 (10/3) MOSFET small-signal model	HS 4.3 HS 4.5	Exp.
pn Junct	ion Devices		
	Lec. 17 (10/5) pn junction diode under forward bias	HS 6.1-6.3	

WEEK	LECTURE TOPICS	READING	LAB
Wook 7			no lab
week /	Lec. 18 (10/8) circuit models, diffusion capacitance	HS 6.4-5	no iuo
	Midterm I. Wednesday, October 10, 6-7:30 pm (no lecture 10/10)		
	Lec. 19 (10/12) bipolar transistors in forward-active bias	HS 7.1-2	
Week 8			Lab Quiz, Exp. 5
	Lec. 20 (10/15) bipolar transistor small-signal model	HS 7.5	2
Integrate	d Single-Stage Amplifiers		
	Lec. 21 (10/17) Amplifier concept; 2-port parameters	HS 8.1,2	
	Lec. 22 (10/19) Common source amplifier: biasing	HS 8.3	
Week 9			Exp. 6
	Lec. 23 (10/22) Common source amp: two-port model	HS 8.5	-
	Lec. 24 (10/24) Common gate amplifier: biasing	HS 8.8	
	Lec. 25 (10/26) Common gate amp: two-port model	HS 8.8	
Week 10			Exp. 7
	Lec. 26 (10/29) Common drain amplifier: biasing	HS 8.9	
	Lec. 27 (10/31) Common drain amp: two-port model	HS 8.9	
	Lec. 28 (11/2) Bipolar amplifier stages	HS 8.1,2,9	
Week 11			Exp. 8
	Lec. 29 (11/5) Summary of single stage amplifiers	HS 8.1-9	Ť
Frequenc	cy Response of Single-Stage Amplifiers		
	Lec. 30 (11/7) Current amplifiers: transition frequency Lec. 31 (11/9) Voltage amplifiers: Miller approximation	HS 10.2-3 HS 10.4	
Wook 19			no lab
Week 12	Veteran's Day Holiday (no lecture)		10 140
	Midterm II. Wednesday, November 14, 6-7:30 pm (no lect	ture 11/14)	
	Lec. 32 (11/16) Voltage and current buffer f response	HS 10.5-6	
Integrate	d Multistage Amplifiers		
Week 13			Ern 0
,, ccn 15	Lec. 33 (11/19) Multistage amplifiers: two-port cascades	HS 9 1-2	Елр. 9
	Lec. 34 (11/21) Direct-coupled amps: DC level shifting	HS 9.3	
	Thanksgiving Holiday (11/22-23)		
Week 14	Let $35(11/26)$ Integrated current supplies	HS 0 /	Lab Quiz, Exp. 11
	Let. 55 (11/20) integrated current supplies	по 9.4	

WEEK	LECTURE TOPICS	READING	LAB			
Lec. 36 (11/28) Lec. 37 (11/30)	HS 9.5					
Week 15						
Multistage Amplifier Frequency Response						
Lec. 38 (12/3) C	pen-circuit time constants	HS 10.4.4				
Lec. 39 (12/5) N	fultistage amp frequency response	HS 10.7				
Lec. 40 (12/7) V	oltage amplifier example	HS 10.7.2				

Final Examination: Wednesday, December 12, 8:00 am – 11:00 am.

HS: R. T. Howe and C. G. Sodini, *Microelectronics: An Integrated Approach*, Prentice Hall, 1997.
Reader: EE 105 course reader, available at Copy Central. OS = W. G. Oldham and S. E. Schwarz, *Introduction to Electrical Engineering*, 1st Edition, HRW, 1984.