# inst.eecs.berkeley.edu/~cs61c CS61C : Machine Structures

#### Lecture #8 – MIPS Procedures

#### 2005-09-26

There is one handout today at the front and back of the room!

#### Lecturer PSOE, new dad Dan Garcia

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New Mexico St who? ⇒ No. 11 Cal runs over the

Aggies 41-13 are are now 4-0 for 1<sup>st</sup> time since 1996. Justin Forsett ran for 235 yds, Joe Ayoob rushed for 3 // TDs. Next Sat...Arizona @ home!





calbears.collegesports.com/sports/m-footbl/recaps/092405aaa.html CS61C L8 MIPS Procedures (1)

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#### Review

- A Decision allows us to decide what to execute at run-time rather than compile-time.
- C Decisions are made using conditional statements within if, while, do while, for.
- MIPS Decision making instructions are the conditional branches: beq and bne.
- In order to help the conditional branches make decisions concerning inequalities, we introduce a single instruction: "Set on Less Than"called slt, slti, sltu, sltiu
- Unsigned add/sub don't cause overflow
- New MIPS Instructions: beq, bne, j, sll, srl slt, slti, sltu, sltiu addu, addiu, subu



### **C** functions

```
main() {
    int i,j,k,m;
                           What information must
                           compiler/programmer
 i = mult(j,k); \ldots
 m = mult(i,i); \ldots
                           keep track of?
/* really dumb mult function */
int mult (int mcand, int mlier) {
 int product;
 product = 0;
 while (mlier > 0)
  product = product + mcand;
  mlier = mlier -1; \}
 return product;
                             What instructions can
                             accomplish this?
```



**Function Call Bookkeeping** 

 Registers play a major role in keeping track of information for function calls.

# Register conventions:

- Return address \$ra
- Arguments \$a0, \$a1, \$a2, \$a3
- Return value \$v0, \$v1
- •Local variables \$s0, \$s1, ..., \$s7

#### • The stack is also used; more later.



Instruction Support for Functions (1/6)
... sum(a,b);... /\* a,b:\$s0,\$s1 \*/
}
int sum(int x, int y) {
 return x+y;
}

address 1000 1004 1008 1012 1016 2000 2004 CS61C L8 MIPS Procedures (5)

In MIPS, all instructions are 4 bytes, and stored in memory just like data. So here we show the addresses of where the programs are stored. Instruction Support for Functions (2/6)
... sum(a,b);... /\* a,b:\$s0,\$s1 \*/
}
int sum(int x, int y) {
 return x+y;
}

M address
I 1000 add \$a0,\$s0,\$zero # x = a
1004 add \$a1,\$s1,\$zero # y = b
1008 addi \$ra,\$zero,1016 #\$ra=1016
S 1012 j sum #jump to sum
1016 ...
2000 sum: add \$v0,\$a0,\$a1
2004 jr \$ra # new instruction



Instruction Support for Functions (3/6)
... sum(a,b);... /\* a,b:\$s0,\$s1 \*/
}
int sum(int x, int y) {
 return x+y;
}

- Question: Why use jr here? Why not simply use j?
  - Answer: sum might be called by many functions, so we can't return to a fixed place. The calling proc to sum must be able to say "return here" somehow.

sum: add \$v0,\$a0,\$a1
jr \$ra # new instruction

## **Instruction Support for Functions (4/6)**

- Single instruction to jump and save return address: jump and link (jal)
- Before:
  - 1008 addi \$ra,\$zero,1016 #\$ra=1016 1012 j sum #goto sum
- After:
  - **1008** jal sum *# \$ra=1012,goto sum*
- Why have a jal? Make the common case fast: function calls are very common. Also, you don't have to know where the code is loaded into memory with jal.



**Instruction Support for Functions (5/6)** 

• Syntax for jal (jump and link) is same as for j (jump):

jal label

- jal should really be called laj for "link and jump":
  - Step 1 (link): Save address of next instruction into \$ra (Why next instruction? Why not current one?)
  - Step 2 (jump): Jump to the given label



**Instruction Support for Functions (6/6)** 

• Syntax for jr (jump register):

jr register

- Instead of providing a label to jump to, the jr instruction provides a register which contains an address to jump to.
- Only useful if we know exact address to jump to.
- Very useful for function calls:
  - jal stores return address in register (\$ra)
  - •jr \$ra jumps back to that address



#### **Nested Procedures (1/2)**

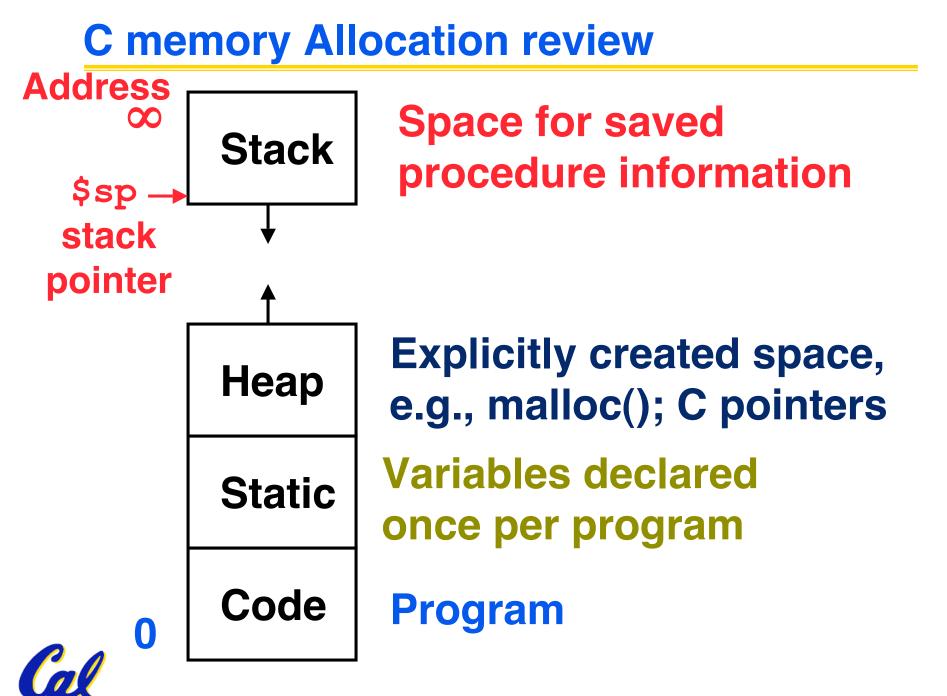
- int sumSquare(int x, int y) {
   return mult(x,x)+ y;
  }
- Something called sumSquare, now sumSquare is calling mult.
- So there's a value in \$ra that sumSquare wants to jump back to, but this will be overwritten by the call to mult.
- Need to save sumSquare return address before call to mult.



#### **Nested Procedures (2/2)**

- In general, may need to save some other info in addition to \$ra.
- When a C program is run, there are 3 important memory areas allocated:
  - Static: Variables declared once per program, cease to exist only after execution completes. E.g., C globals
  - Heap: Variables declared dynamically
  - Stack: Space to be used by procedure during execution; this is where we can save register values







Using the Stack (1/2)

- So we have a register \$sp which always points to the last used space in the stack.
- To use stack, we decrement this pointer by the amount of space we need and then fill it with info.
- So, how do we compile this?

int sumSquare(int x, int y) {
 return mult(x,x)+ y;
}



Using the Stack (2/2)

•Hand-compile int sumSquare(int x, int y) { return mult(x,x) + y; }

sumSquare: "addi \$sp,\$sp,-8 # space on stack sw \$ra, 4(\$sp) # save ret addr sw \$a1, 0(\$sp) # save y

> add \$a1,\$a0,\$zero # mult(x,x) # call mult jal mult

lw \$a1, 0(\$sp) # restore y add \$v0,\$v0,\$a1 # mult()+y lw \$ra, 4(\$sp) # get ret addr "pop" addi \$sp,\$sp,8 # restore stack jr \$ra mult: ...



#### **Steps for Making a Procedure Call**

- 1) Save necessary values onto stack.
- 2) Assign argument(s), if any.
- 3) jal call
- 4) Restore values from stack.



- Called with a jal instruction, returns with a jr \$ra
- Accepts up to 4 arguments in \$a0, \$a1, \$a2 and \$a3
- Return value is always in \$v0 (and if necessary in \$v1)
- Must follow register conventions (even in functions that only you will call)! So what are they?
  - We'll see these in a few slides...



#### **Basic Structure of a Function**

#### Prologue

```
entry label:
  addi $sp,$sp, -framesize
  sw $ra, framesize-4($sp) # save $ra
  save other regs if need be
                                           ra
Body · · · (call other functions...)
Epilogue
                                         memory
  restore other regs if need be
  lw $ra, framesize-4($sp) # restore $ra
  addi $sp,$sp, framesize
 jr $ra
```



#### Administrivia

- We have a midterm&review time & date
  - Review: Sun 2005-10-16 @ 2pm in 10 Evans
  - Midterm: Mon 2005-10-17, 5:30-8:30pm here!
  - DSP or Conflicts? Email Jeremy
- Dan's before-class graphics videos: www.siggraph.org/publications/video-review/SVR.html
- Project 1 due tonight @ 11:59pm
  - An easy HW3 follows, due Friday



Loop: addi 
$$\$s0, \$s0, -1$$
 # i = i - 1  
slti  $\$t0, \$s1, 2$  #  $\$t0 = (j < 2)$   
beq  $\$t0, \$0$ , Loop # goto Loop if  $\$t0 == 0$   
slt  $\$t0, \$s1, \$s0$  #  $\$t0 = (j < i)$   
bne  $\$t0, \$0$ , Loop # goto Loop if  $\$t0 != 0$   
( $\$s0=i, \$s1=j$ )  
What C code properly fills in  
the blank in loop below?  
do {i--;} while(\_);  
Cost C LEMIPS Procedures (20)

## **Register Conventions (1/4)**

- Calle<u>R</u>: the calling function
- Calle : the function being called
- When callee returns from executing, the caller needs to know which registers may have changed and which are guaranteed to be unchanged.
- Register Conventions: A set of generally accepted rules as to which registers will be unchanged after a procedure call (jal) and which may be changed.



**Register Conventions (2/4) - saved** 

- \$0: No Change. Always 0.
- \$s0-\$s7: Restore if you change. Very important, that's why they're called saved registers. If the <u>callee</u> changes these in any way, it must restore the original values before returning.
- \$sp: Restore if you change. The stack pointer must point to the same place before and after the jal call, or else the caller won't be able to restore values from the stack.



#### **Register Conventions (3/4) - volatile**

- \$ra: Can Change. The jal call itself will change this register. Caller needs to save on stack if nested call.
- \$v0-\$v1: Can Change. These will contain the new returned values.
- \$a0-\$a3: Can change. These are volatile argument registers. <u>Caller needs</u> to save if they'll need them after the call.
- \$t0-\$t9: Can change. That's why they're called temporary: any procedure may change them at any time. <u>Caller</u> needs to save if they'll need them afterwards.



## **Register Conventions (4/4)**

- What do these conventions mean?
  - If function R calls function E, then function R must save any temporary registers that it may be using onto the stack before making a jal call.
  - Function E must save any S (saved) registers it intends to use before garbling up their values
  - Remember: Calle<u>r</u>/calle<u>e</u> need to save only temporary/saved registers they are using, not all registers.



## Parents leaving for weekend analogy (1/5)

- Parents (main) leaving for weekend
- They (caller) give keys to the house to kid (callee) with the rules (calling conventions):
  - You can trash <u>the temporary room(s)</u>, like the den and basement (<u>registers</u>) if you want, we don't care about it
  - <u>BUT</u> you'd better leave the rooms (registers) that we want to save for the guests untouched. "these rooms better look the same when we return!"



## Parents leaving for weekend analogy (2/5)

- Kid now "owns" rooms (registers)
- Kid wants to use the saved rooms for a wild, wild party (computation)
- What does kid (callee) do?
  - Kid takes what was in these rooms and puts them in the garage (memory)
  - Kid throws the party, trashes everything (except garage, who goes there?)
  - Kid restores the rooms the parents wanted saved after the party by replacing the items from the garage (memory) back into those saved rooms



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## Parents leaving for weekend analogy (3/5)

- Same scenario, except <u>before</u> parents return and kid replaces saved rooms...
- Kid (callee) has left valuable stuff (data) all over.
  - Kid's friend (another callee) wants the house for a party when the <u>kid</u> is away
  - Kid knows that friend might trash the place destroying valuable stuff!
  - Kid remembers rule parents taught and now becomes the "heavy" (caller), instructing friend (callee) on good rules (conventions) of house.



## Parents leaving for weekend analogy (4/5)

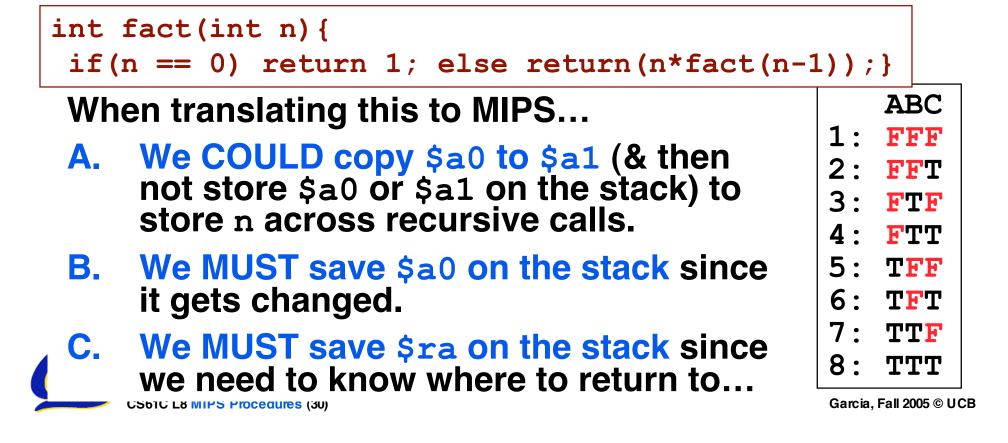
- If kid had data in temporary rooms (which were going to be trashed), there are three options:
  - Move items directly to garage (memory)
  - Move items to saved rooms whose contents have already been moved to the garage (memory)
  - Optimize lifestyle (code) so that the amount you've got to shlep stuff back and forth from garage (memory) is minimized



Parents leaving for weekend analogy (5/5)

- <u>Friend</u> now "owns" rooms (registers)
- Friend wants to use the saved rooms for a wild, wild party (computation)
- What does friend (callee) do?
  - Friend takes what was in these rooms and puts them in the garage (memory)
  - Friend throws the party, trashes everything (except garage)
  - Friend restores the rooms the kid wanted saved after the party by replacing the items from the garage (memory) back into those saved rooms





#### **Bonus Example: Compile This (1/5)**

```
main() {
 int i,j,k,m; /* i-m:$s0-$s3 */
 i = mult(j,k); \ldots
 m = mult(i,i); \ldots
}
int mult (int mcand, int mlier) {
 int product;
 product = 0;
 while (mlier > 0) {
  product += mcand;
  mlier -= 1; \}
 return product;
  CS61C L8 MIPS Procedures (31)
```

#### **Bonus Example: Compile This (2/5)**

start:

add \$a0, add \$a1, jal mult add \$s0,	\$s2,\$0	# #	arg0 = j arg1 = k call mult i = mult()
add \$a0, add \$a1, jal mult add \$s3,\$	\$s0,\$0	# #	arg0 = i arg1 = i call mult m = mult()
done			i-m:\$s0-\$s3 */
CS61C L8 MIPS Procedures (32)	<pre>i = mult(j,k m = mult(i,i</pre>	); );	Garcia, Fall 200

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## **Bonus Example: Compile This (3/5)**

#### • Notes:

- •main function ends with done, not jr \$ra, so there's no need to save \$ra onto stack
- all variables used in main function are saved registers, so there's no need to save these onto stack



#### **Bonus Example: Compile This (4/5)**

mul <sup>·</sup> Looj	add	\$t0,\$0,\$0	# prod=0
Fin:	slt beq add addi i	\$t1,\$0,Fin	<pre># mlr &gt; 0? # no=&gt;Fin # prod+=mc # mlr-=1 # goto Loop</pre>
	add jr	\$v0,\$t0,\$0 \$ra	# \$v0=prod # return
	_	<pre>int mult (int mca int product = 0; while (mlier &gt; 0) product += mcand mlier -= 1; } return product;</pre>	{
al		}	

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## **Bonus Example: Compile This (5/5)**

#### • Notes:

- no jal calls are made from mult and we don't use any saved registers, so we don't need to save anything onto stack
- temp registers are used for intermediate calculations (could have used s registers, but would have to save the caller's on the stack.)
- \$a1 is modified directly (instead of copying into a temp register) since we are free to change it
- result is put into \$v0 before returning (could also have modified \$v0 directly)



#### **MIPS Registers**

The constant 0	<b>\$0</b>	\$zero
<b>Reserved for Assembler</b>	\$1	\$at
Return Values	\$2-\$3	<b>\$v0-\$v1</b>
Arguments	\$4-\$7	\$a0-\$a3
Temporary	\$8-\$15	<b>\$t0-\$t7</b>
Saved	\$16-\$23	\$s0-\$s7
More Temporary	\$24-\$25	<b>\$t8-\$t9</b>
Used by Kernel	\$26-27	\$k0-\$k1
Global Pointer	\$28	\$gp
Stack Pointer	\$29	\$sp
Frame Pointer	\$30	\$fp
Return Address	\$31	\$ra

#### (From COD 3<sup>rd</sup> Ed. green insert) Use <u>names</u> for registers -- code is clearer!



- \$at: may be used by the assembler at any time; unsafe to use
- \$k0-\$k1: may be used by the OS at any time; unsafe to use
- \$gp, \$fp: don't worry about them
- Note: Feel free to read up on \$gp and \$fp in Appendix A, but you can write perfectly good MIPS code without them.



#### "And in Conclusion..."

- Functions called with jal, return with jr \$ra.
- The stack is your friend: Use it to save anything you need. Just be sure to leave it the way you found it.
- Instructions we know so far

Arithmetic: add, addi, sub, addu, addiu, subu Memory: lw, sw, lb, sb, lbu Decision: beq, bne, slt, slti, sltu, sltiu **Unconditional Branches (Jumps):** j, jal, jr

- Registers we know so far
  - All of them!



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