Inverter:

1-0-1   0-1-0
                V
                C

Models inputs to other
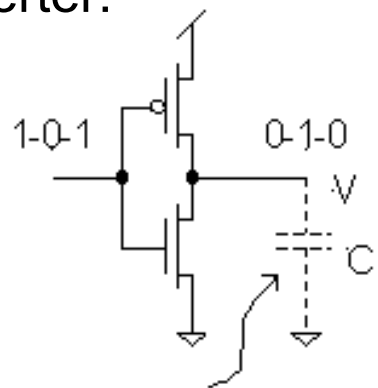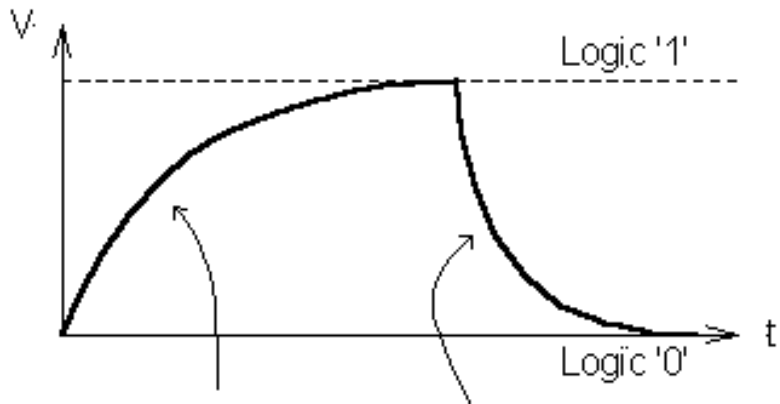gates & wire capacitance

V

Logic '1'

Logic '0'   t

rate dependent on
pullup strength & C

rate dependent on
pulldown strength & C