

**University of California at Berkeley**  
**College of Engineering**  
**Department of Electrical Engineering and Computer Science**

EECS150, Spring 2013

**Guidelines for Preliminary Project Proposal**  
**Due: Beginning of your lab session, week of 2/4.**

Please submit to us using glookup *in PDF format* a short (at least a few paragraphs, but less than 2 pages) description of your idea for a final project.

If you have identified a partner, make sure both names appear on the proposal. If you are still in the process of finding a partner, let us know where you are in the process (have talked to people, have a potential partner, etc.)

Remember that your project needs to use at least one FPGA development board from the lab. If you have a good reason for needing more than one board, that can be arranged. You are allowed to attach the board to additional components, if necessary, but we encourage you to keep things simple and take advantage of the available input and output devices already part of the board. Once we see the set of proposed projects, we'll let you know what "gateway" we will make available to you, but most likely this will include, among others, a simple processor core, Ethernet interface, external DRAM interface, and a video controller.

Your proposal should be a clear description of the the project you are interested working on. Be sure to answer the following questions:

- What is the function your final device will perform?
- Is this a device or problem domain with which you have some prior experience?
- Have you thought about how you would implement the function? If yes, provide some details.
- What is the form of the inputs and outputs?

Remember, that a picture can go a long way to help us understand what you have in mind. We don't expect that you have worked out all the details yet, but try to be as specific as you can at this point. We will later help you modify your plan in a way we see appropriate for the goals of the course and time and hardware resource constraints.

Finally, include a link to any online information that might help us understand the problem domain or technical approach you intend to take.