Negative edge of serial input – reception begins

$T_s = 1 / \text{(baud rate)}$
$T_s = 1 / (115200 \text{ s}^{-1})$
$T_s = 8.68 \mu s$
$T_{\text{glitch}} = 9 \mu s = 1.0368T_s$

$$T_s = \frac{1}{115200} \text{ s}$$
$$T_{\text{glitch}} = 9 \mu s$$

Byte received is 8'b11111111 or 0xff

T_s/2

Sample start bit (low – no error)

Sample stop bit (high – no error)