

University of California at Berkeley
College of Engineering
Department of Electrical Engineering and Computer Science

EECS 150
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Problem Set # 10/11

2)

Transfer Operation

Control Signals

R[0] \leftarrow R[0]

READR0, XFER, WRITER0

R[0] \leftarrow R[1]

READR1, XFER, WRITER0

R[0] \leftarrow R[2]

READR2, XFER, WRITER0

R[0] \leftarrow R[3]

READR3, XFER, WRITER0

R[1] \leftarrow R[0]

READR0, XFER, WRITER1

R[1] \leftarrow R[1]

READR1, XFER, WRITER1

R[1] \leftarrow R[2]

READR2, XFER, WRITER1

R[1] \leftarrow R[3]

READR3, XFER, WRITER1

R[2] \leftarrow R[0]

READR0, XFER, WRITER2

R[2] \leftarrow R[1]

READR1, XFER, WRITER2

R[2] \leftarrow R[2]

READR2, XFER, WRITER2

R[2] \leftarrow R[3]

READR3, XFER, WRITER2

R[3] \leftarrow R[0]

READR0, XFER, WRITER3

R[3] \leftarrow R[1]

READR1, XFER, WRITER3

R[3] \leftarrow R[2]

READR2, XFER, WRITER3

R[3] \leftarrow R[3]

READR3, XFER, WRITER3