UNIVERSITY OF CALIFORNIA
College of Engineering
Department of Electrical Engineering and Computer Sciences

EE290D Handout #33
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READING MATERIALS

1) S. J. Newman *et al.*, "Development of a 5.1-in. field-emission display," *SID International Symposium Digest of Technical Papers*, pp. 95-98, 1998.
2) C. J. Spindt *et al.*, "ThinCRT^{TM} flat-panel-display construction and operating characteristics," *SID International Symposium Digest of Technical Papers*, pp. 99-102, 1998.
3) D. G. Plug *et al.*, "100 nm aperture field emitter arrays for low voltage applications," *International Electron Devices Meeting Technical Digest*, pp. 855-858, 1998.
4) H. Takemura *et al.*, "Si field emitter array with 90-nm-diameter gate holes," *International Electron Devices Meeting Technical Digest*, pp. 859-862, 1998.