

University of California at Berkeley
College of Engineering
Department of Electrical Engineering and Computer Sciences

EECS150
Spring 2000

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Homework #8

This homework is due on **Friday April 7th by 11am**. Homework will be accepted in the EECS150 box on the door to room 218 Cory Hall. Late homework will be penalized by 50%. No late homework will be accepted after the solution is handed out.

All the following problems are from the **Katz book**:

<i>Conversion Between Forms</i>	3.1
<i>NAND-NAND, NOR-NOR</i>	3.2, 3.3
<i>Multilevel Network Mappings</i>	3.4
<i>Canonical Forms</i>	3.5(a-f)
<i>Multilevel Logic</i>	3.8, 3.9, 3.10
<i>Subtractor Logic</i>	5.9
<i>Remainder Function</i>	5.12