

**University of California at Berkeley**  
**College of Engineering**  
**Department of Electrical Engineering and Computer Sciences**

EECS150  
Spring 2000

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Homework #11

This homework is due on **Friday April 28<sup>th</sup> by 11am**. Homework will be accepted in the EECS150 box on the door to room 218 Cory Hall. Late homework will be penalized by 50%. No late homework will be accepted after the solution is handed out.

From the **Katz book**:

*Counter Design*

7.10, 7.11 *Implement the counters using D-type Flip-flops.*

*FSMs*

8.5, 8.11, 8.17