University of California at Berkeley
College of Engineering
Department of Electrical Engineering and Computer Sciences
Computer Science Division

CS 150 J. Wawrzynek
Fall 1996 Project Info.

Final Project Demonstration and Report

This document has information that you will need to know to receive credit for you project.

1 Demonstration

Project demonstrations will take place the last week of classes (12/4–12/6). For that week we will move the regularly scheduled Tusday lab section to Thursday, and Monday lab sections to Friday. All other lab sections will meet as scheduled. **You must demonstrate your project during your normally scheduled lab section.** There will be no make-up time for demonstrations except in the case of medical emergencies.

(11)	M	9–12	→	F	9–12
(12)	M	5–8	→	F	1–4
(13)	T	5–8	→	Th	1–4

Before your lab section next week you must sign up for a demonstration time slot. Sign-up sheets are posted in 204B Cory. At the beginning of your lab section during demo week, if we do not find you name on the list, we will assume that you have given up on the project and will receive a 0 grade.

During your demonstration we will ask you for a floppy disk with the schematic and "bits" file for your project. We will also ask to see a **hardcopy** of your schematics. Once we look at schematic hardcopy, you can keep them for use in your final report.

It is very important that you have something working at the demonstration. If you decide to make last minute changes before the demonstration, be certain to save away a copy of a working version of your project. That way, if something goes wrong at the last moment, you will still have something to show. And remember, a working project that does not meet all the specs is worth more that a project that meets all the specs but doesn't work!

Get to the lab well before you demo time to get things setup. If you are not ready when your time slot comes up, we will be forced to move on to the next group, and you will not get credit for a demonstration.

2 Final Report

A short final report is due in 204b before noon on Monday 12/9. The report can be **no more than** 10 pages of (double spaced) text and 10 pages of figures. Better reports mix the text and figures together.

Here is a suggested outline and page breakdown for your report. You do not need to strictly follow this outline, it is here just to give you an idea of what we will be looking for.

1. Project Functional Description and Design Requirements. Describe the high-level operation of the IR interface. ($\approx$ 1 page)
2. Theory of Operation. Describe how the hardware achieves the functional requirements. ($\approx$ 1 page)
3. High-level organization. How is your project broken down into pieces. Block diagram level-description. ($\approx$ 1 page)
4. Detailed Description of Sub-pieces. Describe how your circuits work. Concentrate here on novel or non-standard circuits. ($\approx$ 2 pages)
5. Status and Results. What is working and what is not? What is the measured transfer rate? ($\approx$ 1 page)
6. Division of Labor. **This section is mandatory.** How did you organize yourselves as a team. Exactly who did what? $\approx$ 0.5 pages)
7. Conclusions. What have you learned from this experience? How would you do it different next time? How could you improve the performance, or simplify the implementation. $\approx$ 2 pages)
8. Appendices. Any material too bulky for inclusion within the main text. For instance, more complete schematic diagrams or state transition diagrams. Include the check-off sheets here.

When we grade you report, we will grade for clarity, organization, and grammar. Make sure to proofread and correct mistakes before turning it in.

3 Project Grading

Design (Functionality and Organization):	80%
Checkpoints:	10%
Report:	10%
Extra (at most):	20%

No extra credit will be given to projects not meeting the minimal requirements.

4 Kit Turn-in

Your last job will be to disassemble you project and return the parts along with the rest of your lab kit. **You must remove the wire wrap wires and wrap-ids from your FPGA board before you turn it in.** Do this carefully!

Return you lab kit to 204B Cory on Monday 12/9 8–5 or Tuesday morning 12/10 8–noon.

5 Important Dates

	W–F	12/4–12/6	**Final Project Demonstration.**
	Th	12/5	**Final Lecture.**
8–5	Mon	12/9	**Turn-in Lab Kits.**
8–noon	Tue	12/10	same
	Mon	12/9	**Project Report Due.**
12:30–3:30	Th	12/12	**Final Exam.**
	Th	12/19	Final Grades Posted